# Implementation of an Advanced Modelica Library for Evaluation of Inverter Loss Modeling

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Abstract - This paper introduces a newly developed Power Electronic Inverter library in Modelica. The library has different levels of complexity to approximate the power losses in the power inverter. In addition, it provides two different modelling domains (ABC and DQ). The library utilizes a multi-level approach with increasing model complexity, each model is interchangeable in the system including power inverter, controller and machine. There are two attributes which are implemented into this library. First, the model calculates the electric behaviour of the power semiconductors, i.e. IGBTs, MOSFETs and diodes including the thermal effects. This can be fully parameterized based on the characteristic curves and parameters specified in the manufacturer's datasheet. Secondly, the analytical approach of power losses approximation based on the datasheet. It is interchangeable between two reference frames (ABC and DQ). The model is then validated with PLECS simulation to confirm the accuracy.

# Index Terms – Power Electronics; Power Losses; Power inverter; Modelica

### I. INTRODUCTION

Power Electronic Inverters (PEIs) transform Direct Current (DC) electrical power to Alternating Current (AC). They play a crucial role in the modern world and are an essential part of electrical power systems in modern transport applications. For example, they are commonly used to drive electrical machines within electric vehicles [1-2] and actuator systems within More Electric Aircraft [3].

There are a variety of methods for modelling the losses in PEIs. The first approach is a complete numerical simulation of the circuit with integrated or parallel running loss calculation [4-5]. A second approach is to calculate the electrical behaviour of the circuit analytically, i.e. voltage and current, for each power semiconductor device [6]. This approach can lead to complex and intensive mathematics based calculation. Simplified calculations are often used for quicker results. It is not possible to define which method produces the best model, it is necessary to determine the optimal balance between system simulation time and model fidelity for each task/application.

In this paper, the presented technique provides the complete analytical calculation of the power semiconductor losses in the PEI within Modelica. The aim is to predict the losses according to the modelling environment i.e. circuit parameters and operating points. This is done in both ABC and DQ domains which will significantly reduce the simulation time. The DQ domain is very useful especially in the machine system as it does not requires the Clarke transformation and Park transformation. By utilizing the DQ rotating reference frame, time varying signals can be represented as constant values allowing larger simulation step size, lower computational demand and hence faster simulations times. Furthermore, the presented techniques also allows the user to parameterize the power semiconductors and interchange between various models of the PEI with different levels of complexity [7]. The developed Modelica library will be presented along with the details and equations. Simulation results will be shown and validated against established market software (PLECS simulation). The main benefit of Modelica software compared to other software such as PLECS and OrCAD capture is the multidisciplinary domains which allow the user to combine mechanical, electrical, thermodynamic, hydraulic, pneumatic, thermal and control systems into a single model.

The structure of this library is classified into switching ABC, non-switching ABC, and non-switching DQ models. The switching ABC model provides the most similar waveform to the practical PEI. It uses the IGBT/MOSFET device in the switching which shows the static and behavior characteristics of the devices. However, using this model will result in high simulation time and processing demand. This is extremely impractical for large, complex system simulation. The non-switching ABC models and non-switching DQ models do not show the static and behavior characteristics of the devices, only the PEI losses are calculated based on the load voltage and current. This minimizes the total simulation time.

#### II. SWITCHING ABC MODEL

# A. IGBT

The IGBT model includes both the conduction losses and switching losses. The characteristics of IGBT depends on the type of technology, two different structures are available; NPT (non-punch through) and PT (punch through) IGBTs. Compared to the MOSFET structure, the NPT IGBT has an additional p+ doped layer between the emitter and collector which means that the forward characteristics of IGBT does not behave like a resistance but acts like a pn-junction. Hence, the static model is slightly different from MOSFET model detailed below. The behavior characteristics are modelled as ideal turnon and turn-off energy losses. In addition, the power losses of the body diode are also included.

The power losses of the IGBT device are based on the information provided by manufacturers' datasheet such as output characteristics ( $V_{CE} = f(I_C)$ ) and switching losses characteristics ( $E_{on}$ ,  $E_{off} = F(I_{CE}, T)$ ).

To achieve an accurate result, the following parameters need to be set as shown in Figure 1. These parameters can be found in the manufacturers' datasheet.



Figure 1. Parameters of IGBT model.

The average conduction loss and switching loss of the IGBT can be calculated by;

$$P_{avg.cond,IGBT} = \frac{1}{T} \int_0^T \left( V_{ce}(t) * I_{ce}(t) \right) dt$$
$$P_{sw,IGBT} = \left( E_{on} + E_{off} \right) * f_{sw}$$
(1)

It should be noted that the switching power loss needs to be normalized with the conditions provided for any application with the nominal values of datasheet. Hence, the switching loss can be rewritten as;

$$P_{sw,IGBT} = \frac{(E_{on} + E_{off}) * f_{sw} * I_{peak} * V_{DC}}{I_{nom} * V_{nom}}$$
(2)



The average conduction loss and switching loss of the body diode can be found as;

$$P_{avg.cond,Diode} = \frac{1}{T} \int_0^T \left( V_f(t) * I_f(t) \right) dt$$

$$P_{sw,Diode} = (E_{rec}) * f_{sw}$$
(3)

Similarly, the switching loss needs to be normalized with the nominal values of datasheet.

$$P_{sw,Diode} = \frac{(E_{rec})*f_{sw}*I_{peak}*V_{DC}}{I_{nom}*V_{nom}}$$
(4)

The final diagram of switching IGBT model is illustrated in Figure 3.



Figure 3. A diagram of inverter model for switching ABC model (SwitchingIGBT).

## B. MOSFET

The physical structure of a MOSFET is different from an IGBT as mentioned earlier. The operation of the MOSFET is also slightly different as it is able to operate both in forward and reverse mode. The conduction losses of the MOSFET can be modelled as the on-resistance whereas the switching losses are modelled as an ideal turn-on and turn-off energy losses similar to IGBT model. Furthermore, the model also includes the power losses of the body diode.

The power losses of the MOSFET device are based on the information provided by manufacturers' datasheet such as output characteristics ( $V_{DS} = f(I_D)$ ) and switching losses characteristics ( $E_{on}$ ,  $E_{off} = F(I_{DS}, T)$ ).

The average conduction loss and switching loss of the MOSFET can be calculated by;

$$P_{avg.cond,MOSFET} = I^2_{DS,RMS} * R_{DS,on}$$
(5)

$$P_{sw,MOSFET} = (E_{on} + E_{off}) * f_{sw}$$
(6)

It should be noted that the switching power loss needs to be normalized with the conditions provided for any application with the nominal values of datasheet. Hence, the switching loss can be rewritten as;

$$P_{sw,MOSFET} = \frac{(E_{on} + E_{off}) * f_{sw} * I_{peak} * V_{DC}}{I_{nom} * V_{nom}}$$
(7)

The parameters required for the MOSFET model are shown in Figure 4. A diagram of MOSFET device is shown in Figure 5.



Figure 4. Parameters of MOSFET model.



Figure 5. A model of MOSFET device.

## III. NON-SWITCHING ABC MODEL

In this model, an analytical approach is implemented to calculate the power losses for the PEI. The benefit of this model is the low simulation time (CPU time) which is a key desired feature in a closed-loop system simulation. The classic PWM frequency is in the range of 500Hz to 50Kz which can be extremely computationally demanding. Therefore, it is necessary to find simplify the model that reduces the CPU time need.

The simulation results of this model show significant reduction of simulation time of approximately of 20,000 times when compared to the switching ABC model. The concept of this model is to use the information from controller (reference voltage and current) to approximate the current flowing in each switching device and diode. Hence, the conduction losses and switching losses can be approximated. The model is based on analytical approach by using the reference voltage and current to calculate the conduction losses and switching losses in each device. Then the power losses are subtracted from the input power.

The concept of this model begins by calculating the power factor angle by measuring the active power (P), reactive power (Q), and apparent power (S). These can be found by:

Active power: 
$$P = V_a I_a + V_b I_b + V_c I_c$$
 (8)  
Reactive power:  $Q = \frac{1}{\sqrt{3}} \cdot \{(V_b - V_c) * I_a + (V_c - V_a) * I_b + (V_a - V_b) * I_c\}$  (9)

Apparent power: 
$$S = \sqrt{P^2 + Q^2}$$
 (10)

**Power factor:** 
$$\varphi = \cos^{-1}(\frac{P}{s})$$
 (11)

To simplify the calculation, the load current of the PEI is assumed to be sinusoidal. Since the power factor angle is determined, the average and RMS values of transistor and diode currents can be calculated by the following equations;

$$\overline{I_Q} = I_{Peak} \left( \frac{1}{2\pi} + \frac{Mcos\varphi}{8} \right)$$
(12)
$$\overline{I_D} = I_{Peak} \left( \frac{1}{2\pi} - \frac{Mcos\varphi}{8} \right)$$
(13)

$$I_{Q,rms} = I_{Peak} \sqrt{\frac{1}{8} + \frac{Mcos\varphi}{3\pi}}$$
(14)

$$I_{D,rms} = I_{Peak} \sqrt{\frac{1}{8} - \frac{Mcos\varphi}{3\pi}}$$
(15)

This information is used to evaluate power losses based on piecewise linear approximation of the device's on voltage characteristics. The power dissipated in a device can be divided into two parts; first is a constant voltage drop which can be calculated by the average current multiply by the voltage drop. Second is a resistive element which is equal to the squared of the RMS current multiply by the resistance. The sum of these is the total power dissipated in the device. Therefore, the conduction losses for transistors and diodes can be found as:

$$P_{Q(cond)} = \overline{I_Q} V_Q + I_{Q,rms}^2 r_Q \tag{16}$$

$$P_{D(cond)} = \overline{I_D} V_D + I_{D,rms}^2 r_D \tag{17}$$

For this three phase PEI, the total conduction losses are then six times the sum of transistor and diode conduction losses.

The switching losses in the transistor depend on the type of transistors (IGBT and MOSFET) and dynamic characteristics. During the turn-off, the power losses depend on two factors; speed of the gate drive and the IGBT's tail current due to minority carriers. However, MOSFETs do not have this tail current effect. The turn-on losses are due to the rate of current change and the stored charge in the free-wheeling diode. The total of switching losses energy can be measured by integrating the product of the current and voltage over time. These energy values are normally given in the device datasheet.

As mentioned earlier, the control used in the model is sine wave PWM. The switching losses are the total switching energy divided by the carrier PWM frequency.

$$P_{sw,one\ cycle} = \frac{E_{tot}(i)}{T_c}$$
(18)

where  $E_{tot}$  is the switching energy as a function of current.

The average switching losses in the transistor can be found by integrating the energy over half a sine period (0 to  $\pi$ ),

$$P_{sw} = \frac{1}{2\pi} \int_0^{\pi} P \ d\theta = \frac{f_{sw} E_{max}}{\pi}$$
(19)

Generally, the information of turn-on and turn-off energy (Eon and Eoff) given in the device datasheet are calculated based on

certain test conditions. Therefore, it is necessary to scale it to the operating condition.

$$P_{sw} = (E_{on} + E_{off}) \cdot \frac{V_s}{V_{test}} \cdot \frac{f_{sw}}{\pi}$$
(20)

where  $V_{test}$  is the datasheet test voltage.

For three phase PEIs, the focus of this paper, the total inverter losses are therefore six times the sum of the diodes and transistor power losses. These losses are then subtracted from the input power to balance the energy of the system.

# IV. NON-SWITCHING DQ MODEL

This model aims at accurate and efficient simulation of the PEI power losses within the DQ reference frame. By utilizing the DQ rotating reference frame, time varying signals can be represented as constant values allowing larger simulation step size, lower computational demand and hence faster simulations times.

In this model, the output reference values of voltage and current are in the DQ domain ( $V_{dq}$  and  $I_{dq}$ ). This can be converted into active power, passive power, apparent power, and power factor by using the following equations;

Active power:	$P = 1.5 \cdot \left( V_d I_d + V_q I_q \right)$	(21)
Reactive power:	$Q = 1.5 \cdot \left( -V_d I_q + V_q I_d \right)$	(22)
Apparent power:	$S = \sqrt{P^2 + Q^2}$	(23)
Power factor:	$\varphi = cos^{-1}(\frac{P}{s})$	(24)

By using this information, the power losses in the switching devices and diode can be calculated in the similar way as "Nonswitching ABC model"

To validate the non-switching DQ model, the simulation is compared with the non-switching ABC mode. The total inverter losses are shown in 6. The result showed exactly the same PEI power losses. Furthermore, the non-switching DQ model can further reduce simulation time significantly. Compared to the non-switching ABC model, the simulation time is reduced by a factor of 30 for the IGBT + Diode DQ model and by a factor of 28 for MOSFET + Diode DQ model.



Table 1: Summary of simulation time between two models for different transistors and diodes.

	Non-switching ABC model	Non-switching DQ model
IGBT + Diode model	0.24s	0.008s
MOSFET + Diode model	0.25s	0.009s

Remark: The result is to simulate the real-time of 2s.

#### IV. MODEL VALIDATION

In order to verify the power losses in the PEI Modelica models, which were built in Dymola, the models are compared with established equivalent models within the PLECS simulation environment. In PLECS, the IGBT/MOSFET devices are able to be parameterized based on manufacturer's datasheets. This is then used to calculate the power losses of the device. This is therefore a good equivalent to the newly implemented models developed in Modelica which are described in this paper. The test circuit which was utilised is shown in Figure 7 (Dymola) and Figure 8 (PLECS). As can be seen from the Figures, in both cases this included an ideal power source, PEI and Permanent Magnet (PM) Machine with a torque load. An equivalent controller was also used in both environments. The PM Machine and controller are carefully parameterised to ensure that both of the models have the same output load requirements.



Figure 7. Test circuit within Dymola - Modelica.



Figure 8. Circuit diagram of the three phase voltage source inverter and PMSM in PLECS.

A PM Machine was built that has the same characteristics and input parameters as the PLECS simulation model. Figure 9 and Figure 10 show the input parameters for the PM in Dymola-Modelica and PLECS, respectively.

Three-phase permanent magnet	synchronous machine with sinusoidal back EMF.	
Parameters Assertions		
Model:	Friction coefficient F:	
Rotor reference frame	• 0	[
Stator resistance R:	Number of pole pairs p:	
2.875	4	[
Stator inductance [Ld Lq]:	Initial rotor speed wm0:	
[8.5e-3 8.5e-3]	0	
Flux induced by magnet Phi:	Initial rotor position thm0:	
0.175	0	[
Inertia J:	Initial stator current [isa0 isb0]:	
0.8e-3	[0 0]	[

Figure 9. Input parameters of the PMSM in Dymola.

pp	4 +		Pole pairs	s number		
Jr	0.8e-3 •	kg.m2	Rotor's m	noment of iner	ia	
Js	5.62e-2	kg.m2	Stator's r	moment of iner	tia (useS	upport=true)
Maximum Values —						
Max				1000	• A	Maximum current
tauMax				1000	• N.m	Maximum torque
Resistance						
R		2.875 •	Ohm	Phase resist	ance (line	e-to-neutral) @ Tre
TRef		20	degC	Reference to	emperatu	re
Alpha	3.920e	-3 🔻 🕨	1/K	Linear tempe	rature co	pefficient @ 20C
Inductances						
Ld		8.5	ie-3 • H	Magnetizing	inductar	nce along the d-axis
Lq		8.5	ie-3 • H	Magnetizing	inductar	nce along the q-axis
Flux induced by ma	gnet					
phi	0.175 Const	ant flux l	inkage indu	uced by the ma	ignets in	the stator windings
Permanent Magnet	Flux					
VsOpenCircuit	20	00 • V	Open	circuit voltage	per pha	se @ fn (rms value)
6	10/		. Nomir	al fragmancu		

Figure 10. Input parameters of the PMSM in PLECS.

The power losses of the PEI are then simulated and compared between both software. The results of simulation are shown in Figure 11.

A. Total PEI losses



Figure 11. A comparison of total PEI losses for different models.

Table 2. A summary of total PEI losses for different models.

Model	Total PEI losses (W)
PLECS	20.991
Modelica non-switching (ABC & DQ) model	20.668
Modelica switching model	19.732

From Figure 11, the results from PLECS model and Modelica non-switching (ABC&DQ) model show nearly the same values of the total inverter losses with only 0.3W difference. However, there is a discrepancy during the initial start-up transient due to the way of switching losses calculation. In the Modelica model, it uses the peak load current while in the PLECS, it uses the switching device. This has a little effect as it occurs during transient (less than 0.02s). While the Modelica switching model has the lowest total inverter losses and 2W difference from PLECS model. This is due to the switching ripple exhibits in the switching device, causing the miscalculation in the averaging function. To minimize this ripple, a low pass filter can be placed after the measurement of load current.

#### B. CPU time

Table 3. A summary of simulation time for differen	t models.
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Model	CPU time (s)
PLECS	6.86
Modelica switching model	736
Modelica non-switching (ABC)	0.083
model	
Modelica non-switching (DQ)	0.0056
model	

Table 3 shows that the Modelica non-switching DQ model has the lowest CPU time, 0.0056s, while the Modelica switching model has the highest CPU time, 736s. As expected, this shows that the Modelica non-switching DQ model massively improves CPU time over Modelica switching model. In addition, the results show that the Modelica non-switching DQ has significantly lower simulation times than both the PLECs and Modelica ABC models. Considering the accuracy of the DQ models, as detailed above, this huge decrease in computational demand can be extremely useful when simulating large systems or those which incorporate systems within other physicals domains such as thermal or mechanical. It can also massively decrease development time when multiple simulations are needed for controller or filter tuning.

#### V. CONCLUSION

This paper has described the implementation of a newly developed multi-level PEI modelling library in Modelica. Each model has been described, including a new implementation of switching loss modelling which gives accurate results in vastly reduced simulation times. The results from simulation verified the developed IGBT and MOSFET models with built-in diode in Modelica. By comparison to established PLECS models, it has been confirmed that the model can accurately calculate the conduction losses and switching losses based on the manufacturer's datasheet.

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