

Scanning photocurrent microscopy of 3D printed light trapping structures in dye-sensitized solar cells

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Abstract

Converting solar energy directly into electricity as a clean and renewable energy resource is immensely important to solving the energy crisis and environmental pollution problems induced by the consumption of fossil fuels. Dye-sensitized solar cells (DSSCs) provide a technically and economically credible alternative that could challenge the dominance of conventional p-n junction photovoltaic devices in the solar energy market. DSSCs use dye molecules adsorbed at the surface of nanocrystalline oxide semiconductors such as TiO₂ to collect sunlight. These thin films require a large surface area, to adsorb many dye molecules, and mesoporous channels so the electrolyte can permeate the film and regenerate the dye molecules. This favourable morphology is traditionally achieved by the random assembly of a network of nanoparticles by the sintering process.

Two-photon polymerization is a 3D printing technique used to fabricate structures with feature resolutions down to 100 nm. We use this technique to fabricate TiO₂ thin films of optimised 3D micro-design for use in DSSCs. Our films have a considerable advantage over the conventional (random assembly) films as it allows the implementation of light scattering designs which are shown to significantly enhance photocurrent in the cell by up to ~25%.

Abbreviations:

DSSC, Dye-sensitized solar cells, TPP, Two-photon polymerization, PGMEA, Propylene glycol methyl ether acetate, FTO Fluorine doped tin oxide.

Keywords:

Solar energy; Microscopy; Dye-sensitised solar cells; Three-dimensional printing; Two-photon polymerisation

1. Introduction

Scientific and technical advances in photovoltaics over the last few decades have made solar technology one of the most viable alternatives to replace the dominant consumption of fossil fuels for energy production. Dye-sensitized solar cells (DSSCs) have attracted a great deal of attention following their development in 1991 by Grätzel and O'Regan [1]. They provide a technically and economically credible alternative to traditional silicon solar cells due to their low material cost, easy and inexpensive fabrication methods and competitive light to current conversion efficiencies [2]. There are however, challenges facing the development of DSSCs, including the improvement of cell stability [3] and cell efficiencies [4].

A typical DSSC consists of transparent conducting oxide electrode, a mesoporous nanocrystalline oxide semiconductor charge transport layer, photosensitive dye molecules anchored to these nanoparticles to absorb photons, a redox electrolyte to regenerate the dyes and a counter electrode as shown schematically in figure 1. DSSCs typically use thin films of sintered TiO₂ nanoparticles for the transport layer [5] since they have a large surface area, required for dye-adsorption, and mesoporous channels, required for electrolyte diffusion.

One area of particular interest for improving efficiency is in the TiO₂ thin film. This can be done by increasing the surface area of the film for greater dye absorption, improving the electron transport through the film, and maximising light-harvesting in the layer [6]. In traditional silicon, solar cells many varieties of light management structures in the active layer have been studied. Pillars, pyramids, nanowires have all been shown to increase efficiency in solar cells [7, 8, 9]. However light trapping in DSSCs is more problematic because high performance cells also require the active layer to be nanocrystalline in order to retain a high surface area for dye adsorption and remain porous for dye regeneration by the redox electrolyte.

Many of the top performing DSSCs utilise simple but effective hierarchical structures to enhance light trapping in the active layer in the cell. The typical diameter of TiO₂ nanoparticles for dye adsorption and electron transport is ~20 nm [3], however another light scattering layer of nanoparticles is applied of diameter ~300 nm [10]. This layer has been shown to increase the overall efficiency of the cell by up to 10% [11]. The light trapping properties of periodic pyramids and other textured surfaces have been well studied in silicon solar cells [12, 13], and more recently studied in DSSCs [14]. More complex light scattering structures in DSSC have been fabricated using a template method whereby a polymer template is fabricated, the template is infiltrated with TiO₂ nanoparticles and the template subsequently removed leaving the desired structure behind [15, 16]. Other interesting and complex light trapping structures

templated on constructs produced in nature have been incorporated into DSSCs and have shown performance enhancing effects [17]. Labelle *et al.* have shown from theoretical and experimental work that designed TiO₂ pyramids can trap light inside a structure which will internally reflect, increasing the light path length through the active material, increasing the chances of absorption. They have shown that a periodic pyramidal patterned film of pitch 10 μm, width 8 μm and height of approximately 5 μm gave an improvement of 24% in short circuit current in their colloidal quantum dot sensitized solar cells QDSC [18].

There are many interesting and novel methods of introducing light trapping into the active layer of DSSCs to increase cell efficiency. However due to current fabrication methods it is difficult to incorporate design into these structures. In this work, we use a submicron 3D printing technique known as two-photon polymerization (TPP) to fabricate light trapping structures for DSSCs. This technique has the ability to fabricate precise micro and nanostructures with feature resolution of around 150 nm [19] and has been used for the fabrication of photonic crystals [20, 21], and optical interconnects between photonic chips [22]. The utilisation of this technique will allow us to introduce designed light trapping structures into DSSCs. Polymer stamps are fabricated using this technique and then used to pattern TiO₂ electrodes. The effectiveness of these structures is then studied using scanning photocurrent microscopy (SPCM) which allows in-depth characterization of the enhancements made by their introduction into the cell. This characterization also allows the study of defects in the cell some of which are invisible using conventional microscope techniques, or when the macroscopic power conversion efficiency of the whole device is measured.

2. Experimental

2.1 Fabrication of polymer stamps

TPP uses photopolymers such as SU-8 which are traditionally used in 2D photolithography techniques and are irradiated by UV light to drive the polymerization process [23]. However, in TPP absorption occurs when two near-infrared (NIR) photons simultaneously drive the electronic transition. The non-linear dependence of the absorption process allows the excitation to be localized within the volume of the focal point of the NIR-laser, leading to the spatial resolution of ~150 nm, which is better than the diffraction limit of the focussed laser beam [24] as shown schematically in figure 1. Polymer stamps were fabricated using a commercial Nanoscribe system (Photonic Professional, Nanoscribe GmbH, Germany), with a Nanoscribe tailored photoresist IP-L.

Samples were prepared on glass wafer substrates which were cleaned with acetone, isopropanol and deionized water and subsequently dried under nitrogen gas flow. The commercial Photoresist IP-L (Nanoscribe) was drop cast onto the substrate and the fabrication process began. Negative pyramids were fabricated with dimensions of 8 μ m width and 5 μ m depth in 200x200 μ m arrays with a 10 μ m pitch. Once the polymerization process was complete the photoresist was developed in a Propylene glycol methyl ether acetate (PGMEA) solvent bath for 30 mins then removed, bathed a second time in fresh PGEMA, rinsed with isopropanol to remove all un-polymerized material and dried under nitrogen gas flow. Stamps were then characterized (Figure 2a) and checked for quality before being using in electrode fabrication.

2.2 Preparation of patterned TiO₂ electrode

Fluorine doped tin oxide (FTO) conducting glass substrates (Pilkington, TEC15, sheet resistance 15 Ω /square) were cleaned in an ultrasonic bath with acetone then ethanol and dried under a nitrogen gas flow. Before TiO₂ deposition, glass FTO substrates were treated with TiCl₄ solution (0.04M) at 70°C for 30 minutes to improve TiO₂ adhesion. Substrates were rinsed with deionized water and dried under nitrogen gas flow. TiO₂ paste (Ti-Nanoxide, Solaronix, S. A.) was applied to the substrate using a doctor blade technique, one layer of paste was applied and the electrode was dried on a hot plate at 100°C for 1 minute. Before the paste was completely dried the stamps were pressed into the paste and left to stand for 5min before being removed. The patterned electrodes were then calcinated by slowly heating the samples to 500°C at 5°C/min and left for 30min, the slow heating ramp helps to reduce cracking in the TiO₂ film. In some cases, after stamping, the polymer stamps peeled from their glass substrate and remained in the TiO₂, the heating process achieves both the sintering of the TiO₂ nanoparticles and the removal of these unwanted stamps and any organic material left in the samples. The patterned electrodes were then characterized with SEM and the highest quality selected for cell fabrication. Energy-dispersive X-ray spectroscopy (Hitachi, Table top TM3030) measurements were also made to ensure that no organic material was left on the sample. After characterization, the film was rinsed again in ethanol and dried under a nitrogen gas flow. The electrodes were then sensitized by immersion into 0.5 mM N-719 dye (Solaronix, Ruthenizer 535-bisTBA) ethanol solution for 24h.

2.3 DSSCs assembly

Counter electrodes were prepared by drop casting a Pt solution (Solaronix, Platisol T) onto FTO glass substrates (Pilkington, TEC15, sheet resistance 15 Ω /square) and calcinated at 450°C for 15 mins. Sensitized patterned TiO₂ electrodes were assembled face to face with

counter electrodes using 25 μm thick thermoplastic frame (Solaronix, Meltonix 1170-25). Finally, the assembled cell was filled with the electrolyte containing the I^-/I_3^- redox system (Iodolyte HI-30, Solaronix) through the predrilled hole in the counter electrode, which was then sealed.

2.4 Scanning photocurrent microscopy of DSSCs

SPCM uses a focused laser to generate photocurrent locally in a device, scanning therefore allows the mapping of photocurrent response in a large area device. This makes it a powerful non-destructive technique for investigating structures in these devices and how they affect performance. Previously the SPCM technique has been used to investigate optoelectronic properties of a wide variety of semiconductor structures and devices such as nanowire field-effect transistors [25], quantum dot thin films [26] and nanowire array solar cells [27]. The SPCM measurements were performed using a focused laser beam (405 nm) onto a sample mounted on an XY-combination of two hybrid piezo-motorised XY-translation stages M-511.HD (PI Instruments) providing XY-translation up to 10cm with precision <10 nm, electrical current was recorded as a function of the position of the laser spot on the sample.

The SPCM experimental set up is illustrated in Figure 2a. Figure 2b and 2c shows the versatility of the system. Spatial resolution of our SPCM set-up can be adjusted between <1 μm and ~ 1 mm by the diameter of the focussed laser spot. Figure 2b shows the entire active area of a conventional 5x5 mm DSSC cells where the photocurrent is mapped with a resolution of ~ 300 μm . In the centre of this map there is clear artefact leading to a corresponding drop in photocurrent, Figure 2c shows a higher resolution (~ 5 μm) map of this defect. It is worth noting here that this artefact was not visible in optical images of the cell and only became apparent after the cells characterization using SPCM, highlighting the power of this technique as a non-destructive experimental probe for detecting and investigating differences in cells which cannot be detected using conventional optical techniques.

3. Results and discussion

Two-photon polymerization allows for the fabrication of repeatable high quality polymer stamps with submicron resolution. Figure 3a shows SEM characterizations of these negative pyramid polymer stamps, here the depth is approximately 5-6 μm . Stamps occasionally became dislodged from their glass substrates during post processing, to mitigate these losses stamps were fabricated in batches of 9 separate 200 μm^2 periodic arrays per sample. This allowed for the selection of the highest quality areas of the patterned electrodes during SPCM mapping. These stamps were then used for the patterning of our TiO_2 electrodes, Figure 3b

shows a typical SEM image of a good quality portion of the patterned electrodes. Throughout the stamping and calcination process some cracking or deformation of the pyramids is unavoidable. Figure 3c and 3d show a photocurrent map alongside the corresponding optical image of 4 isolated pyramid structures in the centre (and another pyramid to the extreme right of the figure). This map shows a clear enhancement of the photocurrent overlapping with the patterned area of the DSSC, with 4 distinct peaks in photocurrent overlapping the 4 pyramids. The next peak comes over an area where there is damaged pyramid structure. The final peak towards the extreme right is also over a pyramid structure.

Figure 4a and c show further enhancement of the photocurrent on patterned areas of the DSSCs. Both maps here show enhancement in photocurrent when compared to the surrounding non-patterned planar TiO_2 . However, the enhancement in both these photocurrent maps is not uniform across the patterned area. In figure 4a enhancement in the photocurrent ranges from $\sim 0-5\%$ in the low enhancement regions and to $\sim 15-20\%$ in the high enhancement regions. In figure 4b enhancement again ranges from $\sim 0-5\%$ in the low enhancement regions and to $\sim 20-25\%$ in the high enhancement regions. Figure 4b and d show the corresponding SEM images of these photocurrent maps respectively. These images also show some non-uniformity in stamped pyramid structures, with the areas towards the outside of the array not being as well stamped as the centre. This is reflected in the photocurrent maps where the enhancement is seen in an area less than the $200 \mu\text{m}^2$ patterned region in the SEM images. The images also show some cracking in both samples incurred during calcination, which could help to account for the non-uniformity of the enhancement in the centre of the patterned areas. However, the cracks seen in the corresponding SEM images of figure 4b and d seem to, in some regions, have a positive effect on the photocurrent enhancement and in other regions a negative one.

For more insight into the effects of these cracks on the photocurrent, further defects were probed in different samples, with both patterned areas and planar areas of the cell being characterized. Figure 5a shows the photocurrent map of a large crack that can be seen in the corresponding SEM image in 5b. Here the crack has a negative effect on the photocurrent leading to a significant drop. This suggests that our SPCM technique provides more accurate local defect characterization than other microscopy techniques. Figure 5c and d show the photocurrent map and the corresponding SEM image of another sample of planar TiO_2 respectively. Here, although no obvious features are visible in the SEM image there is a clear drop of current in the sample. The drop in current over this small area is significantly larger than the smaller current variations across the rest of this sample. These figures show how effective this technique is at characterising not just visible defects such as cracks and to what

extent they can affect the cell performance, but also how this technique can be used to find and characterize defects that are hidden below the surface.

Much research has been done into the formation of micro-cracks in crystalline silicon solar cells and how these cracks affect the cells electrical and optical properties [28]. However, it is difficult to get a full picture of how these defects affect cell performance. Cracking in the active layer does not necessarily lead to an initial strong reduction of the cells output power [29] but they do lead to lower cell stability which in turn leads to a reduction of the performance of cell over time [30]. There is also evidence for heavy cracking leading to an introduction of new recombination centres that lead to a subsequent reduction in cell performances [31].

Whilst studies of the localisation and characterization of specific cracks and defects in DSSC are rare much care is taken during the fabrication of high performance DSSCs to reduce cracking in the TiO_2 to a minimum [32]. When a variety of different TiO_2 nanoparticle pastes are studied, pastes which lead to a surface that is heavily populated with micro-cracks has been shown to perform significantly worse than electrodes with smooth surfaces [33]. The bending of TiO_2 electrodes in flexible DSSCs tests leads to increased cracking and decreased cell efficiency with increasing number of bends [34], with this reduction being attributed to the decline of the short circuit current from the increased electron transport resistance, and to electron collection. Photocurrent mapping has been used to study intentionally induced defects of cracks on the millimetre scale. These large cracks all lead to significant drops in photocurrent performance [35].

The periodic pyramid patterned TiO_2 electrodes fabricated and studied in this paper were able to trap light incident to the cell to enhance the photocurrent generated when compared to planar regions of the sample cell. In this paper, we demonstrate that the 3D-pinting of periodic submicron structures can enhance solar cell performance increasing photocurrent by up to ~25%. This enhancement is in agreement with one observed by Labelle *et al.* [19] on cells with a similar geometry. Further reports in the literature of DSSCs [36] and QDSCs [37] show the performance-enhancing effects of periodic light trapping microstructures in the TiO_2 active layer of these types of solar cells.

3. Conclusion

The advantage of our 3D-pinting technique for the fabrication of light trapping structures is the high degree of design that can be incorporated. In this study polymer stamps are fabricated which are used to pattern the TiO_2 . However, using two-photon polymerization any negative polymer structure, with a resolution approaching the nanoscale, could be produced and the

TiO₂ nanoparticles infiltrated into the template before being removed via calcination much like the removal of the stamps in this study. Highly complex structures with intricate internal 3D light trapping designs can be fabricated using this technique. This can allow designers to no longer be limited by previous fabrication methods such as the self-assembly of latex spheres [38], or the use of multi-beam interference lithography to make polymer templates [39] which can both create complex internal structures but are restricted in the level of control that can be achieved. This fabrication technique is not limited to the fabrication of light trapping structures in DSSCs and could be employed for the fabrication of structures in a wide variety of photovoltaic devices. Using this technique, a number of theoretically aided light trapping designs can be fabricated onto the same samples a few microns apart thereby reducing the variables involved when trying to compare samples and ensuring almost identical local conditions during characterization. Utilising SPCM these different light trapping structures can be experimentally tested to see which has the best photocurrent performance enhancing effects.

This work demonstrates a flexible and novel 3D-printing method for the fabrication of DSSC based on periodic submicron structures. Our structures are shown to produce significant performance enhancing effects of up to ~25% on the photocurrent when compared to planar TiO₂. This fabrication technique shows the ability to be able to fabricate highly complex designed 3D light trapping structures that would be impossible using other methods. Furthermore, these structures can be fabricated onto the same sample allowing for close control of conditions across all structures during characterization. This work also shows the effectiveness of the developed SPCM technique in the mapping of local DSSC efficiencies with submicron resolution on a large scale (>1cm) devices. Our findings could lead to the development of novel 3D-printed submicron optoelectronic device.

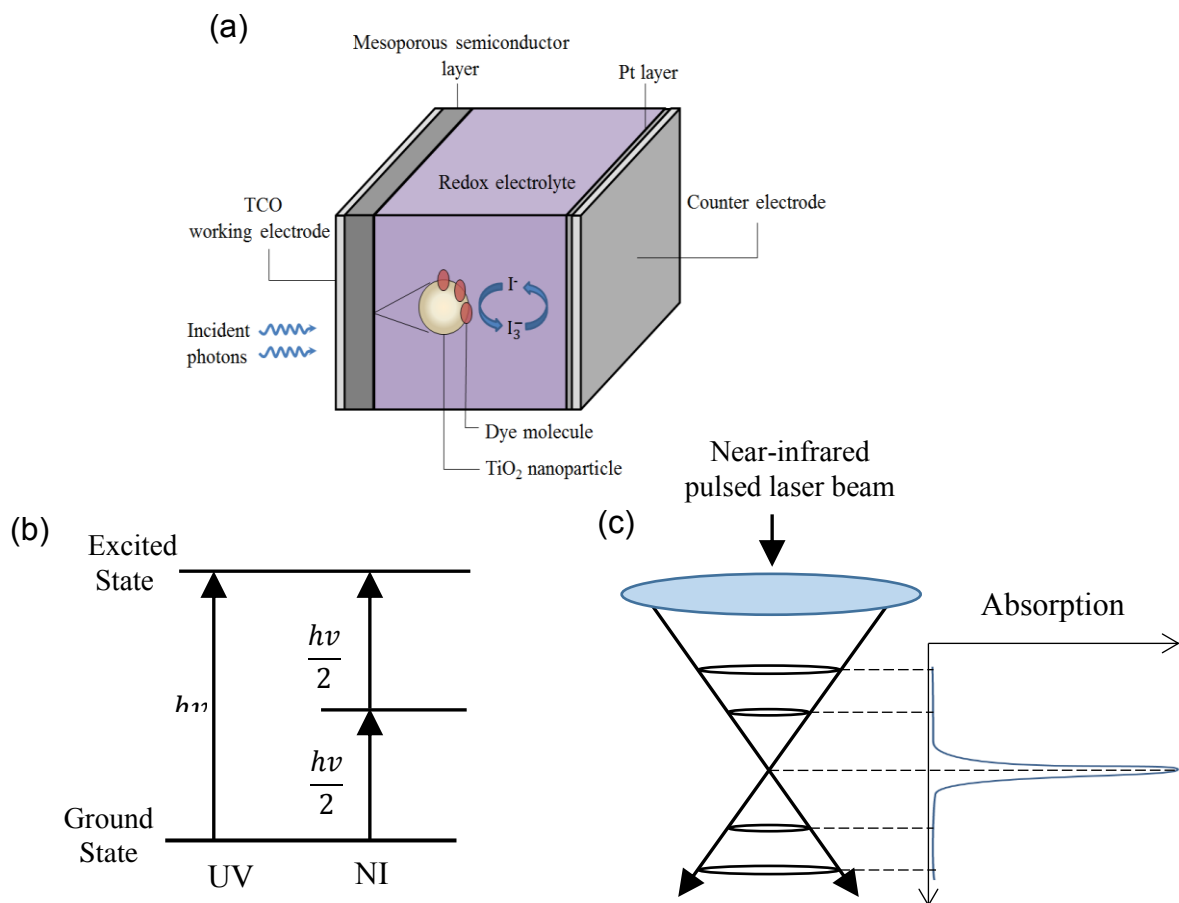


Figure 1 (a) Schematic of DSSCs (b) Single and two-photon absorption processes (c) Absorption of focused laser light in TPP.

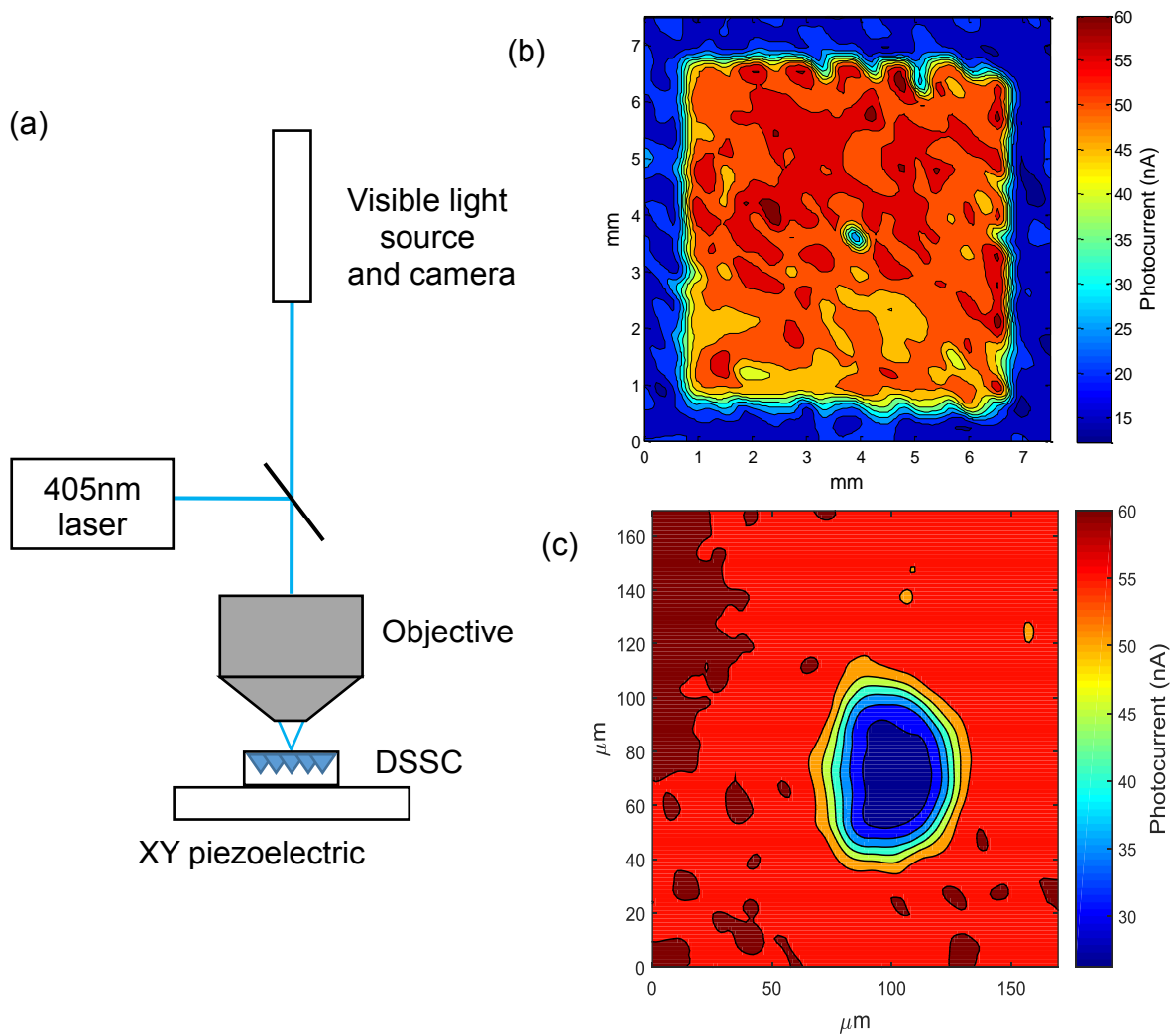


Figure 2 (a) Schematic of experimental set up (b) Low resolution photocurrent map of active area of DSSC (c) High resolution photocurrent map of centre of same cell.

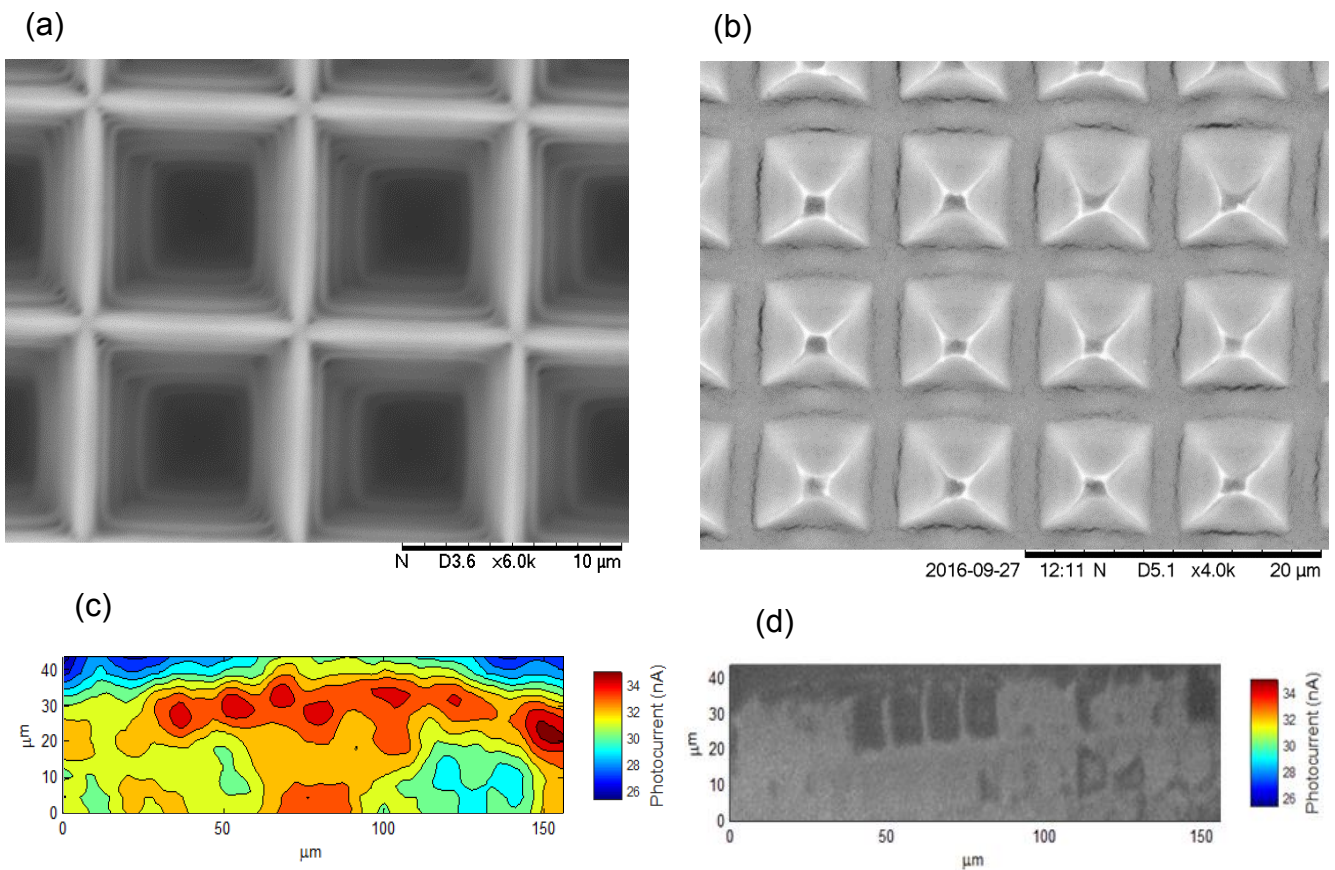


Figure 3 (a) SEM Image of polymer 3D printed stamps (b) SEM image of corresponding patterned TiO₂ electrode (c) Photocurrent map showing enhancement from pyramid structures (d) Corresponding optical image.

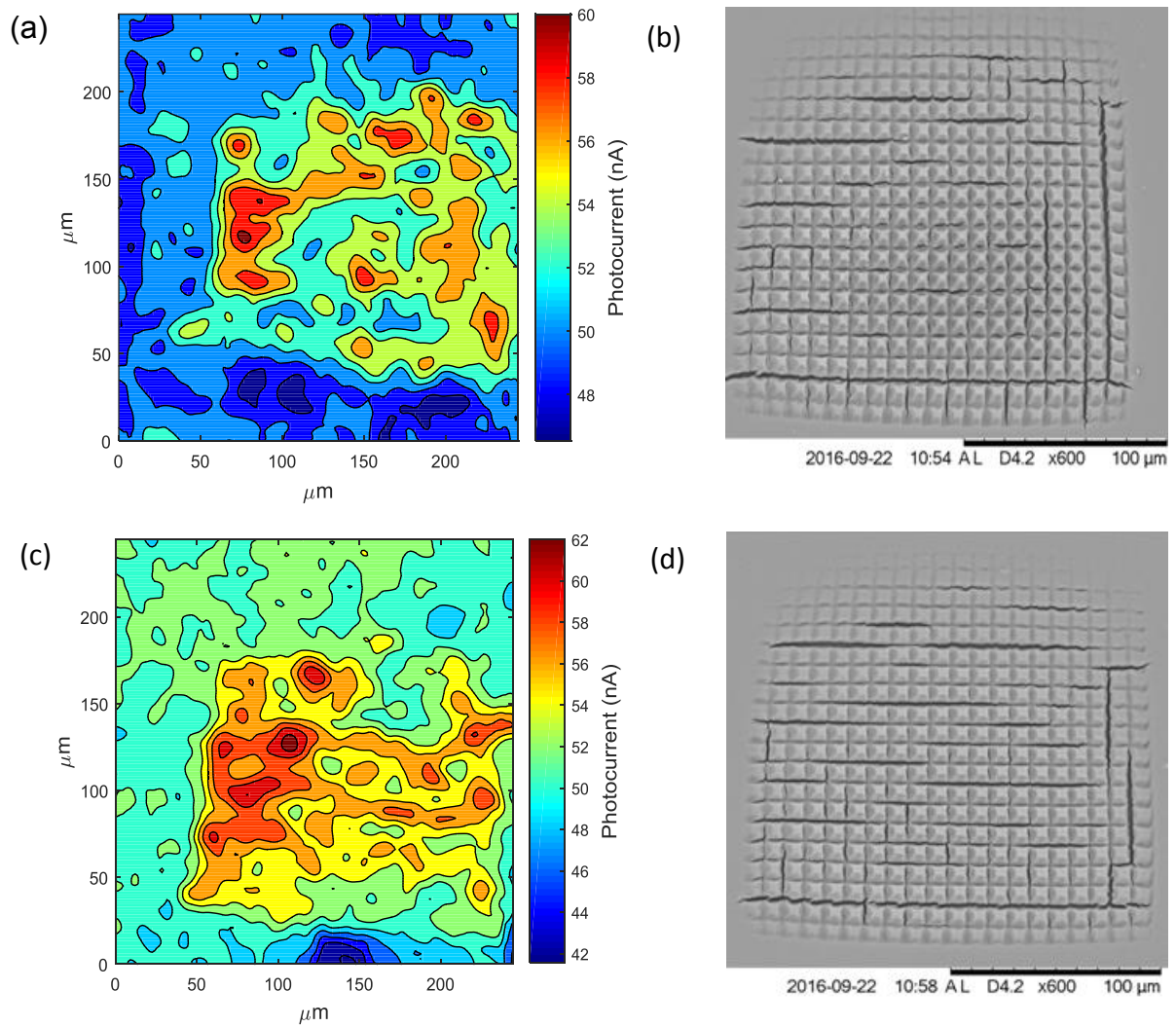


Figure 4 (a) Photocurrent map showing enhancement (b) Corresponding SEM image. (c) Photocurrent map showing enhancement (d) Corresponding SEM images.

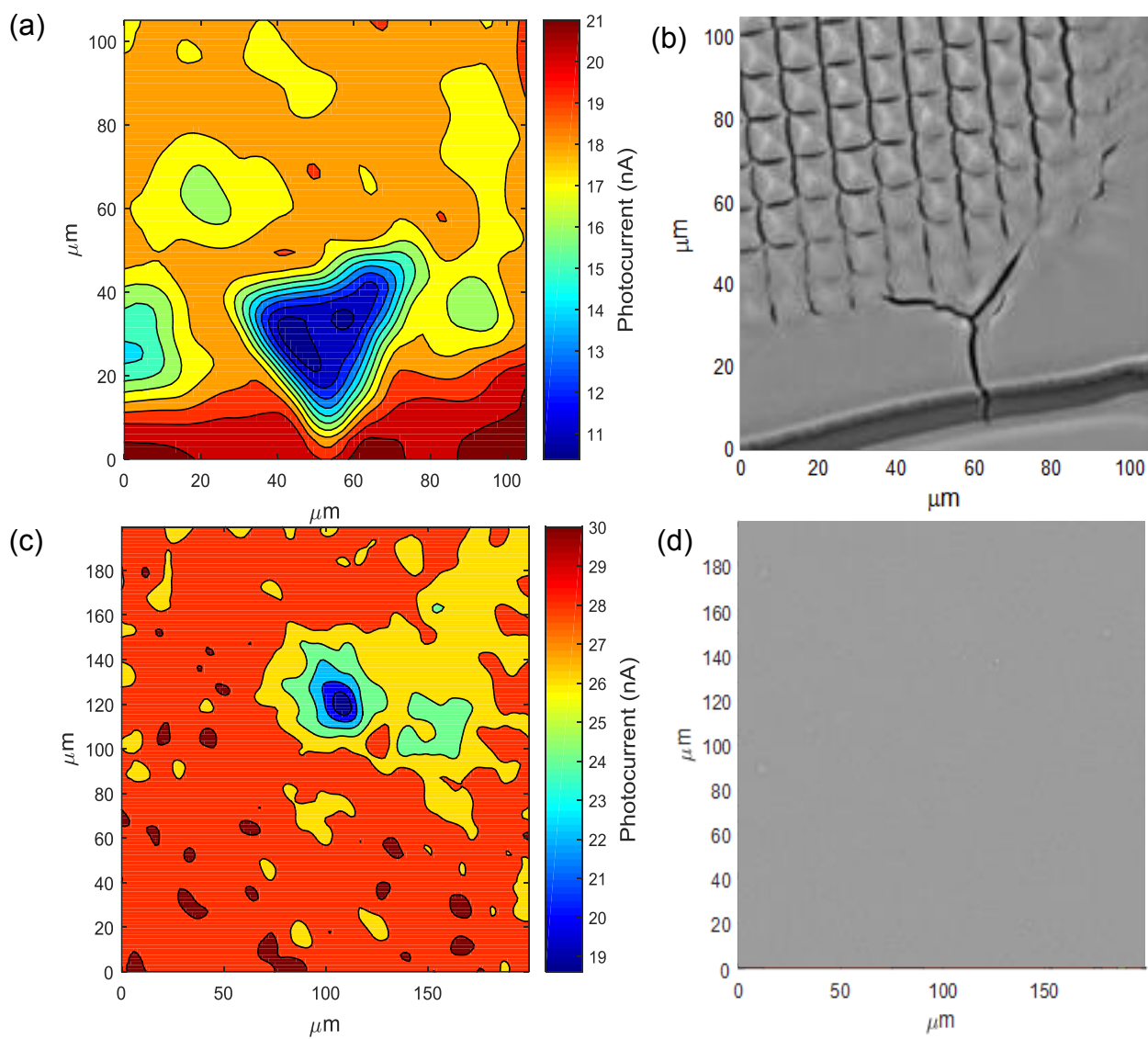


Figure 5 (a) Photocurrent map showing crack in TiO_2 electrode (b) Corresponding SEM image (c) Photocurrent map of hidden defect in TiO_2 electrode (d) Corresponding SEM image.

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