

## Magnetotransport and lateral confinement in an InSe van der Waals Heterostructure

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### Abstract

**In the last six years, Indium selenide (InSe) has appeared as a new van der Waals heterostructure platform which has been extensively studied due to its unique electronic and optical properties. Such as transition metal dichalcogenides (TMDCs), the considerable bandgap and high electron mobility can provide a potential optoelectronic application. Here we present low-temperature transport measurements on a few-layer InSe van der Waals heterostructure with graphene-gated contacts. For high magnetic fields, we observe magnetoresistance minima at even filling factors related to two-fold spin degeneracy. By electrostatic gating with negatively biased split gates, a one-dimensional channel is realized. Close to pinch-off, transport through the constriction is dominated by localized states with charging energies ranging from 2 to 5 meV. This work opens new possibility to explore the low-dimensional physics including quantum point contact and quantum dot.**

### Introduction

Indium selenide (InSe) is a novel 2D semiconductor that belongs to the family of post-transition metal chalcogenides, which have gained significant attention due to their unique electronic properties such as large variation of the bandgap as a function of number of layers.[1–5] The property that the bandgap of InSe increases significantly with decreasing number of layers open a possibility to facilitate bandgap engineering

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3 technology. Furthermore, unlike transition metal dichalcogenides (TMDCs) that have a  
4 direct band-gap only in films with one or two layers[6,7], InSe has a direct-band gap over  
5 a wide range of layer numbers.[8] Even when the crystal becomes an indirect band gap  
6 semiconductor, it remains optically active, enabling the fabrication of devices that are  
7 highly photosensitive over an extended wavelength range from the near-infrared to the  
8 visible.[9–14] The valence band of few-layer InSe exhibits an almost flat and slightly  
9 inverted “Mexican hat” near the  $\Gamma$  point which could provide an opportunity to get strong  
10 correlations in hole-doped material such as superconductivity and tuneable  
11 magnetism.[15–18]  
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15 Most importantly, the in-plane electron masses are significantly lower than in  
16 TMDCs.[19] In recent experiments, the mobility of few-layer InSe has surpassed  $\sim 1,000$   
17  $\text{cm}^2/\text{Vs}$  and  $\sim 12,000 \text{ cm}^2/\text{Vs}$  at room temperature and 4.2 K, respectively.[1] These  
18 extraordinary electrical properties provide an excellent platform for 2D electronics and  
19 applications of InSe in low-dimensional devices that require in-plane confinement of  
20 carriers and to explore the low-dimensional physics including quantum point contact and  
21 quantum dot. Recently developments in the fabrication of electronic nanostructures has  
22 led to the exploration of field-effect devices based on 2D semiconductors such as  
23 phosphorene[20,21] and TMDCs.[22–25] In particular, laterally gate-defined quantum  
24 devices were realized using molybdenum disulfide ( $\text{MoS}_2$ ) heterostructures with high  
25 carrier mobility.[26–29] However, high quality quantum confinement in other 2D  
26 semiconductors has not been explored to date since the surface of InSe can react with  
27 water and oxygen like in many other 2D materials, In particular, oxidation[30,31] can  
28 degrade the surface and cause contact barriers[32] leading to highly resistive and non-  
29 ohmic contacts, and an underestimation of the field effect mobility.[33,34] Due to the fact  
30 that InSe degrades in ambient conditions, the fabrication of high quality InSe device is  
31 challenging and the realization of in-plane confinement is so far experimentally  
32 unexplored. To prevent the oxidation, few-layer InSe flakes can be encapsulated between  
33 hexagonal boron nitride (hBN) layers in a glovebox filled with high purity argon  
34 gas.[21,35]  
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38 In this manuscript, we report on the fabrication of a gate-defined InSe  
39 heterostructure and its electrical properties. To create reliable contacts, we fabricate gates  
40 covering regions of overlapping InSe and graphene layers. We find that applying  
41 appropriate contact gate voltages helps to prevent the formation of Schottky contacts and  
42 to increase the local density of states in this region, leading to linear current-voltage  $I$ - $V_{sd}$   
43 characteristics, i.e. ohmic behavior. We further utilize top-gates covering InSe in a split-  
44 gate geometry to deplete the electron gas in the InSe layer below the gates thus forming a  
45 constriction. For high magnetic fields, we observe magnetoresistance minima at even  
46 filling factors, which can be accounted for by a two-fold spin degeneracy.  
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## 54 55 **Methods** 56 57 58 59 60

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3 To fabricate our devices we employ the dry transfer technique.[25,27,28] First,  
4 we use the top hBN layer (35nm thick) on polymethyl methacrylate (PMMA) membrane  
5 to pick up four few-layer graphene flakes serving as electrical contacts to the InSe  
6 crystal. Then InSe crystal is mechanically exfoliated on a PPC-covered wafer and  
7 identified by optical contrast in the glovebox. A selected six-layer InSe flake is  
8 transferred onto the center of the top hBN/graphene heterostructure so that each graphene  
9 layer touches the InSe on four sides, as shown in Figs. 1a and 1b. Finally, the  
10 heterostructure is deposited onto another hBN flake (8nm thick) on degenerately doped  
11 silicon wafers with a 285 nm SiO<sub>2</sub> layer. Both top and bottom hBN isolate and protect  
12 InSe from the environment and reduce scattering of carriers by phonons and charged  
13 impurities in the Si-substrate. The top hBN serves as a dielectric for the split-gates (SG)  
14 with a gap of 120 nm and the contact gates (CG) in Fig. 1c. After the assembling process,  
15 the resulting heterostructure is taken out of the glovebox. The split-gates and the contact-  
16 gates on the top hBN are fabricated by standard electron-beam lithography and metal  
17 deposition. Finally, one-dimensional Cr/Au (5/45 nm) edge contacts form the graphene  
18 electrodes.[36]  
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## 26 **Results and discussion**

27 The quality of the contacts is assessed by low temperature ( $T = 1.8$  K) two-  
28 terminal  $I$ - $V_{sd}$  characteristics at different  $V_{bg}$  and  $V_{cg}$ , as shown in Fig. 2a. At  $V_{cg} = 0$  V  
29 and  $V_{bg} = 30, 40$  and  $50$  V, we observe non-linear  $I$ - $V_{sd}$  curves indicating non-ohmic  
30 contacts. However, linear behavior is achieved at  $V_{cg} = 16$  V leading to three-fold current  
31 enhancement through the InSe channel as compared to  $V_{cg} = 0$  V, thus suggesting that the  
32 contact gate controls the chemical potential of graphene and tunes the Schottky barrier  
33 between the 2D semiconductor and few-layer graphene.[1,23,37] This can be better seen  
34 in Fig. 2b where the isolated contact resistances  $R_c = \frac{1}{2} (R_{12,12} - R_{12,34})$  are plotted as a  
35 function of  $V_{bg}$  for various  $V_{cg}$  (where  $R_{12,12}$  and  $R_{12,34}$  are the two-terminal and four-  
36 terminal resistances, respectively). For the definition of  $R_{PQ,RS}$ , the current is injected  
37 through contact P and extracted through contact Q. The voltage is measured between  
38 contacts R and S. The contact resistance  $R_c$  decreases by up to three orders of magnitude  
39 with increasing  $V_{bg}$  and  $V_{cg}$ . For  $V_{cg} = 12$  V and  $V_{bg} > 30$  V,  $R_c$  is sufficiently small ( $< 100$   
40 k $\Omega$ ) for performing four-probe measurements using standard lock-in techniques.  
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46 For the determination of the sheet resistivity, we measured the resistance in  
47 two configurations,  $R_{34,12}$  and  $R_{23,41}$ , at  $V_{cg} = 16$  V, as shown in Fig. 2c. Because the  
48 separation between contacts 1 and 4 is shorter than that between contacts 1 and 2,  $R_{23,41}$  is  
49 smaller than  $R_{34,12}$  for all  $V_{bg}$ . Although the sample does not have an ideal van der Pauw  
50 geometry, we roughly estimate the sheet resistivity ( $\rho$ ) of the InSe using the van der Pauw  
51 formula with the results shown in Fig 2c. The resistivity varies in the range 1 - 6 k $\Omega$  at  $V_{bg}$   
52  $> 30$  V and  $V_{cg} = 16$  V. At  $V_{bg} < 30$  V, owing to both the high channel and contact  
53 resistances, four-probe measurements using standard lock-in techniques are not possible  
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3 but only two probe measurements could be carried out. Fig. 2d displays the mobility ( $\mu$ ),  
4 as derived from  $\mu = l/nep$ , as a function of  $V_{bg}$ , where  $n$  is the electron density. The mobility  
5 increases with increasing  $n$ . It reaches a value of  $\sim 1,500$  cm<sup>2</sup>/Vs at the maximum  $V_{bg}$ ,  
6 smaller than the field effect mobility ( $\sim 2,300$  cm<sup>2</sup>/Vs), which is derived from the  $V_{bg}$   
7 dependence of  $1/\rho$  in the range  $V_{bg} = 60-100$  V, as shown in the inset of Fig. 2d. This  
8 discrepancy is likely to originate from the large size of the contacts compared to the sample  
9 area. To investigate the magnetoresistance, we measured  $R_{12,43}$  as a function of the magnetic  
10 field ( $B$ ) and  $V_{bg}$ . Although some radial lines at high  $B$  are only weakly visible in Fig. 3a,  
11 the derivative of  $R_{12,43}$  with respect to  $B$  clearly shows a Landau fan at  $B > 5$  T (Fig. 3b),  
12 which suggests a quantum mobility of  $\sim 2,000$  cm<sup>2</sup>/Vs in agreement with the field effect  
13 mobility. Each dashed line in Fig. 3b corresponds to  $\nu = nh/Be$ , where  $\nu$  is the filling factor.  
14 The charge carrier density as a function  $V_{bg}$  is determined from the SdH (Shubnikov-de  
15 Haas) oscillations, as shown in the inset of Fig. 2c. The slope of the density versus gate  
16 voltage curve yields a back-gate capacitance  $C_{bg}/e = 7.3 \times 10^{10}$  cm<sup>-2</sup> V<sup>-1</sup>, which is in good  
17 agreement with the value  $7.36 \times 10^{10}$  cm<sup>-2</sup> V<sup>-1</sup> estimated from geometrical considerations  
18 taking into account the thicknesses of the SiO<sub>2</sub> (285nm) and the bottom hBN (8nm) layers.  
19 Shubnikov-de Haas minima match well with even filling factors  $\nu = 14, 16, 18$ , which is  
20 evidence for the two-fold spin degeneracy expected for InSe.[1]  
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24 In the next step, we investigate transport through a split-gate defined constriction  
25 with a lithographic gap of 120 nm. First, we apply a positive back gate voltage to create a  
26 uniform electron density in the sample. Then negative split-gate voltages are tuned such  
27 as to locally deplete the two-dimensional electron gas (2DEG) under the top gates. Fig.  
28 3c shows the conductance ( $G_{12,43}$ ) as a function of  $V_{bg}$  and  $V_{sg}$  at  $V_{cg} = 16$  V. As expected,  
29 for higher  $V_{bg}$ , a larger negative  $V_{sg}$  is required to pinch off the channel. The slope of the  
30 diagonal line between the red region (highly conductive) and the blue region (nearly  
31 insulating) is 8.6, corresponding to a capacitive coupling ratio  $C_{tg}/C_{bg} = 8.58$  in  
32 agreement with geometrical considerations. As shown in Fig. 3d, for  $V_{bg} < 80$  V, the  
33 conductance abruptly decreases with increasing  $V_{sg}$  towards negative values, indicating  
34 that the electron gas below the split-gate begins to deplete. Under these conditions the  
35 device becomes insulating with conductance values  $G < 0.01$   $\mu$ S. Interestingly, at  $V_{bg} >$   
36  $80$  V, the drop of the conductance at  $V_{sg} \sim -6$  V is followed by a slower decrease of the  
37 conductance with decreasing  $V_{sg}$  and the appearance of reproducible features. This  
38 indicates that in the high carrier density regime, after pinching off below the split-gates, it  
39 is possible to form a one-dimensional channel. This can be seen from the fact that the  
40 channel carrier density ( $n_{ch}$ ) between the split gates can be reduced by  $V_{sg}$ . Consequently,  
41 this transition occurs when  $n_{ch}$  reaches a value of  $\sim 2.5 \times 10^{12}$  cm<sup>-2</sup> and the device is  
42 highly resistive, as shown in Fig. 2c and inset. Due to the short mean free path ( $< 50$ nm)  
43 relative to the channel length ( $\sim 120$ nm), quantized conductance through the one-  
44 dimensional channel is not discernible.  
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3 To investigate the transport in the 1D channel, we measure the two-terminal AC  
4 current ( $I_{ac}$ ) as a function of  $V_{sd}$  and  $V_{Rsg}$  at  $B = 0$  T and  $V_{Lsg} = -10$  V (see Fig. 1c for the  
5 gate labels). A series of different diamond-shaped zero-current regions appear at  $V_{Rsg} < -8$   
6 V, matching the resonance peak positions in Fig. 4b. From the extent of these diamonds  
7 in  $V_{sd}$ -direction, we estimate the charging energy of the localized states, which ranges  
8 between 2 and 5 meV. These features are likely to originate from the disordered potential  
9 of the InSe layer and require further investigation.  
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## 13 **Conclusion**

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15 In summary, a high quality InSe van der Waals heterostructure encapsulated by  
16 hBN are achieved by a novel fabrication process under the oxygen- and moisture-free  
17 environment. We have demonstrated two-dimensional and one-dimensional electron  
18 transport with an electron mobility of  $\sim 2,300$  cm<sup>2</sup>/Vs at 1.8 K by additional electrostatic  
19 gating at the contacts and split-top gates. We observe a Landau fan in Quantum Hall  
20 regime indicating two-fold spin degeneracy and formation of a narrow conducting  
21 channel between split-gates with transport facilitated by localized states. As an  
22 electrostatically tunable tunneling barrier reaches complete pinch-off, atomically thin  
23 InSe with the relative small in-plane electron mass and high mobility can be an excellent  
24 platform to study the low-dimensional physics including quantum dots. Further  
25 development of this technology paves the way toward quantized transport and the  
26 manipulation of the single charge carriers in the electrostatic confinement.  
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## 34 **Acknowledgments**

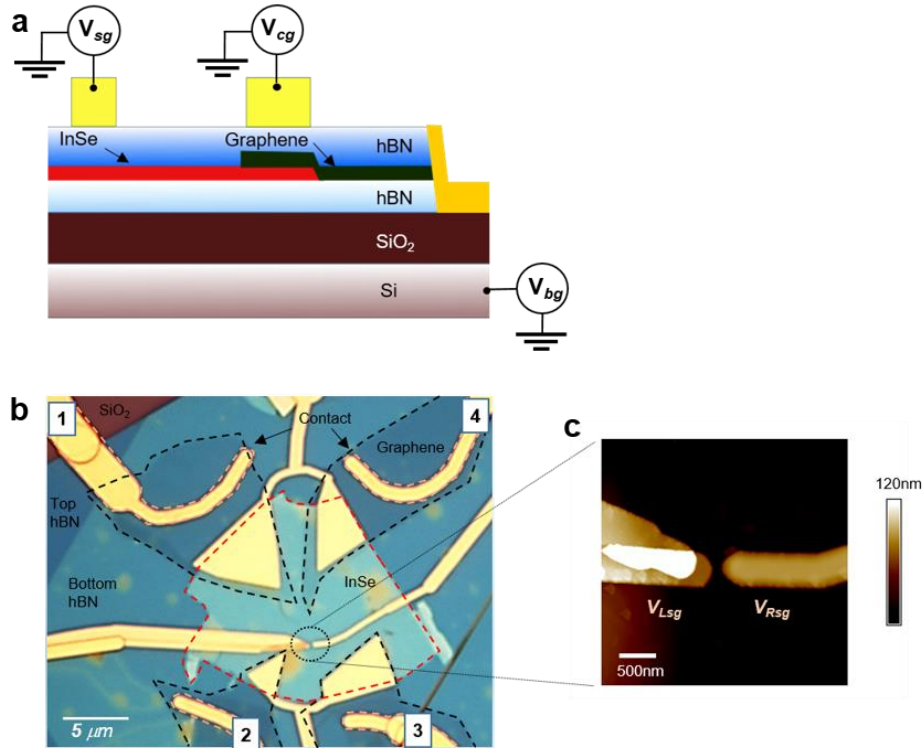
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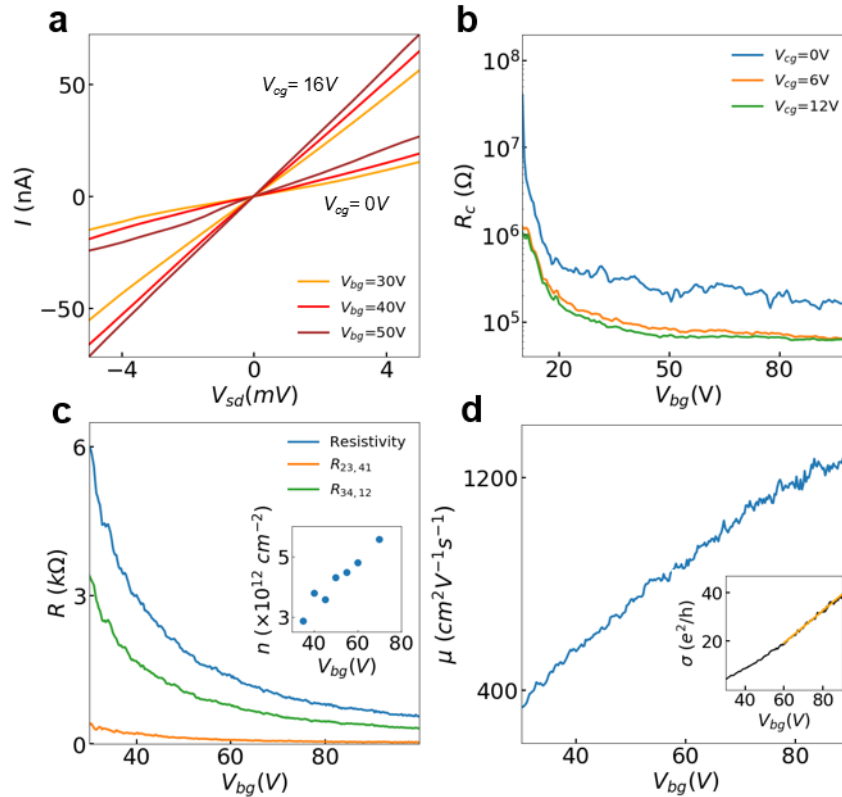
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**Figure 1. InSe van der Waals heterostructure.** (a) Cross-sectional schematic of an InSe-based field effect device. (b) Optical microscope image of a device. Red and black dashed lines outline the InSe flake and graphene contacts, respectively. The scale bar is 5  $\mu\text{m}$ . (c) Atomic force microscope image of the split-gate ( $V_{Lsg}$  and  $V_{Rsg}$ ) with a gap of 120 nm. The scale bar is 500 nm.



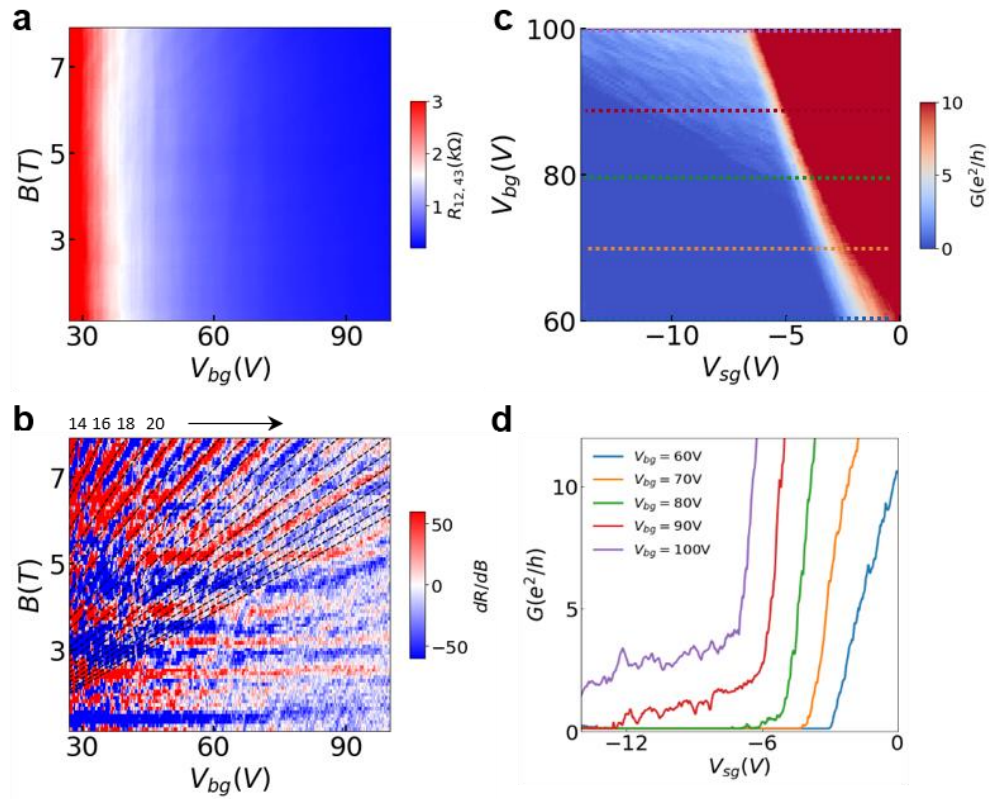


**Figure 2. Transport properties of few-layer InSe at  $T = 1.8\text{K}$**  (a)  $I$ - $V_{sd}$  characteristic at  $V_{bg} = 30, 40$  and  $50\text{V}$  when  $V_{cg} = 0$  and  $16\text{V}$ . (b) Contact resistance as a function of  $V_{bg}$  at  $V_{cg} = 0, 6$  and  $12\text{V}$ . (c) Resistivity as a function of  $V_{bg}$  by using the van der Pauw method at  $V_{cg} = 16\text{V}$ . Inset: Carrier density as a function of  $V_{bg}$  determined from SdH. (d) Hall mobility  $\mu$  as a function of  $V_{bg}$ . Inset: The conductivity as a function of  $V_{bg}$ . The linear fitting (orange) is performed to extract the field-effect mobility,  $\sim 2,300\text{ cm}^2/\text{Vs}$ .



**Figure 3. Magnetotransport and constriction transport in few-layer InSe.**

(a) Resistance and (b) differential resistance as a function of  $B$  and  $V_{bg}$  at constant  $V_{cg} = 16V$  and  $T = 1.8K$ . Each dashed line represents an even filling factor. (c) Conductance as a function of  $V_{bg}$  and  $V_{sg}$ . (d) Line traces from (c) at  $V_{bg} = 60, 70, 80, 90$  and  $100V$ .



**Figure 4.** (a) Two-terminal pinch off curves as a function  $V_{Rsg}$  at  $V_{bg} = 82.5V$  and  $V_{Lsg} = 10V$ . Due to localized states, several resonance peaks appear close to pinch off. (b) Measured AC current as a function of  $V_{sd}$  and  $V_{Rsg}$  with a  $250 \mu V$  AC excitation.

