Bidirectional Partial Power Converter Interface for Energy Storage Systems to Provide Peak Shaving in Grid-Tied PV Plants

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Abstract—The ever growing participation of modern renewable resources in electric markets has shaken the paradigm of generation-demand constant match. Most modern renewables add intermittent behaviour and high variability to electric markets, forcing other renewables and themselves to perform power curtailment and/or having extra generating units connected to the network to compensate power, voltage and frequency variations. In order to handle this scenario, Energy Storage Systems (ESSs) have risen as enabling technologies capable to provide backup energy to compensate power, voltage and frequency fluctuations and, at the same time, offer additional benefits as ancillary services, peak shaving, load shifting, base load generation, etc. This paper presents a novel bidirectional Partial Power Converter (PPC), as an interface between a Battery ESS (BESS) and a gridtied Photovoltaic (PV) plant. To obtain a better understanding of the converter, its mathematical model is presented and its operation modes are explained. The main purpose of this configuration is to provide peak shaving capability to a grid-tied PV plant, while providing a high efficiency BESS. Simulation results show the operation of the full system (grid-tied PV plant and BESS), performing peak shaving under a step-down and up in solar irradiation.

I. INTRODUCTION

Wind and PV energy have been at the forefront of renewables integration [1]. During the last decades electric markets have experience an immense growth in participation of (traditional and modern) renewable resources, where modern renewables have reached an impressive 10.2% [2]. This has been mostly motivated by government politics, environmental concerns and fossil fuel depletion [3].

High variability and intermittency of modern renewables have set a virtual limit to renewables participation share in electric markets, since highly variable systems require back-up energy generation [4]–[6]. Nowadays, the mainstream solution to deal with generation-demand mismatches is to have spinning-reserves [5], i.e. sets of back up fast-response costly fossil fuel based generating systems, which must be kept operating idle or at low power level.

ESSs have been vastly researched as an alternative to deal with generation-demand mismatch and provide additional services [6]–[10]. In [6], the addition of ESSs to Wind farms in order to perform peak shaving is analysed. Where an ESS sizing strategy based on Homogeneous Markov Chain, considering wind-storage reliability and increasing the income,

is proposed. In [7], the addition of BESS to perform peak shaving in a grid-tied network composed of several loads and PV generation is proposed. The sizing of the BESS considers different pricing strategies, probabilistic neural network fore-casting of the behaviour of the load and PV generation. In [8], [9], the addition of super capacitor based ESS is considered to perform global maximum power point tracking in a central inverter PV plant, while complying grid code restrictions on maximum power variation per minute. The addition of a BESS to an islanded wind-diesel-loads power sysatem is analysed in [10], where the BESS provides peak shaving and frequency regulation capabilities.

A paramount part of adding ESSs to a system is choosing the proper Energy Storage Device (ESD) for the application among several different technologies, namely Pumped Hydro Storage, Compressed Air Energy Storage, Flywheel, Fuel Cell, Rechargeable Batteries, Super Capacitor, etc. There have been some studies comparing ESDs features, for instance, in [11] price, energy density, power density, specific power, specific energy, discharge/charge rate, life cycle, depth of discharge, lifespan, energy conversion efficiency, daily self-discharge rate, and ramp time of several ESDs, to provide uninterruptible power supply to data centres, are presented. A deeper comparison of ESDs is presented in [12], where several ESDs are numerically analysed presenting their specific energy, energy density, specific power, power density, efficiency, lifespan, life cycle, life cycle, daily self-discharge rate and scale, cost.

Efficiency is a topic of great importance when analysing power systems and even more when analysing ESDs, since its bidirectional power flow nature incurs in losses during both energy conversion processes (storing and releasing energy). In order to quantify those losses, table I summarises the energy conversion efficiency and daily self-discharge rate of several ESDs shown in [12].

To assess the overall efficiency of ESSs, not only the efficiency of ESDs must be considered, but also the efficiency of the interfacing power converter. Partial Power Converters (PPCs) have emerge as a higher efficiency alternative to traditional DC-DC Full Power Converters (FPCs), in the former only a part of the full system power is processed by the converter, reducing its size and losses compared to FPCs [13]. PPCs have been proposed for a broad variety of unidirectional

 TABLE I

 ESD ENERGY CONVERSION EFFICIENCY AND DAILY SELF-DISCHARGE

 [12]

ESS	Efficiency	Daily self-discharge
Lead-acid	63 - 90%	0.033 - 1.10%
Lithium-ion	70 - 100%	0.03 - 0.33%
Super capacitor	65-99%	0.46 - 40%
Flywheel	70 - 96%	24 - 100%
Pumped Hydro.	65-87%	0%
Compressed Air.	57-89%	0%

power flow applications, such as PV power plants [13]–[15], long LED arrays [16] and electric vehicle fast charging stations [17].

This work proposes a novel bidirectional partial power converter topology, as an interface to connect a ESD to a grid-tied PV plant, in order to provide peak shaving capability. Lithium-ion batteries were chosen as the ESD to be applied, since they present the highest efficiency range and the lowest daily self-discharge rate (according to [12]).

This paper is organised as follows: section II presents the proposed configuration, its models, Partial Power Converters equations and BESS sizing. The control scheme for the system is described in section III, the simulation results are explained in section IV and finally section V provides the conclusions of the work.

II. CONFIGURATION & MATHEMATICAL MODELS

The proposed configuration is shown in Fig. 1, where a BESS is connected to the dc-link of a central inverter PV plant. The battery pack is interfaced, to the dc-link of the central inverter configuration, through 5 interleaved PPCs. Each PPC is formed by 8 semiconductors (MOSFETs), a transformer and an inductance, connected as shown in the figure. The central inverter configuration is composed by an array of several PV modules and a single 2 Level Voltage Source Inverter (2LVSI) connected to the grid.

The mathematical model of PPC j $(j = \{1, ..., 5\})$ is described by equations (1) to (6). The first 3 equations correspond to voltage dynamics, namely voltage across the inductance L (v_{Lj}) , voltage across the top winding and its semiconductors (v_{fbpj}) and voltage in the bottom winding and its semiconductors (v_{fbsj}) . The latter 3 equations correspond to the current dynamics of PPC j, where i_{xj} , i_{bpj} and i_{ppcj} correspond respectively to the current through inductance L, the bypass current and partial power converter current.

The variables m_j , v_x and v_{pv} represent respectively the modulation index of PPC j ($m_j \in [0,1]$), the voltage in the terminals of the battery pack and the voltage across the dc-link. The parameters n_1 and n_2 correspond respectively to the number of turns in the primary (Fig. 1, top winding) and secondary (Fig. 1, bottom winding) of the transformer.

$$v_{Lj} = \left(\frac{n_2}{n_1 + n_2}\right) \cdot v_{pv} \cdot m_j - v_x \tag{1}$$

$$v_{fbpj} = \left(\frac{n_1}{n_1 + n_2}\right) \cdot v_{pv} \cdot m_j \tag{2}$$

$$v_{fbsj} = \left(\frac{n_2}{n_1 + n_2}\right) \cdot v_{pv} \cdot m_j \tag{3}$$

$$i_{xj} = \frac{1}{s \cdot L} \left(\left(\frac{n_2}{n_1 + n_2} \right) \cdot v_{pv} \cdot m_j - v_x \right)$$
(4)

$$i_{bpj} = \left(\frac{n_2}{n_1 + n_2}\right) \cdot m_j \cdot i_{xj} \tag{5}$$

$$i_{ppcj} = \left(\frac{n_1}{n_1 + n_2}\right) \cdot m_j \cdot i_{xj} \tag{6}$$

In order to increase the equivalent switching frequency and reduce the current ripple from and towards the battery pack and dc-link, 5 PPCs are interleaved and their PWM carriers are shifted in $\phi = 2\pi/n$, where n is the amount of interleaved PPCs ($\phi = 2\pi/5$ in this case).

The mathematical model of the 2LVSI grid currents in dq rotational reference frame $(i_{gd} \text{ and } i_{gq})$, in Laplace domain, is shown in equations (7) and (8). Where L_g , R_g , ω , v_{rd} , v_{rq} , v_{gd} and v_{gq} correspond respectively to the inductance and resistance of the filter, grid angular frequency, inverter voltages in dq rotational axes and grid voltages in dq rotational reference frame.

$$i_{gd} = \frac{1}{L_g \cdot s + R_g} \left(v_{rd} + L_g \cdot \omega \cdot i_{gq} - v_{gd} \right) \tag{7}$$

$$i_{gq} = \frac{1}{L_g \cdot s + R_g} \left(v_{rq} - L_g \cdot \omega \cdot i_{gd} - v_{gq} \right) \tag{8}$$

The mathematical model of the voltage across the dc-link capacitor C_{pv} (v_{pv}) is shown in equation (9). Where i_{pv} , i_{bp} , m_u , m_v , m_w , i_{gu} , i_{gv} , i_{gw} correspond to the PV plant output current, total bypass current (through all PPCs), modulation indexes per inverter phase and grid currents.

$$v_{pv} = \frac{1}{s \cdot C_{pv}} \left(i_{pv} + i_{bp} - m_u \cdot i_{gu} - m_v \cdot i_{gv} - m_w \cdot i_{gw} \right)$$
(9)

A. Partial Power Converters

In order for a power converter to be considered a PPC, the power processed by the converter must be lower than the input power. This relationship is called partial power ratio (k_{pr}) and is mathematically represented by the ratio between the PPC processed power and its input power [16]. Therefore, a power converter must comply with $k_{pr} < 1$ to be a PPC. Table II summarizes the equations governing the partiality of the proposed bidirectional PPC, for both power flow directions,

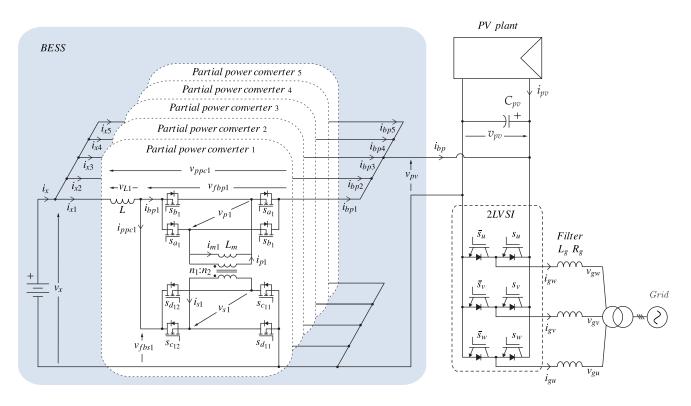


Fig. 1. Proposed configuration

TABLE II PARTIALITY EQUATIONS

Power flow towards BESS	Power flow towards dc-link
$k_{pr\;bess} = \frac{v_x \cdot i_{ppc}}{v_{pv} \cdot i_{bp}}$	$k_{pr\ dc} = \frac{v_{pv} \cdot i_{bp}}{v_x \cdot i_{ppc}}$
$G_{v \ bess} = \frac{v_x}{v_{pv}}$	$G_{v\ dc} = \frac{v_{pv}}{v_x}$
$\eta_{bess} = G_{v\ bess} + k_{pr\ bess}$	$\eta_{dc} = \frac{G_{v \ dc} \cdot k_{pr \ dc}}{G_{v \ dc} + k_{pr \ dc}}$

towards the BESS (noted by $_{bess}$) and towards the dc-link (noted by $_{dc}$).

Where G_v and η correspond respectively to the voltage gain and the efficiency of the converter.

B. Battery Energy Storage Sizing

A worst case scenario of providing 10% of the PV plant peak power (10% of 1 MW) during t = 10 minutes was considered. This time consideration corresponds to the available time for non-spinning reserves to respond after a contingency, having to be fully operational and able to provide 100% of its rated power to the electric network [18]. Hence, the BESS must be able to provide:

$$E_{BESS} = \frac{10\% \cdot P_{pv \, mpp}}{1000 \cdot 60} \cdot t \, [kWh] = 17[kWh]$$
(10)

Where $P_{pv mpp}$ is the maximum power the PV plant can generate and t is the time in minutes.

Due to computational limitations the peak shaving effect provided by the BESS is analysed during short time intervals, nevertheless the battery pack was designed to provide 100 kW during 10 minutes.

III. CONTROL

A conventional Voltage Oriented Control (VOC) scheme, composed by an outer dc-link voltage loop and two inner current loops (direct i_{gd} and quadrature i_{gq} grid currents), is applied to the 2LVSI of the central inverter configuration as shown in Fig. 2. Where v_{rd} and v_{rq} correspond respectively to the direct and quadrature inverter voltages. The dc-link voltage reference v_{pv}^* is generated by a traditional Perturb and Observe (P&O) Maximum Power Point Tracking algorithm.

Figure 3 shows the control scheme applied to each PPC. The leftmost block (PPC bypass current reference), which is explained through Fig. 4, generates the bypass current reference (i_{bp}^*) for each interleaved PPC. A PI controller processes the error between i_{bp}^* and the measurement of the bypass current in PPC j (i_{bpj}) , generating the modulation index, which is later passed to the PWM modulation block generating the signals \tilde{s}_{aj} , \tilde{s}_{bj} , \tilde{s}_{cj1} , \tilde{s}_{cj2} , \tilde{s}_{dj1} and \tilde{s}_{dj2} . Later depending on the power flow direction those signals are enabled (or disabled) by multiplying them by a signal named *boost* or its negated *boost* generating the gate signals s_{aj} , s_{bj} , s_{cj1} , s_{cj2} , s_{dj1} and s_{dj2} , as shown in Fig. 3. When power flows towards the dc-link *boost* = 1 ($\overline{boost} = 0$) the gate signals s_{aj} and s_{aj} are

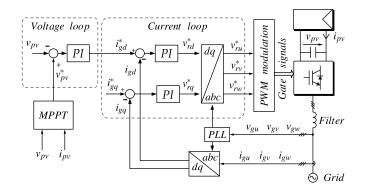


Fig. 2. Inversor control scheme

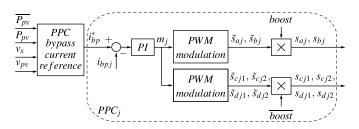


Fig. 3. Bidirectional PPC control scheme

TABLE III EST CURRENT REFERENCE VALUES

\hat{i}_{bn} 100 A i_{bn}	
i_{bp} 100 A i_{bp}	$\gamma -4A i_4 2A$
$i_{bp\alpha}$ 4 A \check{i}_b	100 4 1 4
$i_{bp\beta}$ $-1A$ i_{3}	$3A$ i_6 $-3A$

modulated, keeping $s_{cj1} = s_{cj2} = s_{dj1} = s_{dj2} = 0$. On the other hand, when the power flows towards the battery pack, boost is 0 ($\overline{boost} = 1$) and gate signals s_{cj1} , s_{cj2} , s_{dj1} and s_{dj2} are modulated, while $s_{aj} = s_{bj} = 0$.

PPC bypass current reference block (Fig. 3), operates according to the graph shown in Fig. 4. Where $\overline{P_{pv}}$, P_{pv} , n, $v_{nv}, v_x, \check{v}_x, \hat{v}_x$ correspond respectively to the filtered PV plant power, current PV plant power, number of interleaved PPCs, dc-link voltage, BESS current voltage, BESS minimum safety voltage (80% Depth of Discharge) and BESS maximum safety voltage (99% of Maximum battery pack voltage). When the quotient between $\lambda = (\overline{P_{pv}} - P_{pv})/(n \cdot v_{pv})$ is greater or equal to i_3 and $\check{v}_x < v_x$ the reference i_{bp}^* is given according to the purple curve (top right). If $\lambda \ge i_3$, but $v_x < \check{v}_x$, then $i_{bp}^* = 0$. On the other hand, when λ is lower than i_4 and $v_x < \hat{v}_x$, the reference is given by the pink curve (bottom left). If $\lambda < i_4$ but $\hat{v}_x < v_x$, then the reference i_{bp}^* is 0. In order to keep BESS voltage (v_x) within a certain range, a charging strategy was added to the bypass current reference generation algorithm, $i_{bp}^* = i_{bp\beta}$ when $i_6 \leq \lambda < i_5$ and $v_x < \hat{v}_x$.

The parameter used in Fig. 4 are shown in table III.

IV. SIMULATION RESULTS

The circuital model of the full system was implemented in PLECS.

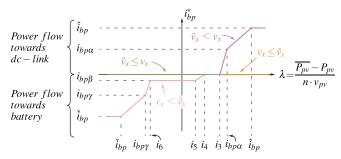


Fig. 4. PPC bypass current reference

TABLE IV CONFIGURATION PARAMETERS.

er STC condi				
$P_{pv \ mpp}$	1 MW			
$v_{pv \ ocv}$	970 V			
$i_{pv \ sc}$	1327 A			
$v_{pv \ mpp}$	796 V			
$i_{pv \ mpp}$	1256 A			
N_{sm}	21			
N_{ps}	140			
2LVSI & Grid				
C_{pv}	$4400 \ \mu F$			
v_{pv}	740 - 1000 V			
$v_{ac RMS}$	$440 V_{LL RMS}$			
L_{q}	0.25 mH			
	50 Hz			
f_{sw}	$5 \ kHz$			
Battery pack				
C_{cell}	6 Ah			
$C_{rate out}$	20 C			
$C_{rate\ in}$	19 C			
N_{cells}	1211			
\hat{v}_x	649 V			
\check{v}_x	480 V			
PPCs PPC inductance L 1 mH				
L	1 mH			
L_m	1 mH			
$n_1: n_2$	2:1			
f_{bb}	$100 \ kHz$			
T_s	$1 \ \mu s$			
	$\begin{array}{c} P_{pv\ mpp} \\ v_{pv\ ocv} \\ i_{pv\ sc} \\ v_{pv\ ocv} \\ i_{pv\ sc} \\ v_{pv\ sc} \\ N_{sm} \\ N_{ps} \\ \hline \\ \hline \\ \hline \\ c_{rot} \\ c_{pv} \\ v_{ac\ RMS} \\ L_g \\ f_g \\ f_{g} \\ f_{g} \\ f_{sw} \\ \hline \\ \hline \\ \hline \\ \\ C_{rate\ out} \\ C_{rate\ in} \\ N_{cells} \\ \hline \\ \hline \\ \\ \hline \\ c_{x} \\ \hline \\ \\ \hline \\ c_{x} \\ \hline \\ \\ \hline \\ \\ \hline \\ \\ c_{x} \\ \hline \\ \\ \hline \\ \\ \hline \\ \\ \\ \\ \hline \\ \\ \\ \hline \\$			

Table IV, shows the parameters applied in the simulation. A 1 MW PV plant was considered, based on Canadian Solar CS6X-340M-FG PV modules, a tailored battery pack formed by muRata 6Ah LiB battery cells was considered.

In order to validate the configuration two steps in solar irradiation were applied, while keeping temperature constant at $25^{\circ}C$. The first step consists in a step-down in solar irradiation from 1000 to $700W/m^2$ at 0.5[s], the second step corresponds to a step-up from 700 to $1000 W/m^2$ at 1.3[s]. The results of the test are shown in the following charts. Fig. 5 shows the solar irradiation (G), sub-plot (b) displays the PV plant output current (i_{pv}) , sub-plot (c) shows the dc-link voltage (v_{pv}) and its reference (v_{pv}^*) , sub-plot (d) shows the grid currents in rotational $(i_{gd}$ and $i_{gq})$ and stationary $(i_{gu}, i_{gv}$ and $i_{gw})$ reference frame.

The step-down in solar irradiation (G) at 0.5 s is reflected in a step-down in PV plant output current (i_{pv}) . The instan-

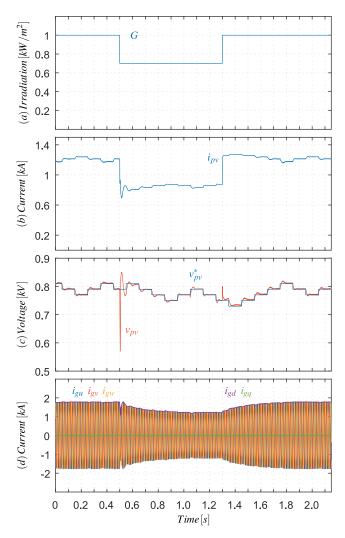


Fig. 5. PV plant and grid side converter signals: (a) Solar irradiation (G), (b) PV plant output current (i_{pv}) , (c) 2LVSI dc-link voltage (v_{pv}) and its reference (v_{pv}^*) , (d) 2LVSI grid currents in stationary $(i_{gu}, i_{gv}$ and $i_{gw})$ and rotational $(i_{gd}$ and $i_{gq})$ reference frame.

taneous mismatch between i_{pv} and the grid currents (i_{gu}, i_{gv}) and i_{gw}) causes a fast decrease of the dc-link voltage (v_{pv}) at t = 0.5 s, which is instantaneously corrected by the 2LVSI controller by decreasing the grid currents. The later increase and smooth decrease of grid currents (from 0.5 to 1.2 s) is explained by the extra power injected to the dc-link by the BESS.

The step-up in solar irradiation (G) at 1.3 s generates a step-up in the PV plant output current (i_{pv}) . The instantaneous mismatch between the grid currents and the PV plant output current generates a sudden increase in the dc-link voltage (v_{pv}) , which is corrected by the inverter control scheme by increasing the grid currents. The smooth increase of the grid currents, despite the step-up in P_{pv} (from 1.3 to 2.0 s), is due to the power being stored in the BESS.

It must be notice that the P&O algorithm keeps tracking the MPP dc-link voltage, even when the BESS is releasing or storing energy.

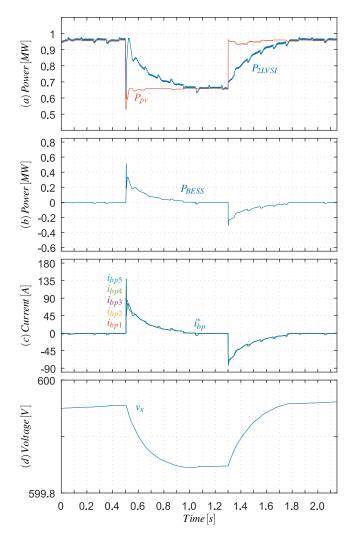


Fig. 6. System power and BESS signals: (a) Power injected to the grid (P_{2LVSI}) and power provided by the PV plant (P_{pv}) , (b) Power injected to the dc-link by the BESS (P_{BESS}) , (c) PPCs bypass currents $(i_{bp1}, i_{bp2}, i_{bp3}, i_{bp4} \text{ and } i_{bp5})$ and its reference (i_{bp}^*) , (d) BESS terminal voltage (v_x) .

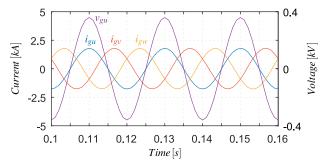


Fig. 7. Zoomed grid signals: grid currents $(i_{gu}, i_{gv} \text{ and } i_{gw})$ and phase u grid voltage (v_{gu}) .

BESS, PV plant output power and 2LVSI output power signals are shown in Fig. 6. Sub-plot (a) shows the PV plant output power (P_{pv}) and the power injected to the grid by the inverter (P_{2LVSI}). The power released (positive) and stored (negative) by the BESS (P_{BESS}) is displayed in sub-plot (b). Sub-plot (c) shows the bypass currents $(i_{bpj}, j = \{1, ..., 5\})$ and its reference (i_{bp}^*) . The terminal voltage across the battery pack (v_x) is shown in sub-plot (d).

The step-down in solar irradiation at 0.5 s causes a positive difference between the average $(\overline{P_{pv}})$ and the instantaneous (P_{pv}) PV plant output power (hence $\lambda > 0$), commanding the BESS to inject current (power) to the dc-link (from 0.5 to 1.2 s). The power injected from the BESS mitigates the step-down in full system output power (P_{2LVSI}) , generating a smooth transition. As the average PV plant output power decreases, the PPCs bypass current reference (i_{bp}^*) decreases proportionally until reaching zero reference. The power drawn from the BESS towards the dc-link is reflected in a decrease of the battery pack terminal voltage (v_x) .

The step-up in solar irradiation at 1.3 s generates a negative difference between $\overline{P_{pv}}$ and P_{pv} ($\lambda < 0$), hence commanding the BESS to take (store) energy from the dc-link, while smoothing the total system output power curve (from 1.3 to 2 s). The negative value in BESS power (P_{BESS}) represents stored energy. As P_{BESS} smoothly increases (towards zero) proportionally increases the bypass current reference (i_{bp}^*) . The injection of power from the dc-link towards the BESS is reflected as an increase of the battery pack terminal voltage (v_x) .

V. CONCLUSION

This paper presents a bidirectional PPC as an interface to merge a battery pack to a grid-tied PV plant, in order to provide peak shaving capability. The BESS enables the PV system to store (release) exceeding (lacking) PV power, providing peak shaved power to the grid.

A full description and model of the system were given, providing a deeper understanding on how this bidirectional PPC operates. A tailored control strategy to manage the power flow between the battery pack and dc-link was tested, enabling the full system to inject peak shaved power to the grid. The standard P&O MPPT algorithm was able to operate normally, even when power was being drawn or injected to the dc-link by the BESS.

The proposed BESS configuration and control scheme were designed to be a plug-in solution for existing grid-tied PV plants, lacking peak shaving capability. Moreover, a small modification in the control strategy will allow BESS to mitigate (avoid) PV plants power curtailment, required by grid operators to compensate grid frequency variations.

As a possible future work the authors propose the possibility to perform peak shaving and short term ancillary services without adding additional energy storage.

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