

# Indirect Predictive Control Strategy with Fixed Switching Frequency for a Direct Matrix Converter

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**Abstract**—The direct matrix converter has a large number of available switching states which implies that the implementation of predictive control techniques in this converter requires high computational cost while an adequate selection of weighting factors in order to control both input and output sides. In this paper, an indirect model predictive current control strategy is proposed in order to simplify the computational cost while avoiding the use of weighting factors. The method is based on the fictitious *dc*-link concept, which has been used in the past for the classical modulation and control techniques of the direct matrix converter. The proposal is enhanced with a fixed switching predictive strategy in order to improve the performance of the full system. Simulated results confirm the feasibility of the proposal demonstrating that it is an alternative to classical predictive control strategies for the direct matrix converter.

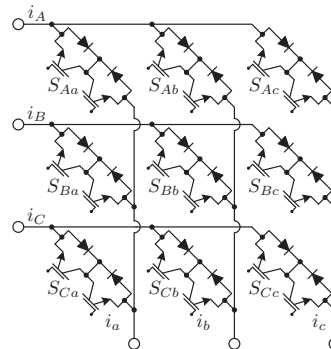


Fig. 1. Power circuit of the direct matrix converter.

## NOMENCLATURE

$\mathbf{i}_s$	Source current	$[i_{sA} \ i_{sB} \ i_{sC}]^T$
$\mathbf{v}_s$	Source voltage	$[v_{sA} \ v_{sB} \ v_{sC}]^T$
$\mathbf{i}_i$	Input current	$[i_A \ i_B \ i_C]^T$
$\mathbf{v}_i$	Input voltage	$[v_A \ v_B \ v_C]^T$
$i_{dc}$	Fictitious <i>dc</i> -link current	
$v_{dc}$	Fictitious <i>dc</i> -link voltage	
$\mathbf{i}_o$	Load current	$[i_a \ i_b \ i_c]^T$
$\mathbf{v}_o$	Load voltage	$[v_a \ v_b \ v_c]^T$
$\mathbf{i}^*$	Load current reference	$[i_a^* \ i_b^* \ i_c^*]^T$
$C_f$	Input filter capacitor	
$L_f$	Input filter inductor	
$R_f$	Input filter resistor	
$R$	Load resistance	
$L$	Load inductance	

## I. INTRODUCTION

The direct matrix converter (DMC), shown in Fig. 1, has been subject of research in the last decades because in comparison with the traditional back-to-back converter, it features some advantages in terms of power densities and capability to operate in harsh pressures and temperatures [1], [2]. The DMC presents sinusoidal input and output currents as well as bidirectional power flow and adjustable input displacement power factor [2], [3]. There are several modulation and control techniques that have been applied to this converter being the most popular Venturini, Pulse Width Modulation (PWM), Space Vector Modulation (SVM) as well as Direct Torque Control (DTC) and Model Predictive Control (MPC) [3].

MPC uses the mathematical model of the system to predict for each valid switching state of the converter the performance

of the variables to be controlled at every sampling time. These predictions are compared with a given reference in a cost function and, the switching state that generates the minimal error between the prediction and the reference, is the one selected to be applied in the next sampling instant [4].

Despite the several progress of MPC for power converters, there are still some issues that are considered as an open topic for research. One of these issues is the correct selection of weighting factors when there are several control objectives. This issue is very relevant because it has a significant effect on the system performance. In most of the cases, this selection is done by using empirical process but there are some papers that offer some guidelines for the optimal weighting factor selection [5]–[9] nevertheless, most of them are complex solutions and require high computation cost.

One of the common problems with implementing MPC instead of traditional modulation methods is the resulting variable switching frequency. The switching configuration is chosen every sampling period, which can lead to high ripple currents, affecting the performance of the system and making filter design challenging. This variable switching frequency issue has recently been considered by some researchers and solutions have been proposed. Work has been presented describing techniques which combine MPC and PWM techniques, for example a motor drive with fast dynamics for a permanent magnet synchronous machine [10]. Another example is a multi-objective modulated MPC method was suggested for a buck converter application, a technique which requires adaptive weighting factors which are assigned depending on

the error in both the inductor current and capacitor voltage [11]. In many applications of Modulated MPC (M2PC) one cost function is used to select the vectors and duty cycles in every sampling period. Once calculated the duty cycles are then applied in the following switching period, a method which leads to the possibility of emulating SVM whilst by using a MPC implementation [12], [13]. These methods give the combined advantages of MPC in terms of fast transient response, multi-objective control and the ability to cope with non-linearities whilst also allowing the control to minimise control variable ripple and fix the switching frequency to allow the realistic design of filter components.

It is possible to apply these M2PC methods for the modulation and control of matrix converters [14]–[16]. The combination of MPC and SVM gives fixed switching frequency operation as well as using all twenty-seven available switching states [14]. This use of all the switching states is something that is not always possible with SVM. The disadvantage of this approach can be the computational cost of having to evaluate all twenty-seven switching states twice in every sampling period.

To solve issues such as computational cost, weighting factor selections and the operation at variable switching frequency, this paper proposes an indirect model predictive current control strategy working at fixed switching frequency. The idea consists in to emulate the DMC as a two stage converter linked by a fictitious  $dc$ -link allowing a separated and parallel control of both input and output stages, avoiding the use of weighting factors and choosing into the cost function a set of optimal vectors and their respective duty cycles to be applied to the converter by using a predefined switching pattern.

## II. MATHEMATICAL MODEL OF THE DMC

The traditional  $ac/ac$  DMC circuit is shown in Fig. 1 and comprises of bidirectional switches, which directly connect any input line with any output line without including any  $dc$ -link energy storage components. There is usually an  $ac$  line input filter included in the converter to reduce the magnitude of switching frequency components in the input currents and prevent voltage overshoots during rapid switching transients. DMCs have some operation constrains: the output current cannot be interrupted due to the usually inductive nature of the load and the operation of the switches cannot cause short-circuits of the input lines due to the input filter capacitance. These two operational limitations can be expressed as:

$$S_{Ay} + S_{By} + S_{Cy} = 1, \quad \forall y = a, b, c \quad (1)$$

The mathematical model of the DMC is defined as:

$$\mathbf{v}_o = \mathbf{T} \mathbf{v}_i \quad (2)$$

$$\mathbf{i}_i = \mathbf{T}^T \mathbf{i}_o \quad (3)$$

where  $\mathbf{T}$  is the instantaneous transfer matrix defined as:

$$\mathbf{T} = \begin{bmatrix} S_{Aa} & S_{Ba} & S_{Ca} \\ S_{Ab} & S_{Bb} & S_{Cb} \\ S_{Ac} & S_{Bc} & S_{Cc} \end{bmatrix} \quad (4)$$

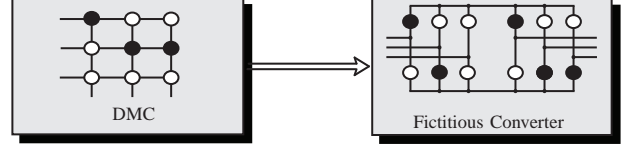


Fig. 2. Representation of the fictitious  $dc$ -link concept for the DMC.

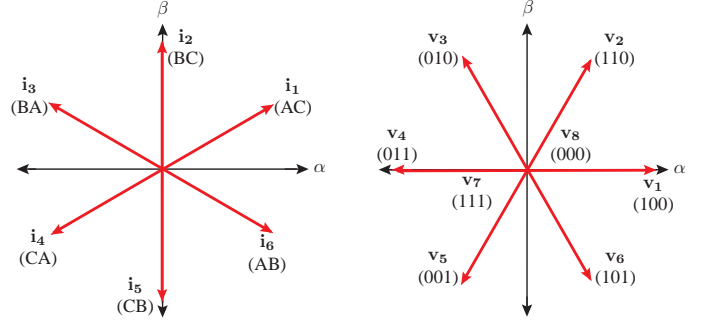


Fig. 3. Left: current space vectors for the fictitious rectifier, right: voltage space vectors for the fictitious inverter.

TABLE I  
VALID SWITCHING STATE ON THE FICTITIOUS RECTIFIER

#	$S_{r1}$	$S_{r2}$	$S_{r3}$	$S_{r4}$	$S_{r5}$	$S_{r6}$	$i_A$	$i_B$	$i_C$	$v_{dc}$
1	1	1	0	0	0	0	$i_{dc}$	0	$-i_{dc}$	$v_{AC}$
2	0	1	1	0	0	0	0	$i_{dc}$	$-i_{dc}$	$v_{BC}$
3	0	0	1	1	0	0	$-i_{dc}$	$i_{dc}$	0	$-v_{AB}$
4	0	0	0	1	1	0	$-i_{dc}$	0	$i_{dc}$	$-v_{AC}$
5	0	0	0	0	1	1	0	$-i_{dc}$	$i_{dc}$	$-v_{BC}$
6	1	0	0	0	0	1	$i_{dc}$	$-i_{dc}$	0	$v_{AB}$

TABLE II  
VALID SWITCHING STATE ON THE FICTITIOUS INVERTER

#	$S_{i1}$	$S_{i2}$	$S_{i3}$	$S_{i4}$	$S_{i5}$	$S_{i6}$	$v_{ab}$	$v_{bc}$	$v_{ca}$	$i_{dc}$
1	1	1	0	0	0	1	$v_{dc}$	0	$-v_{dc}$	$i_a$
2	1	1	1	0	0	0	0	$v_{dc}$	$-v_{dc}$	$i_a+i_b$
3	0	1	1	1	0	0	$-v_{dc}$	$v_{dc}$	0	$i_b$
4	0	0	1	1	1	0	$-v_{dc}$	0	$v_{dc}$	$i_b+i_c$
5	0	0	0	1	1	1	0	$-v_{dc}$	$v_{dc}$	$i_c$
6	1	0	0	0	1	1	$v_{dc}$	$-v_{dc}$	0	$i_a+i_c$
7	1	0	1	0	1	0	0	0	0	0
8	0	1	0	1	0	1	0	0	0	0

Some techniques use the concept of fictitious  $dc$ -link in order to simplify the control of the DMC, which divide the converter in a current source rectifier and a voltage source inverter linked by a fictitious  $dc$ -link, Fig. 2. The rectifier have associated six active current space vectors shown in Fig. 3 (left) and Table I. The inverter have associated eight voltage space vectors, represented in Fig. 3 (right) and Table II. The technique modulates both converters separately, but considering the relationship between both stages.

## III. INDIRECT MODEL PREDICTIVE CONTROL METHOD FOR THE DMC WITH FIXED SWITCHING FREQUENCY

In [15], [16] is presented a M2PC technique for a DMC feeding an induction machine where both input and output stages are controlled together by considering a predictive

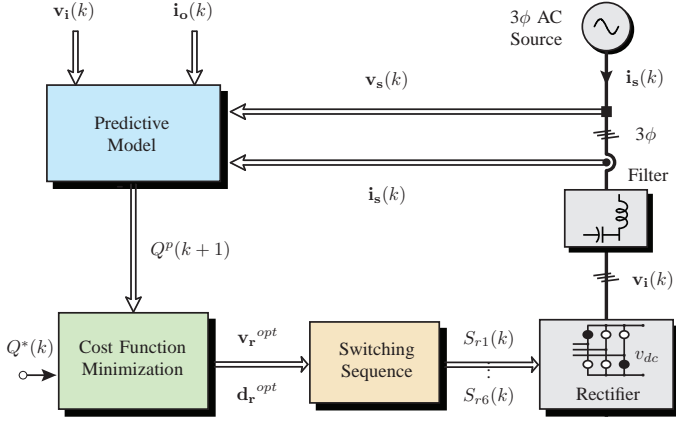


Fig. 4. Indirect predictive control strategy for the fictitious rectifier.

model of the instantaneous reactive input power and a predictive model of the load currents. These predictions are compared with their respective references in a single cost function (so it is necessary also to consider a weighting factor in order to provide more priority to one of the controlled variables). At every sampling instant, three optimal active and zero vectors are chosen which are applied to the converter. In this method two main issues are observed: first, it is necessary the correct selection of a suitable weighting factor value in order to prioritise for the control of the load current or the instantaneous reactive input power and second, as the full converter control is considered, a large amount of available switching states is considered. The idea of this proposal is to separate the control of both input and output fictitious stages of the converter in order to avoid complex and large calculations and as well simplify the controller while avoiding the use of weighting factors.

#### A. Control of the Rectifier

The mathematical model of the rectifier stage has inputs representing the input phase voltages  $\mathbf{v}_i$  and fictitious  $dc$ -link current  $i_{dc}$  and outputs representing the  $dc$ -link voltage  $v_{dc}$  and input currents  $\mathbf{i}_i$ . The relationship between these variables is shown in eq. (5) and eq. (6):

$$v_{dc} = \begin{bmatrix} S_{r1} - S_{r4} & S_{r3} - S_{r6} & S_{r5} - S_{r2} \end{bmatrix} \mathbf{v}_i \quad (5)$$

$$\mathbf{i}_i = \begin{bmatrix} S_{r1} - S_{r4} \\ S_{r3} - S_{r6} \\ S_{r5} - S_{r2} \end{bmatrix} i_{dc} \quad (6)$$

The rectifier stage has six active current space vectors, as shown in Fig. 3 (left) and detailed in Table I, which are then used to derive the control structure shown in Fig. 4. In the same way as for the MPC strategy of the traditional DMC it is necessary to include the input current as well as the output voltage for the model:

$$\frac{d\mathbf{i}_s}{dt} = \frac{1}{L_f}(\mathbf{v}_s - \mathbf{v}_i) - \frac{R_f}{L_f}\mathbf{i}_s \quad (7)$$

$$\frac{d\mathbf{v}_i}{dt} = \frac{1}{C_f}(\mathbf{i}_s - \mathbf{i}_i) \quad (8)$$

All predictive controllers are implemented in discrete time, therefore one has to be derived a discrete time model in order to describe the power converter converter. The cost function  $g_r$  can be defined by following the method described and validated in [17] using the input current and output voltage predictions:

$$g_r = [v_{s\alpha}(k+1)i_{s\beta}(k+1) - v_{s\beta}(k+1)i_{s\alpha}(k+1)]^2 \quad (9)$$

Using the vector diagram of the input currents shown in Fig. 3 (left) the two active current vectors  $\mathbf{i}_i$  can be identified and located in one of the six sectors. At every sampling instant  $T_s$ , the cost function  $g_r$ , is evaluated for every pair of input current vectors. This process results two cost functions being generated, one for the input current vector  $g_{r1}$  and other for the adjacent current vector  $g_{r2}$ . The duty cycles to be applied in the next switching period can then be calculated from these cost functions. This process assumes that the duty cycles are inversely proportional to the value of the cost function value:

$$\begin{aligned} d_{r1} &= K_r/g_{r1} \\ d_{r2} &= K_r/g_{r2} \\ d_{r1} + d_{r2} &= 1 \end{aligned} \quad (10)$$

where  $K_r$  is a constant to be determined. By combining these two cost equations the overall cost function can be defined:

$$g_{rec} = d_{r1}g_{r1} + d_{r2}g_{r2} \quad (11)$$

This cost function is evaluated in each sampling time for all possible vector combinations and the states which minimise the cost function  $g_{rec}$  are applied in the switching next period. The relative time that each vector is applied can then be simply calculated from the duty cycles:

$$\begin{aligned} t_{r1} &= d_{r1}T_s \\ t_{r2} &= d_{r2}T_s \end{aligned} \quad (12)$$

#### B. Control of the Inverter

The control diagram of this stage is represented in Fig. 5. For the mathematical model of the inverter it is considered the

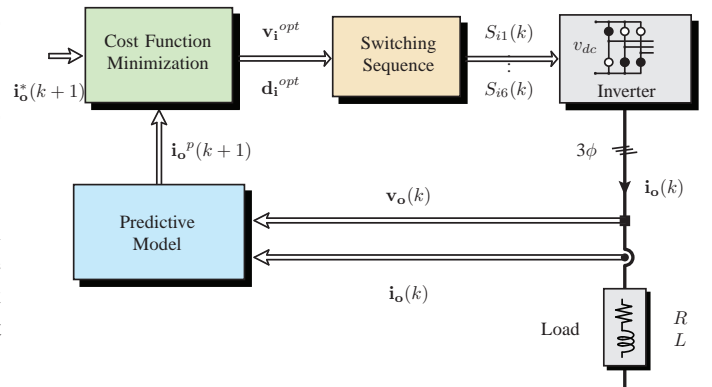


Fig. 5. Indirect predictive control strategy for the fictitious inverter.

output currents  $\mathbf{i}$  and fictitious  $dc$ -link voltage  $v_{dc}$  as inputs, and the fictitious  $dc$ -link current  $i_{dc}$  and the output voltage  $\mathbf{v}_o$  as outputs. This can be seen in equations (13) and (14), respectively:

$$i_{dc} = \begin{bmatrix} S_{i1} & S_{i3} & S_{i5} \end{bmatrix} \mathbf{i}_o \quad (13)$$

$$\mathbf{v}_o = \begin{bmatrix} S_{i1} - S_{i4} \\ S_{i3} - S_{i6} \\ S_{i5} - S_{i2} \end{bmatrix} v_{dc} \quad (14)$$

For a passive  $RL$  load the mathematical model of the load can be given as:

$$\mathbf{v}_o = L \frac{d\mathbf{i}_o}{dt} + R\mathbf{i}_o \quad (15)$$

Using these identities a prediction model of the converter output can be found using a forward Euler approximation:

$$\mathbf{i}_o(k+1) = c_1 \mathbf{v}_o(k) + c_2 \mathbf{i}_o(k) \quad (16)$$

Constants  $c_1 = T_s/L$  and  $c_2 = 1 - RT_s/L$  depend on the load parameters and  $T_s$ . The associated cost function  $g_i$  for the output stage in  $\alpha$ - $\beta$  plane is defined as:

$$g_i = (i_{\alpha}^* - i_{\alpha}(k+1))^2 + (i_{\beta}^* - i_{\beta}(k+1))^2 \quad (17)$$

The six output voltage sectors are shown in Fig. 3 (right). At each sampling time the cost function  $g_i$  can be calculated for every possible combination of vectors. For the output side of the converter it is also possible to use the zero vectors, therefore three cost functions  $g_{i0}$ ,  $g_{i1}$  and  $g_{i2}$  are calculated for each sector. These cost functions can then be used to calculate the switch duty cycles, again assuming that these duty cycles are inversely proportional to the value of the relevant cost function:

$$\begin{aligned} d_{i0} &= K_i/g_{i0} \\ d_{i1} &= K_i/g_{i1} \\ d_{i2} &= K_i/g_{i2} \\ d_{i0} + d_{i1} + d_{i2} &= 1 \end{aligned} \quad (18)$$

where  $K_i$  is a constant to be determined. By combining these two cost equations the overall cost function can again be defined:

$$g_{inv} = d_{i1}g_{i1} + d_{i2}g_{i2} \quad (19)$$

This cost function is evaluated in each sampling time for all possible vector combinations and the states which minimise the cost function  $g_{inv}$  are applied in the next switching period. The relative time that each vector is applied can then be simply calculated from the duty cycles:

$$\begin{aligned} t_{i0} &= d_{i0}T_s \\ t_{i1} &= d_{i1}T_s \\ t_{i2} &= d_{i2}T_s \end{aligned} \quad (20)$$

After obtaining the duty cycles and selecting the optimal vectors to be applied in both the rectifier and inverter, a switching pattern procedure, such as the one shown in Fig. 6, is adopted with the goal of applying the optimal vectors [18].

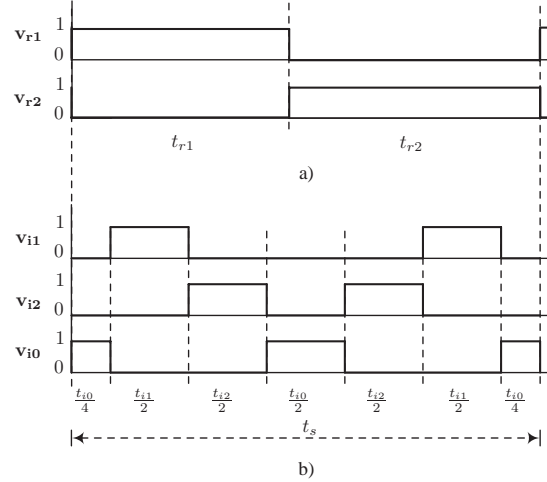


Fig. 6. Switching pattern: a) for the rectifier side; b) for the inverter side.

### C. Relationship between the fictitious converter and the DMC

As it is necessary to apply the switching signals to the switches of the DMC, it is required to adapt the switching states of both input and output fictitious stages to the real one. This is given by the relationship between input and output stages and described as follows. As indicated in eq. (2), the relationship between the input voltage  $\mathbf{v}_i$  and load voltage  $\mathbf{v}_o$  depend on the state of the switching given by matrix  $\mathbf{T}$ . Based on the fictitious definition, the load voltage  $\mathbf{v}_o$  is given as indicated in eq. (14). At the same time, the fictitious  $dc$ -link voltage  $v_{dc}$  is given by eq. (5). In summary,

$$\mathbf{v}_o = \begin{bmatrix} S_{i1} - S_{i4} \\ S_{i3} - S_{i6} \\ S_{i5} - S_{i2} \end{bmatrix} \begin{bmatrix} S_{r1} - S_{r4} & S_{r3} - S_{r6} & S_{r5} - S_{r2} \end{bmatrix} \mathbf{v}_i \quad (21)$$

and thus the relationship between the switches of the DMC and fictitious converter is given as:

$$\begin{bmatrix} S_{Aa} \\ S_{Ba} \\ S_{Ca} \\ S_{Ab} \\ S_{Bb} \\ S_{Cb} \\ S_{Ac} \\ S_{Bc} \\ S_{Cc} \end{bmatrix} = \begin{bmatrix} (S_{i1} - S_{i4})(S_{r1} - S_{r4}) \\ (S_{i1} - S_{i4})(S_{r3} - S_{r6}) \\ (S_{i1} - S_{i4})(S_{r5} - S_{r2}) \\ (S_{i3} - S_{i6})(S_{r1} - S_{r4}) \\ (S_{i3} - S_{i6})(S_{r3} - S_{r6}) \\ (S_{i3} - S_{i6})(S_{r5} - S_{r2}) \\ (S_{i5} - S_{i2})(S_{r1} - S_{r4}) \\ (S_{i5} - S_{i2})(S_{r3} - S_{r6}) \\ (S_{i5} - S_{i2})(S_{r5} - S_{r2}) \end{bmatrix} \quad (22)$$

## IV. RESULTS

In order to validate the effectiveness of the proposed method, simulation results in Matlab-Simulink were carried out in both steady and transient conditions. The simulation parameters are shown in Annexes - Table III.

### A. Results in Steady State

Fig. 7 and Fig. 8 show simulations results in steady state for the proposed indirect predictive controller.

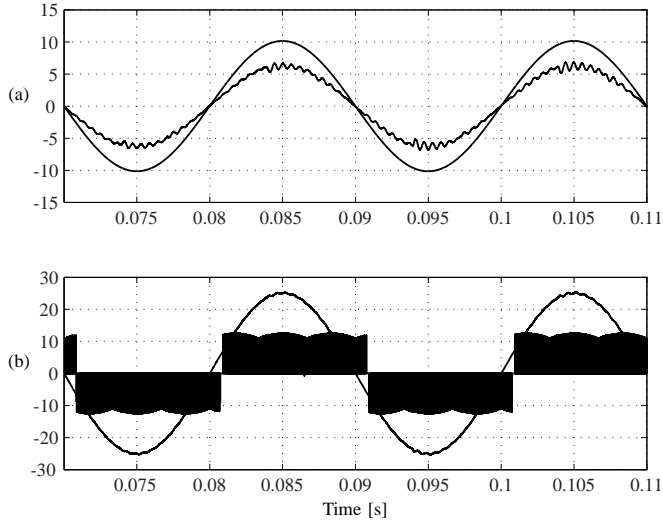


Fig. 7. Simulation results of the proposed method in steady state: (a) source voltage  $v_{sA}$  [V/25] and source current  $i_{sA}$  [A]; (b) capacitor voltage  $v_A$  [V/10] and input current  $i_A$  [A].

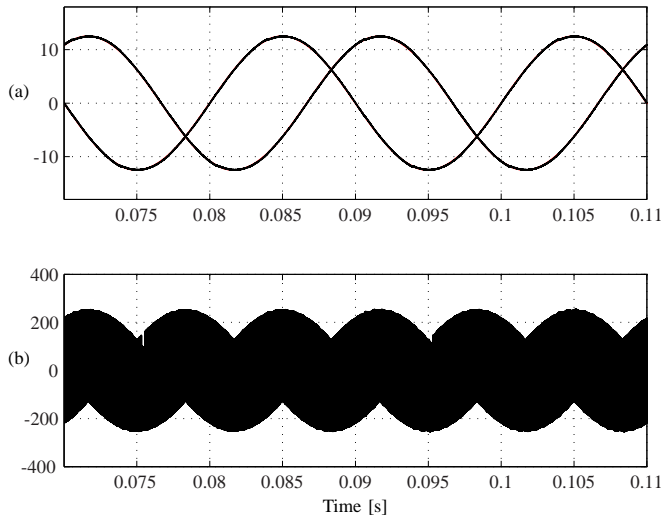


Fig. 8. Simulation results of the proposed method in steady state: (a) load currents  $i_o$  [A] and references  $i_o^*$  [A]; (b) load voltage  $v_a$  [V].

In Fig. 7(a) is observed a source current  $i_{sA}$  in phase with to its respective source voltage  $v_{sA}$  with an almost sinusoidal waveform and a THD of 5.07%. The effect and performance of the input filter is also reflected in this figure where the high order harmonics present in Fig. 7(b) are eliminated as expected. In Fig. 7(b) it can be observed the commutated input current  $i_A$ , which is given as function of the DMC switches and the load currents  $i_o$ .

A very good tracking of the load currents  $i_o$  to its respective references  $i_o^*$  is observed in Fig. 8(a) with a sinusoidal waveform and a THD of 0.52%. In this case the reference is established as  $I_o^*=12.5$  [A]. In Fig. 8(b) is also observed the load voltage which is given as a function of the DMC switches and the input voltages  $v_i$ .

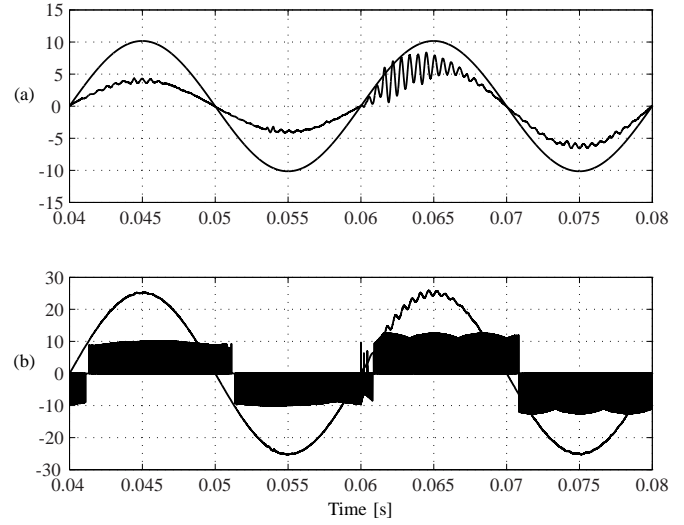


Fig. 9. Simulation results of the proposed method in transient state: (a) source voltage  $v_{sA}$  [V/25] and source current  $i_{sA}$  [A]; (b) capacitor voltage  $v_A$  [V/10] and input current  $i_A$  [A].

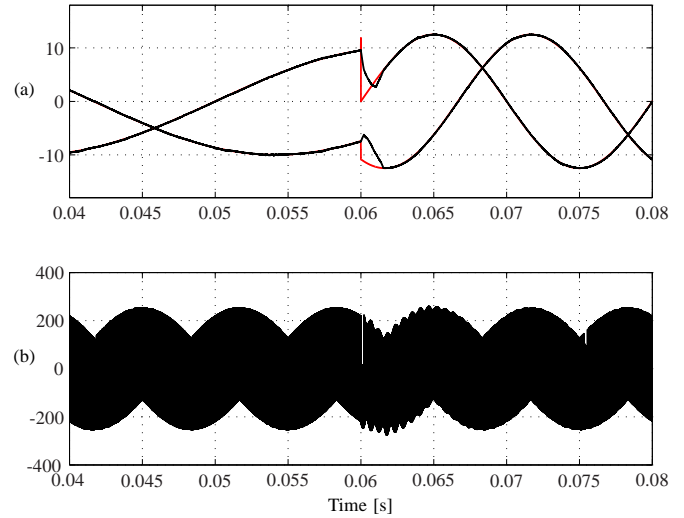


Fig. 10. Simulation results of the proposed method in transient state: (a) load currents  $i_o$  [A] and references  $i_o^*$  [A]; (b) load voltage  $v_a$  [V].

## B. Results in Transient Condition

A step change in the load current is applied to the converter in order to evaluate the performance of the proposed strategy in terms of dynamic response.

This analysis is done as depicted in Fig. 9 and Fig. 10. In Fig. 9 are shown the input variables where is observed a small resonance of the input filter due to the load variation, Fig. 9(a). It is important to highlight that this predictive control strategy shows a fixed switching frequency, improving the performance of the system. It is also evident the good performance of the input filter which mitigates almost all the high harmonic frequencies observed in Fig. 9(b) which are produced by the commutation of the switches. In Fig. 10(a) is observed a good dynamic response of the load current  $i_o$  to

its respective reference  $i_o^*$  with a very fast dynamic response and a very good tracking of the load current. The step change is from  $I_o^*=10[\text{A}]\text{@}20\text{Hz}$  to  $I_o^*=12.5[\text{A}]\text{@}50\text{Hz}$ . In both cases it is observed a very good tracking of the load current to its respective reference.

## V. CONCLUSION

In this paper an indirect model predictive current control strategy has been presented with minimization of the instantaneous reactive input power for a direct matrix converter operating at fixed switching frequency. The method uses the idea of fictitious  $dc$ -link in order to separate the control of both input and output stages of the converter. By doing this, it is possible to reduce the complexity of the control, the operation at fixed switching frequency but also avoid the calculation of a suitable weighting factor for the control of both instantaneous reactive input power and load currents variables. By considering the proposed strategy, a new alternative has emerged for the control of both the input and load currents in a direct matrix converter.

## ACKNOWLEDGMENTS

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## ANNEXES

TABLE III  
PARAMETERS OF THE IMPLEMENTATION

Variables	Description	Value
$V_s$	Amplitude $ac$ -voltage	311[V]
$C_f$	Input filter capacitor	21[ $\mu\text{F}$ ]
$L_f$	Input filter inductor	400[ $\mu\text{H}$ ]
$R_f$	Input filter resistor	0.5[ $\Omega$ ]
$R$	Load resistance	10[ $\Omega$ ]
$L$	Load inductor	10[ $\mu\text{H}$ ]
$T_s$	Sampling time	20 [ $\mu\text{s}$ ]
	Simulation step	1 [ $\mu\text{s}$ ]

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