

Combining Fault Location Estimates for a Multi-Tapped Distribution Line

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Abstract—Multi-tapped lines are common in integrated power systems and microgrids which supply variable loads between the main source and the main load. Adopting a cost effective and efficient method for fault location is important for fast power recovery and improving system reliability. A method requiring measurements only at the ends of the main distribution line is proposed in this paper to solve the issue of locating faults on the tapped lines as well as on the main line without any measurement required from the taps. A combination of single-ended and double-ended algorithms based on higher frequency impedance estimation are utilized to locate the faults within the tapped line. The study considers different fault types in different locations as well as various fault inception angles. The presented results shows the efficiency and the accuracy of the suggested technique with maximum error less than 3% of the total line length.

Index Terms— Fault location, impedance estimation, integrated power systems, protection and tapped lines.

I. INTRODUCTION

Fast and precise fault location algorithm on multi-tapped distribution lines of integrated power system (IPS) is very important for reducing the time required to restore the power delivered to the loads as well as to improve the reliability of the system [1]. Moreover, designing a cost effective fault location method for multi-tapped lines with the minimum number of measurements is one of the challenging issues.

Impedance based fault location methods are one of the most common methods for fault location in distribution systems. Traditional methods are based on measuring the impedance to the fault location using the fundamental frequencies (50 or 60 Hz) [2]. However, this provides an accuracy of a few hundred of meter for typical cable parameters, and hence it is not suitable for short distribution lines. The impedance based fault location methods used in this paper can be classified as one of two types, a single-ended method, which utilize the measurement from one end of the

line, and a double-ended method, which use the measurements from both terminations of the primary line [3].

Many works have been published on using impedance estimation for fault location [3]–[10]. Some of these works used only single-end measurement [3]–[7] and many other techniques used measurements from two terminals or more. A three phase voltage and current in single-ended method is used to estimate fault distance in multi-tapped distribution system while a healthy phased current at the possible faulted sections is used to discriminate between the possible locations [4–5]. While in [6] a combined single-ended impedance estimation and voltage sag matching algorithm are used to locate fault in multi-tapped system. A matching algorithm is used to compare the measured voltage sag with a data bank to decide on the faulted line [6]. In [7], a single-ended method that applicable for an unbalance and multi-source distribution network is proposed. However, In case of faults that fall on two identical laterals, the scheme is not capable of decide which the faulted lateral [7]. The method proposed in [8] require a synchronized measurement from two ends. Moreover, a load flow is required to estimate the voltages and current at the faulted section in order to estimate the exact fault distance. Three phase currents and voltages from all terminals were measured and synchronized in [9]. The authors suggested a technique which reduces the multi-terminal line to a two terminal line based on the voltage differential between the terminals [9]. Another reduction method that convert multi-terminal to two terminal method is suggested in [10]. However, the proposed algorithm is complicated. A communication system is required in the sectionalized microgrid where a central protection unit and phase measurement unit (PMU) for each section is required [11].

This paper presents a direct and precise method for fault location in multi-tapped lines with two ends voltage and current measurements with the aim of minimizing the number of required measurements. The method combines both the

single-ended and the double-ended impedance estimation methods at non-fundamental frequencies to locate the fault on both the main line and the tapped lines with errors of less than 1m.

II. SINGLE AND DOUBLE ENDED ALGORITHMS REVIEW

This section introduces the single-ended and double-ended impedance based fault location methods based at non-fundamental frequency content.

A. Single-Ended Algorithm review

The impedance estimation fault location method in an IPS based on a single-ended measurement will be introduced and demonstrated. A single phase circuit with a short circuit fault on the distribution line, as in Figure 1, will be used to introduce the basis of this method. The supply impedance is represented by Z_s , while Z_L is the equivalent load impedance. The cable impedance between the fault and the sending end is Z_x and the remaining impedance Z_{l-x} represents the cable impedance from the fault to the receiving end of the line [12].

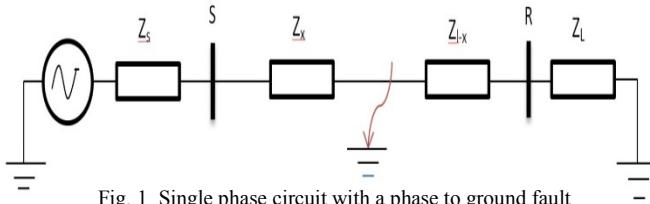


Fig. 1 Single phase circuit with a phase to ground fault

The fault can be considered to be a voltage source which creates voltage and current transients that contains information over a wide frequency range when the fault occurs. The supply source at the non-fundamental frequencies is short circuited as shown in the Thevenin's equivalent circuit of Figure 2, while the fault is represented as a transient source which creates an equal and opposite voltage to the instantaneous pre-fault voltage (V_{pre-f}) at the fault location [12].

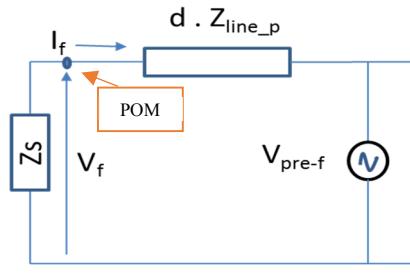


Fig. 2 System at non-fundamental frequency during fault situation

Applying Kirchhoff's voltage law to the non-fundamental equivalent circuit in Figure 2 in order to calculate the voltage drop from the measuring point (POM) to the fault location as in (1):

$$V_f - I_f \cdot Z_x - V_{pre-f} = 0 \quad (1)$$

Rearranging (1) to estimate the impedance between POM and the fault location Z_x gives (2):

$$Z_x = \frac{V_f}{I_f} - \frac{V_{pre-f}}{I_f} \quad (2)$$

The fault distance is computed by dividing Z_x by the perimeter impedance of the line, as shown in (3). Only the imaginary part of the impedance is used because the reactance is not influenced by the fault resistance and because at higher frequencies the reactance dominates the overall impedance more than the resistance.

$$d = \text{imag} \left(\frac{\frac{V_f}{I_f} - \frac{V_{pre-f}}{I_f}}{Z_{line-p}} \right) \quad (3)$$

The V_{pre-f} in (2) is a created step voltage with value equal to the measured pre-fault voltage at the POM, assuming that the voltage drop between the POM and the fault location is negligible. Based on this assumption, an initial error in the fault distance estimation is presented. To estimate the correct V_{pre-f} , the initial estimated distance form (3) is used to calculate new V_{pre-f} shown in (4):

$$V_{pre-f(\text{new})} = V_{pre-f(POM)} - I_{pre-f} \cdot d \cdot Z_{line-p} \quad (4)$$

An initial fault distance is estimated using (3) and this estimated distance is used to calculate new V_{pre-f} at fault location using (4). The fault location is then re-estimated using (3) and the updated calculation of V_{pre-f} . This iteration is repeated until the two successive fault location estimates converge to within an acceptable tolerance of each other, for example, $d_{n+1} - d_n < 0.5m$.

B. Double-ended Algorithm review

The impedance estimation fault location method based on double ended measurements will be introduced and demonstrated using the same circuit and fault condition used to describe the single ended method. Figure 3 shows the circuit at non-fundamental frequencies using the double ended measurements, where the supply source is short circuited and the fault represented as a transient voltage source [3].

The fault provides a transient voltage, V_f , at non fundamental frequencies and R_f is the fault resistance. POM1 is the point-of-measurement at the source end, while POM2 is the point-of-measurement at the load end. Kirchhoff's Laws are applied to the measured voltage and current during a fault for measurements at both ends of the line to calculate the impedance between POM1 and the fault. Kirchhoff's voltage law was applied to Figure 3 to derive (5).

$$V_s + I_s \cdot Z_x + I_f \cdot R_f = V_r + I_r \cdot Z_{l-x} + I_f \cdot R_f \quad (5)$$

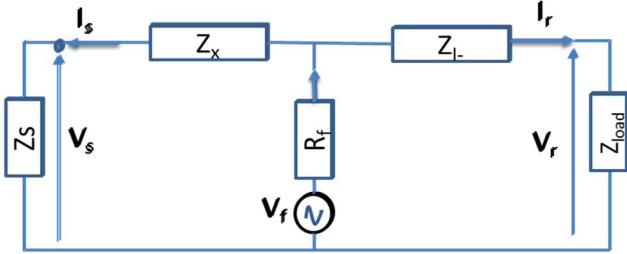


Fig. 3 System at non-fundamental frequency during fault situation using double-ends measurement

Where V_s, I_s are the voltage and current measurements at the source end of the line and V_r, I_r are the measured voltage and current at the receiving end. The total line impedance is $Z_l = Z_x + Z_{l-x}$, hence:

$$Z_x = \frac{V_r - V_s + I_r \cdot Z_l}{I_r + I_s} \quad (6)$$

The impedance between the fault point and source end is estimated using (6). The fault location can be found by dividing the estimated impedance by the per-unit length impedance of the line. As is clear from (6), the fault resistance information is not required by the double-ended method and neither are knowledge of the load and the supply impedances.

C. Tapped lines

The system in Figure 1 is modified to contain a single tap line as shown in Figure 4 and a short circuit fault is applied on the tapped line.

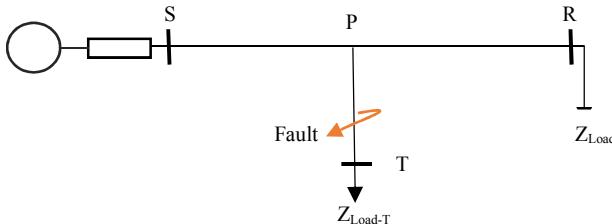


Fig. 4 Single phase circuit with a fault on Tapped line

The equivalent circuit at non-fundamental frequencies will be updated to include the tap line as shown in Figure 5, (5) is also updated as follows to give (7):

$$V_s + I_s \cdot Z_x + I_f \cdot Z_T = V_r + I_r \cdot Z_{l-x} + I_f \cdot Z_T \quad (7)$$

Rearranging (7) in order to calculate the value of Z_x produces (8):

$$Z_x = \frac{V_r - V_s + I_r \cdot Z_l}{I_r + I_s} \quad (8)$$

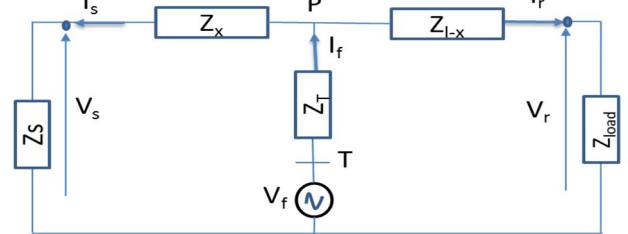


Fig. 5 System at non-fundamental frequency during fault on Tapped line using double-ends measurement

According to (8), the double-ended technique does not require details of the tapped line and its load. Hence, the tapped line may be considered as a fault impedance according to the double-ended method and the estimated distance to the fault is the distance from the supply end to tapping point P in Figure 5. As a result, the double ended technique is unable to locate faults on the tapped line, however, it has the ability to locate the faulted tapped line so it is utilised to discriminate between possible fault locations calculated using the single-ended technique.

The flowchart of Figure 6 shows the procedure used when estimating the distance between the sending end measuring point and the fault location and describes how to discriminate between the possible locations.

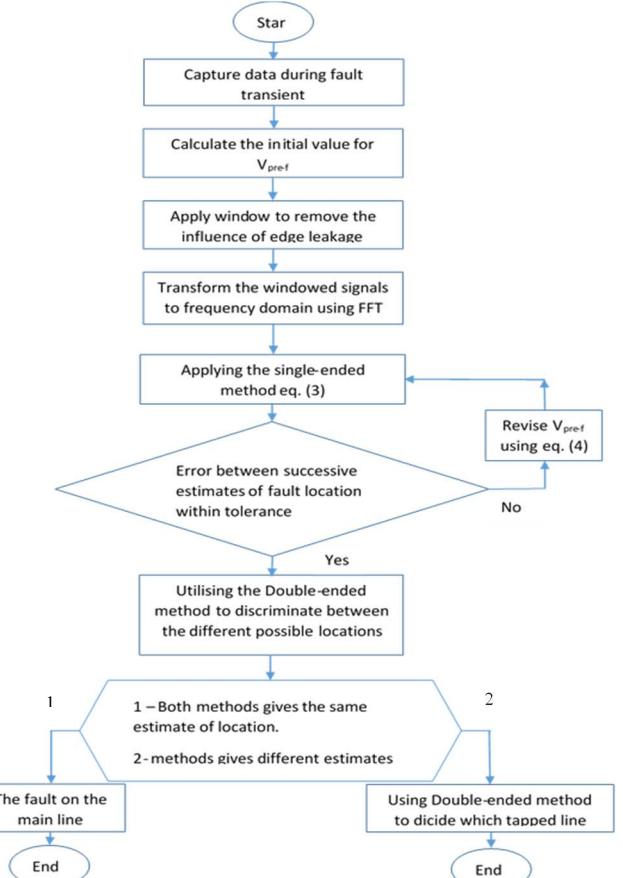


Fig. 6 Flowchart of the proposed process

III. SIMULATION AND RESULTS

A three phase low voltage integrated power system (IPS) with single source, main line of 50m and three tapped lines with different lengths and loads is shown in Figure 7. The details of the circuit are given in Table I.

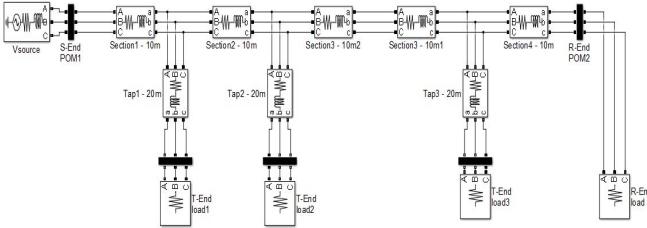


Fig. 7 Simulated IPS

The required data was measured from the sending and receiving ends of the main line of the system shown in Figure 7. Different fault types and locations were applied to the system to validate the proposed method for locating faults on tapped lines as well as the main line using only two sets of measurements.

DL applied to a different location and Figure 10 provide the DLG results. The magnitude of the used fault resistance for all cases was 0.01Ω which is very low when compared to the system load impedance. The actual reactance X_{act} is the green dashed line and the X_{est} is the estimated reactance while “iter” is the required number of iteration for the single-ended method. The final iteration of the single-ended method is presented in the figures which shows only two iteration were required for the error to coverage to preset tolerance.

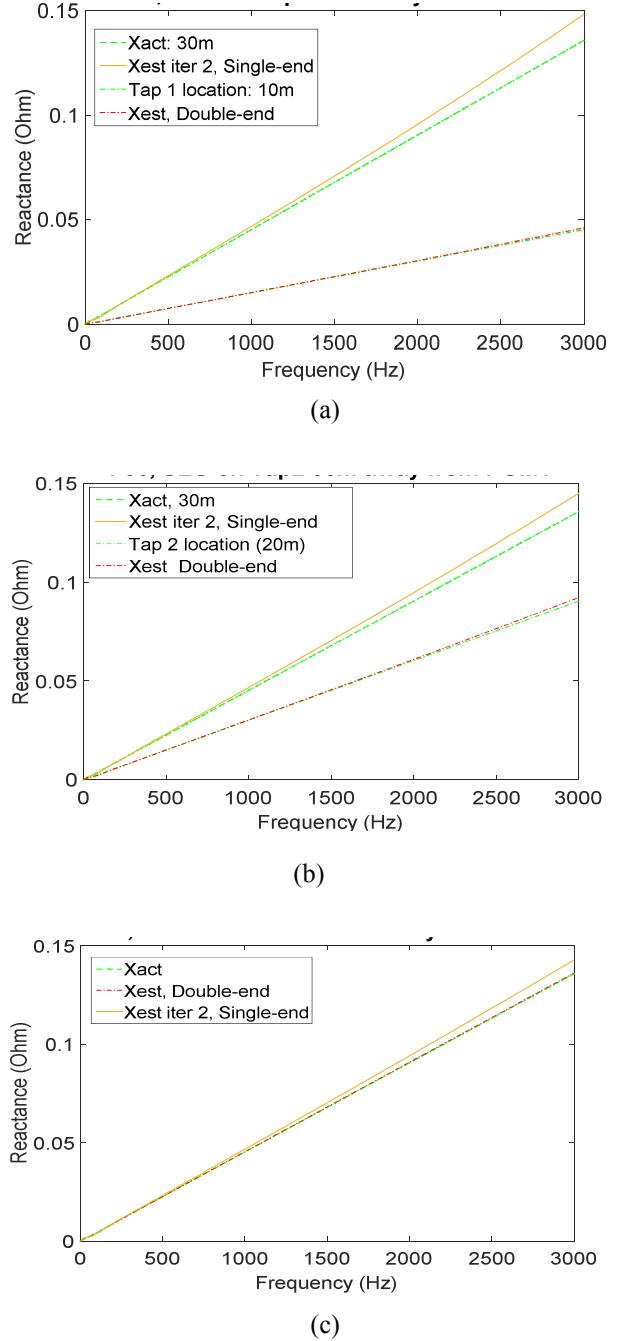


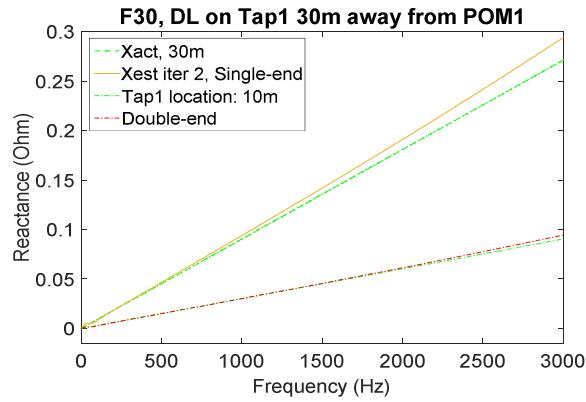
Fig. 8 SLG fault 30m away on different location (a) On Tapped line 1
(b) On Tapped line 2 and (c) On the main line

Table I The IPS parameters

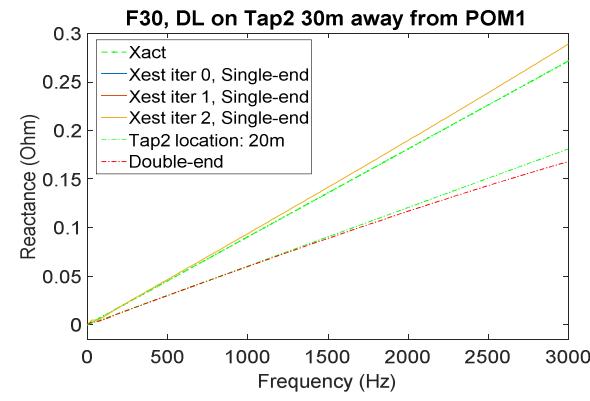
Circuit parameter	Value
Source voltage (ph-ph)	440 (V)
Source impedance	$0.0011 + j0.0096i$
Line per-meter resistance	$30 \mu\Omega$
Line per-meter inductance	$0.24 \mu H$
R-end load	100 kW
Tap-end loads	20 – 30 kW
Sampling frequency	100 kHz

A single line to ground (SLG) fault was initiated 30m away from POM1 on tapped line 1, tapped line 2 and then on the main line to verify the suggested procedure. In Figure 8a the fault was on Tap 1 and the single ended method estimated a fault 31.3m away, but with this distance there are three possible fault locations: on Tap 1, Tap 2 and the main line. Hence, the double ended method was used to discriminate between the possible locations: the estimated fault location was 10m from the supply using the double-ended method, which corresponds with tapping location 1. As a result of combining both methods, the fault is found to be on tapped line 1, which is the correct choice. The same process (Figures 8b and 8c) shows the estimated fault distance and how it is been discriminated from the other possible location using the double-ended method. The calculated distance is 31.3 m, while the actual distance is 30 m, which gives an error of 2.6%.

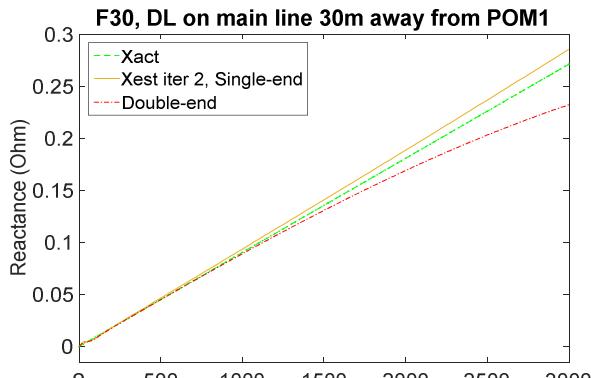
The same process is repeated with a double-line (DL) fault and double-line to ground (DLG) fault which further shows the capability combining the two methods to locate different faults types on the tapped lines without any measurement from the taps. Figure 9 presents the estimation results obtained when the



(a)



(b)



(c)

Fig. 9 DL fault 30m away from POM1 (a) On Tapped line 1 (b) On Tapped line 2 and (c) On the main line

Further simulations were performed using different fault locations in order to investigate the accuracy and the reliability of the presented method. The percentage error calculation are summarized in Table II and Table III for SLG and DL faults in the possible fault locations

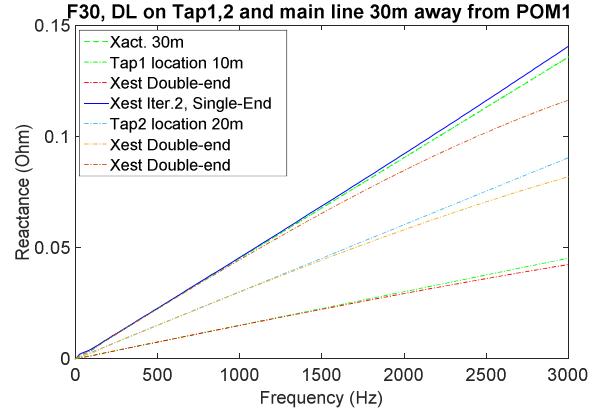


Fig. 10 DLG fault on Tap1, Tap2 and Main Line

Table II Percent error calculation for SLG faults

Actual distance (m)	Estimated distance (m)	Percentage error
00 (on Main line)	0.128	0.25
10 (on Main line)	10.175	0.35
20 (on Main line)	20.18	0.35
30 (on Main line)	30.26	0.52
40 (on Main line)	40.35	0.7
50 (on Main line)	50.45	1.4
30 (Tap line 1)	30.68	1.36
30 (Tap line 2)	30.44	0.9
50 (Tap line 3)	50.6	1.2

Table III Percent error calculation for DL faults

Actual fault distance (m)	Estimated distance (m)	Percent error
00 (on Main line)	0.21	0.42
10 (on Main line)	10.10	0.20
20 (on Main line)	20.10	0.20
30 (on Main line)	30.124	0.25
40 (on Main line)	40.125	0.25
50 (on Main line)	50.142	0.285
30 (Tap line 1)	30.50	1.00
30 (Tap line 2)	30.275	0.55
50 (Tap line 3)	50.225	0.45

The percentage error was calculated according to (9) given below:

$$\text{percent error} = \frac{\text{Estimated dist.} - \text{actual dist.}}{\text{Line length}} \times 100\% \quad (9)$$

It is apparent from the results presented in Tables II and III that the single-ended method shows a high accuracy to estimate the distance to the fault location with maximum percentage error of less than 2%. The calculation based on the single-ended estimation and the estimated distance is the average of the distance over the 3 kHz frequency range of Figures 8—10. Moreover, the percentage error increased as the fault moved toward the end of the line. This is because larger tapped line current is neglected in the utilized scheme as well as the SNR decreased because of decreased fault transient step.

Furthermore, the influence of fault inception angle changing on the accuracy of the proposed method was investigated. The inception angle was varied from 30 degrees to 180 degrees (zero crossing point) and Table IV shows the inception angle and the calculated percentage error in the estimated fault location when a SLG fault is initiated at the end of tapped line 2.

Table IV influence of fault inception angle on the single-ended method.

Inception angle (degree)	% error
30	1.2
60	0.9
90	0.9
120	0.92
145	0.3
165	0.81
175	2.7
178	4.7
180	5.5

The results presented in Table IV indicates that the single-ended method can estimate the fault distance with percentage error up to 3% except when the fault is initiated near to the zero crossing point, where the error is increased to 5.5%. However, this amount of error is still within an acceptable tolerance and faults near to the zero crossing point also present a problem for other fault location methods.

IV. CONCLUSION

A combination scheme of single-ended and double-ended techniques based on impedance estimation at non-fundamental frequencies has been used for fault location in a multi-tapped integrated power system environment. The single-ended method was used to estimate the distance between the sending end (POM1) and the fault location based on the estimated reactance, while the double-ended method was used to discriminate between the possible fault locations. The presented results show that the scheme is able to locate different fault type in different fault locations with maximum error less than 3%. The method only requires measurements from the two ends of the main line in addition to knowledge of the per meter line impedance. Moreover, the method can locate the faults with high accuracy with any inception angle except when the fault is initiated near the zero crossing angle, where the error increased to 5.5%. However, this is still acceptable tolerance for fault at the zero crossing point. Future work is planned in order to validate the simulation results presented in this paper.

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