

# FEA based Transformer Loss Analysis for Dual Active Bridge DC-DC Converter using Triple Phase Shift Modulation

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**Abstract**—High frequency transformers are a key component of dual active bridge (DAB) dc-dc converters, providing isolation and voltage scaling. The losses in the transformer should be minimized to improve efficiency and extend lifetime. The existing research focuses on the optimization of design parameters, such as core material, cross sectional area and winding configuration to minimize losses. However, the transformer losses also depend on the harmonic content of the current as well as the design parameters and thus the efficiency can be further improved by selecting a modulation scheme which results in minimum current harmonics. This paper evaluates the effect of modulation schemes on the transformer losses of a DAB converter operating under triple phase shift (TPS) modulation by using finite element analysis. A comparison of losses under different modes of TPS modulation has been provided and it is proved that losses can be significantly reduced by selecting optimal modulation schemes. The experimental results performed on a 1kW DAB converter laboratory prototype are provided to validate the proposed work. Moreover, a generalized framework has been proposed to analyze and optimize transformer losses for isolated converters.

**Index Terms**—Dual active bridge (DAB) dc-dc converter, finite element analysis (FEA), triple phase shift (TPS), winding loss.

## I. INTRODUCTION

IN recent years, isolated bidirectional dual active bridge (DAB) dc-dc converters, first proposed in [1] have gained popularity in the research community. DAB dc-dc converters are capable of bidirectional power flow, galvanic isolation, and have the potential for high power density and soft switching realization. As a result, it is considered, in various forms for use in high voltage DC transmission (HVDC), power quality improvement devices, renewable energy sources, flexible AC transmission system (FACTS) and drives systems [2]–[5].

The DAB dc-dc converter, shown in Fig.1, consists of two H-bridges (HB) galvanically isolated by a high frequency transformer. The transformer provides galvanic isolation between the two HBs, and can also step up/down voltage. Furthermore, the leakage inductance of the transformer is used to support the control of power flow through the converter. The average transmission power and the output voltage level of the converter can be controlled by modulating the phase shift ratios of the two HBs. Various modulation techniques have been proposed in the literature, and can be categorized as single phase shift (SPS)[6], extended phase shift (EPS)[6], dual phase shift (DPS) and triple phase shift (TPS) [7].

In DAB dc-dc converters nearly 20 percent of the total losses are consumed in the form of transformer and inductor losses while around 35 percent of the total losses are in the form of switching loss[8]. The rest is a result of conduction and auxiliary losses [8]. Thus, switching losses are a major source of loss in the DAB converter which cannot be ignored in any situation. The main focus of current research is to consider the modulation schemes that result in soft switching, in order to minimize the switching losses, and also to reduce the switch current stress to minimize conduction losses. Compared to other modulation schemes, TPS utilizes all three available phase shift ratios of the converter. Based on the ranges of the three phase shift ratios, many distinct modes of operation can be obtained Each mode offers a different peak current and waveform, suggesting a different harmonic content for the same output voltage and transmission power with more than one mode that satisfy soft switching [7], [9]. The focus of the TPS modulation schemes is to reduce the current stress, eliminate reactive power in the high frequency ac link and extend the soft switching operating range to improve the efficiency and the life expectancy of the semiconductor devices[6]-[7],[9]–[12]. However, the transformer losses, which play a significant role in the overall efficiency of the converter, and the impact of different modulation schemes on the transformer losses are not discussed in the existing literature.

High frequency transformers can consume significant

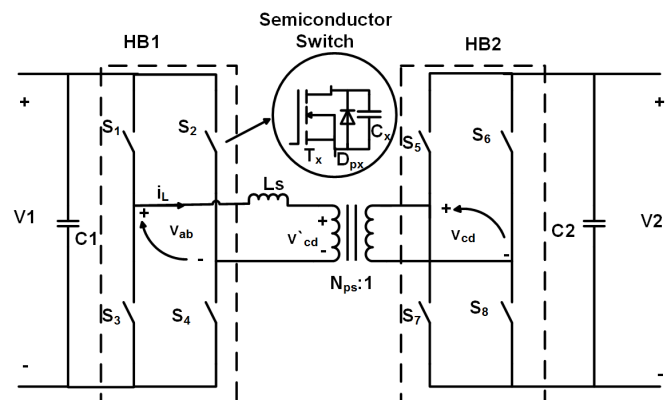


Fig. 1. Isolated Bidirectional DAB dc-dc converter

amount of power in the form of winding and core losses [13]. The winding power loss, which is typically 60-80% of the total loss of the transformer is significantly influenced by the high frequency eddy currents that cause skin and proximity effects [12], [13]. The transformer used in a DAB dc-dc converter is subject to non-sinusoidal waveforms containing high frequency harmonic content which increases the winding power loss. The temperature of the transformer rises as a result of this high power loss. Consequently, hot spots can be induced on the surface of transformer, which can damage the insulation of the winding, resulting in local burnouts. Moreover, the rise in temperature reduces the operational transformer ratings. Therefore, transformer losses need to be minimized for the efficient operation of the DAB dc-dc converter and it is important that the harmonic content and peak value of the ac current should be taken into consideration to achieve maximum efficiency. However, the existing research effort appears to be working towards optimization of the design parameters of the transformer such as core material, size of core and number of windings in order to minimize the transformer losses and increase the power density [14]–[26]. An optimization approach based on the brute force technique has been used in [19], which evaluates different combinations of design parameters to obtain maximum efficiency and power density. Similarly, a grid based optimization technique has been used to find the optimal design parameter in [20]. Machine learning based approaches have been used in [21] and [22] to obtain the optimal design parameters. Scaling laws to optimize medium frequency transformer have been suggested in [23]. An air core transformer has been presented in [24] to reduce the complexity, cost and size of the converter. Another approach to reduce the overall size of the transformer has been presented in [25] by integrating the required inductance of the converter in the transformer.

The aforementioned literature has not included any insight into the reduction of the transformer losses during operation. [27] provides design procedure of an AC-DC DAB converter for optimizing efficiency and power density. A comparison of transformer efficiency has been provided in [28] for multilevel Si and SiC DAB converters and it is shown that 3 level modulation is an optimized modulation scheme for high power density, high efficiency and low cost. A modulation based on the analytical expressions has been suggested in [29] with focus to minimize losses and extend ZVS in AC-DC DAB converter. An optimized modulation scheme is also provided for DPS in [30] by including the transformer losses in the optimization function. However, different modes available under TPS and its effect on the transformer losses due to the unique harmonic content have not been evaluated in any of the discussed literature. The aim of this research is to analyze the impact of the modulation schemes specifically for TPS modulations scheme on the power loss of the transformer with given design parameters.

To evaluate the effect of modulation schemes, the resultant currents generated by the five different operating modes of TPS modulation defined in [7] as unified TPS (UTPS) have been selected in this research; UTPS is a current stress optimized scheme that presents closed form solutions for the

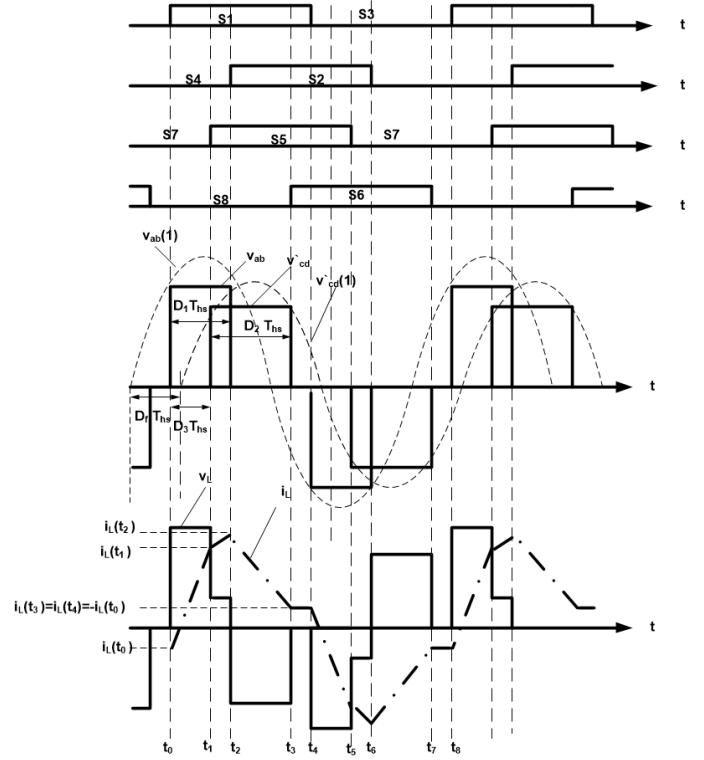


Fig. 2. General operating waveforms of DAB with TPS modulation

phase shift ratios for each of the five defined modes[7]. The power losses of the transformer in a DAB dc-dc converter are calculated in steady state using finite element analysis (FEA)[31],[32] performed using ANSYS Maxwell for each of the five operating modes. This research shows that the winding power loss is distinct for each mode even though some modes have the same peak currents. Moreover, it is also presented in this research that core loss is also different for each mode due to the non-identical shape of waveforms. The operating mode which results in minimum transformer loss for a specific power range is presented based on this analysis. A comparison between the modes with minimum and maximum transformer losses is provided which shows that approximately, 3-5% of the total power can be saved by selecting the optimal modulation scheme. Furthermore, a generalized framework based on the methodology used in this research to calculate the transformer losses in any isolated converter is presented. A hardware prototype with the same parameters as the ANSYS Maxwell simulation has been constructed in the lab and experiments have been performed to verify the presented scheme.

The remaining parts of the paper are organized as follows. Section-II provides a detailed discussion on UTPS and the optimized values of phase shift ratios. Section-III provides a detailed analysis of the power losses occurring in the transformer. Moreover, this section also includes a comparative analysis of various techniques to calculate losses. Section-IV discusses the finite element method (FEM) used to calculate the transformer losses and the analysis of these losses for each mode of UTPS performed in ANSYS. The

details of the laboratory prototype of DAB dc-dc converter and experimental results are presented in section-V. Section-VI discusses the overall efficiency of the converter. Section-VII presents the generalized methodology which can be used to optimize the transformer losses in isolated converters. Section-VIII concludes the paper.

## II. UNIFIED TRIPLE PHASE SHIFT

A DAB dc-dc converter is shown in Fig.1, along with its general operating waveforms under TPS control in Fig.2.  $V_1$  and  $V_2$  are the dc voltages of the two HBs of the converter,  $v_{ab}$  and  $v_{cd}$  are the ac equivalent voltages of HBs on  $V_1$  side.  $v_{cd}$  is given by the product of  $N_{ps}$  and  $v_{ab}$ , where  $N_{ps}$  denotes the primary to secondary turns ratio of the transformer. The semiconductor switch is denoted by  $S_x$ , which consists of the active switch  $T_x$ , anti-parallel diode  $D_{px}$ , and the junction capacitance, denoted by  $C_x$ .  $L_s$  is the sum of series inductance and leakage inductance of the transformer and  $i_L$  is the inductor current.  $T_s$  is the switching period while  $T_{hs}$  is half switching period. The inner phase shift ratios for HB1 and HB2 are defined as  $D_1$  with range  $0 \leq D_1 \leq 1$  and  $D_2$  with range  $0 \leq D_2 \leq 1$ , respectively, while  $D_3$  with range  $-1 \leq D_3 \leq 1$  is the outer phase shift ratio between  $v_{ab}$  and  $v_{cd}$ . Moreover,  $D_f$  is defined as the phase shift ratio between the fundamental components of  $v_{ab}$  and  $v_{cd}$ , which is related to other phase shift ratios given by:

$$D_f = D_3 + D_2/2 - D_1/2 \quad (1)$$

Expressions for base values of power  $P_b$  and current  $I_b$ , defined in terms of input voltage  $V_1$ , series inductance  $L_s$  and switching period  $T_s$  are given by Eq: 2

$$I_b = \frac{V_1 T_s}{2\pi L_s}, \quad P_b = \frac{V_1^2 T_s}{2\pi L_s} \quad (2)$$

The required transmission power is represented by  $P_o$  and  $P_{on}$  will be used to represent the value of required transmission power normalized to the base power  $P_b$ .  $I_{pn}$  will represent current stress normalized to the base current  $I_b$ .

The switches on the same leg of the DAB always conduct in alternate half cycles such as  $S_1$  and  $S_3$  as shown in Fig.2, similarly  $S_2$  and  $S_4$  also conduct in alternate half cycles. The phase shift ratio between the turning on of switch  $S_1$  and  $S_2$  is  $D_1$ , as shown in Fig. 2. Switches  $S_1$  and  $S_4$  are on and conducting in this duration while  $S_2$  and  $S_3$  are not conducting resulting in voltage  $V_{ab}$  across the output of HB1, which is equal to  $V_{in}$ .  $S_2$  is turned on at the end of  $D_1$  interval, thus  $S_1$  and  $S_2$  are conducting during this interval with zero voltage created across the output terminals of the HB1. This state continues until switch  $S_1$  is turned off at the end of the half cycle. From the start of the next half cycle,  $S_2$  and  $S_3$  conduct until  $S_2$  is switched off. The magnitude of  $V_{ab}$  is same as the input voltage but with negative direction during this interval.

Similarly, on the second bridge, HB2,  $D_2$  is the phase shift ratio between the turning on of switch  $S_5$  and  $S_6$ . As shown in Fig.2, switches  $S_5$  and  $S_8$  conduct for this duration while  $S_6$  and  $S_7$  are turned off, thus  $V_{cd}$  is equal to  $V_0$ .  $S_6$  is turned on after  $D_2$ , therefore  $S_5$  and  $S_6$  are conducting during this

TABLE I  
MODE OPERATIONAL CONSTRAINTS [7]

Switching Mode	Mode operational constraints
Mode I	$0 \leq D_f \leq \frac{D_2}{2} - \frac{D_1}{2}$
Mode II	$0 \leq D_f \leq \frac{D_1}{2} - \frac{D_2}{2}$
Mode III	$\left  \frac{D_1}{2} - \frac{D_2}{2} \right  \leq D_f \leq \min\left(\frac{D_1}{2} + \frac{D_2}{2}, 1 - \frac{D_1}{2} - \frac{D_2}{2}\right)$
Mode IV	$1 - \frac{D_1}{2} - \frac{D_2}{2} \leq D_f \leq \frac{D_1}{2} + \frac{D_2}{2}$
Mode V	$\frac{D_1}{2} + \frac{D_2}{2} \leq D_f \leq 1 - \frac{D_1}{2} - \frac{D_2}{2}$

interval forcing zero voltage until switch  $S_5$  is turned off.  $S_6$  and  $S_7$  start conducting at the end of the half cycle until  $S_6$  is turned off. The magnitude of  $V_{cd}$  is equal to the output voltage with negative direction.

$D_3$  is the phase shift ratio between the first switch  $S_1$  of first bridge and the first switch  $S_5$  of the second bridge, which is same as the phase shift ratio between the voltages  $V_{ab}$  and  $V_{cd}$ . The slope of the inductor current varies based on the switching pattern as shown in the Fig. 2, and a switching period can be divided into small intervals depending on the shape of current waveform.

Five distinct switching modes are obtained based on the switching sequence of the two HBs. The operating waveform for all modes, which are used to derive the expression of normalized transmission power and current stress are presented in [7]. The main contribution of UTPS is to derive optimal control parameters to minimize current stress with the required transmission power and voltage conversion ratio  $d$ , where the control parameters are the optimal values of the three phase shift ratios ( $D_f$ ,  $D_1$ ,  $D_2$ ). Thus, the optimization approach aims to find optimal values of phase shift ratios ( $D_{f,opt}$ ,  $D_{1,opt}$ ,  $D_{2,opt}$ ) from the specified mode determined by the operational constraints presented in Table I, which minimize current stress  $I_{pn}$  at the required transmission power  $P_{on}$  and conversion ratio,  $d$ , where  $d < 1$ .

A Karush-Kuhn-Tucker (KKT) approach, which takes into account all of the equality and inequality constraints in solving the optimization problem, has been used to find the optimized solution given by  $X^*=(D_{f,opt}, D_{1,opt}, D_{2,opt})$  for minimum current stress. The boundary conditions of the optimal phase shifts result in two different transmission power ranges, the lower transmission power range is identified as range-A and the higher transmission power range is referred as range-B. The optimal phase shift ratios resulting in minimum current and the transmission power ranges arising from the optimal control parameters of each mode are given in the Table II.

## III. TRANSFORMER LOSSES

### A. Types of Losses

Transformer structures consist mainly of a core, made of electromagnetic material, and the windings, usually made of copper. Thus there are two types of losses: core loss, and winding loss.

1) *Core Loss*: The core loss is composed of the hysteresis loss, eddy current loss and the residual loss [33]. The

TABLE II  
OPTIMAL CONTROL PARAMETERS FOR SELECTED SWITCHING MODES[7]

Mode	Power range	Local optimal control parameters
Mode I	IA: $[0, \frac{(1-d)d^2\pi}{2}]$	$D_{1,opt} = \sqrt{\frac{2P_{on}}{(1-d)\pi}}, \frac{D_{1,opt}}{d} \leq D_{2,opt} \leq 1, D_{f,opt} = \frac{\sqrt{2(1-d)\pi P_{on}}}{2d\pi}$
	IB: $[\frac{(1-d)d^2\pi}{2}, \frac{d\pi}{8}]$	$D_{1,opt} = \begin{cases} \frac{1}{2} - \frac{1}{2}\sqrt{\frac{d\pi-8P_{on}}{d\pi}}, 0 < d \leq \frac{1}{2} \\ \frac{1}{2} + \frac{1}{2}\sqrt{\frac{d\pi-8P_{on}}{d\pi}}, \frac{1}{2} < d \leq 1 \end{cases}, D_{2,opt} = 1, D_{f,opt} = \begin{cases} \frac{1}{4} - \frac{1}{4}\sqrt{\frac{d\pi-8P_{on}}{d\pi}}, 0 < d \leq \frac{1}{2} \\ \frac{1}{4} + \frac{1}{4}\sqrt{\frac{d\pi-8P_{on}}{d\pi}}, \frac{1}{2} < d \leq 1 \end{cases}$
Mode II	IIA: $[0, \frac{d(1-d)\pi}{2(2-d)^2}]$	$D_{1,opt} = (2-d)\sqrt{\frac{2P_{on}}{d(1-d)\pi}}, D_{2,opt} = \sqrt{\frac{2P_{on}}{d(1-d)\pi}}, D_{f,opt} = \frac{\sqrt{2(1-d)\pi P_{on}}}{2d\pi}$
	IIB: $[\frac{d(1-d)\pi}{2(2-d)^2}, \frac{d\pi}{8}]$	$D_{1,opt} = 1, D_{2,opt} = \frac{1}{2} + \frac{1}{2}\sqrt{\frac{d\pi-8P_{on}}{d\pi}}, D_{f,opt} = \frac{1}{4} + \frac{1}{4}\sqrt{\frac{d\pi-8P_{on}}{d\pi}},$
Mode III	IIIA: $[0, \frac{(1-d)d^2\pi}{2}]$	$D_{1,opt} = \sqrt{\frac{2P_{on}}{(1-d)\pi}}, D_{2,opt} = \frac{1}{d}\sqrt{\frac{2P_{on}}{d(1-d)\pi}}, D_{f,opt} = \frac{\sqrt{2(1-d)\pi P_{on}}}{2d\pi}$
	IIIB: $[\frac{(1-d)d^2\pi}{2}, \frac{d\pi}{6}]$	$D_{1,opt} = \frac{2}{3} - \frac{1}{3}\sqrt{\frac{(2-3d)^2(d\pi-6P_{on})}{(1-3d+3d^2)d\pi}}, D_{2,opt} = \frac{2}{3} - \frac{1}{3(2-3d)}\sqrt{\frac{(2-3d)^2(d\pi-6P_{on})}{(1-3d+3d^2)d\pi}}$ $D_{f,opt} = \frac{1}{3} - \frac{1-3d}{6(2-3d)}\sqrt{\frac{(2-3d)^2(d\pi-6P_{on})}{(1-3d+3d^2)d\pi}},$
Mode IV	IVA: $[0, \frac{(1-d)d^2\pi}{2}]$	$D_{1,opt} = \begin{cases} \frac{1}{2} - \frac{1}{2}\sqrt{\frac{d\pi-8P_{on}}{d\pi}}, 0 < d \leq \frac{1}{2} \\ \frac{1}{2} + \frac{1}{2}\sqrt{\frac{d\pi-8P_{on}}{d\pi}}, \frac{1}{2} < d \leq 1 \end{cases}, D_{2,opt} = 1, D_{f,opt} = \begin{cases} \frac{1}{4} - \frac{1}{4}\sqrt{\frac{d\pi-8P_{on}}{d\pi}}, 0 < d \leq \frac{1}{2} \\ \frac{1}{4} + \frac{1}{4}\sqrt{\frac{d\pi-8P_{on}}{d\pi}}, \frac{1}{2} < d \leq 1 \end{cases}$
	IVB: $[\frac{(1-d)d^2\pi}{2}, \frac{d\pi}{4}]$	$D_{1,opt} = 1 - (1-d)\sqrt{\frac{d\pi-4P_{on}}{(1-2d+2d^2)d\pi}}, D_{2,opt} = 1, D_{f,opt} = \frac{1}{2} - \frac{1}{2}\sqrt{\frac{d(d\pi-4P_{on})}{(1-2d+2d^2)\pi}}$
Mode V	VA: $[0, \frac{d^2\pi}{2(1+d)^2}]$	$D_{1,opt} = \frac{\sqrt{2\pi P_{on}}}{\pi}, D_{2,opt} = \frac{\sqrt{2\pi P_{on}}}{d\pi}, D_{f,opt} = \frac{(1+d)\sqrt{2\pi P_{on}}}{2d\pi}$
	VB: $[\frac{d^2\pi}{2(1+d)^2}, \frac{d\pi}{8}]$	$D_{1,opt} = \frac{1}{2} - \frac{1}{2}\sqrt{\frac{d\pi-8P_{on}}{d\pi}}, D_{2,opt} = \frac{1}{2} + \frac{1}{2}\sqrt{\frac{d\pi-8P_{on}}{d\pi}}, D_{f,opt} = \frac{1}{2}$

hysteresis loss is the power lost during the magnetization and demagnetization of the core, caused by the multi-valued nature of the B-H curve. Thus, it is dependent on the frequency of the magnetizing force,  $f$ , and the area of the hysteresis loop, which in turn depends on the square of magnetic flux density [34],[35].

The eddy current loss is caused by the current flowing in the core due to a time varying magnetic field. Its generating mechanism is similar to the current flowing in the conductor, and has the same effects as the ohmic loss. Eddy current loss is directly proportional to the square of frequency,  $f$ , and the square of magnetic flux density,  $B$  [36].

The residual loss is due to the relaxation processes and occurs when the magnetic material has to establish a new equilibrium position during changes in thermal equilibrium. It is a small fraction of the loss as compared to hysteresis and eddy current loss and is directly proportional to the frequency,  $f$ , and magnetic flux density,  $B$  [33].

2) *Winding Loss*: The winding loss, also commonly known as copper loss, depends on the frequency, the structure of the conductors and the relative geometric position of the conductors to other conductors as well as to the magnetic core of the transformer. It consists of the ohmic part or loss due to dc resistance and an ac part due to ac resistance[37],[38].

The two effects that increase the ac resistance are: skin effect and proximity effect. Under high frequency operational conditions, the concentration of current increases near the surface of the conductor due to skin effect. Skin depth is a measure of the distance at which the current penetrates into the conductor, it is given by [39]:

$$\delta = \frac{1}{\sqrt{\pi f \sigma \mu_0 \mu_r}} \quad (3)$$

where  $\delta$  is the skin depth,  $\sigma$  is the conductivity of the

conductor, and  $\mu_0$  and  $\mu_r$  is absolute and relative permeability of the conductor, respectively. Therefore, skin depth decreases with an increase in frequency as shown in Eq. (3). Hence, the effective area of the conductor is reduced as the current tends to flow only in the circumference of the conductor and not in the whole conductor area.

Proximity effect is the eddy current effect caused by the alternating magnetic field of one conductor on another conductor in its vicinity. This causes the current to concentrate on one side of the conductor, which is in the close proximity of another current carrying conductor. Thus, the effective area for ac current flow decreases, which in turn increases the resistance. This resistance further increases at a higher frequency, which causes the winding loss to increase [40].

### B. Calculation of transformer losses

Several techniques have been proposed in the literature to calculate core loss [41]–[48] and winding loss [49]–[52]. Core loss calculation methods can be categorized into two groups: loss separation and empirical methods. In the loss separation method, core loss is divided into three components- hysteresis loss, eddy current loss and residual loss. However, this method requires extensive experimentation and curve fitting techniques to calculate the coefficients and consider all components of the loss, which makes its application potentially impractical.

The original Steinmetz equation (OSE), presented by Steinmetz, is an empirical formula, which is widely used to calculate the core loss [42]. It is given by:

$$P_{core} = K f^\alpha B_m^\beta \quad (4)$$

where  $P_{core}$  is the core loss,  $f$  is the fundamental frequency and  $B_m$  is the maximum flux density of the magnetizing force. Furthermore,  $K$ ,  $\alpha$  and  $\beta$  are the constants, which

depend on the material of the core, that can be determined by manufacturers data sheet or by curve fitting techniques. The OSE has good accuracy for sinusoidal excitation. However, it is not suitable for the transformers used in all power electronics converters due to the non sinusoidal excitation of the component. Several adjustments to the OSE such as the modified Steinmetz equation (MSE) [44], generalized Steinmetz equation(GSE) [45] and the improved generalized Steinmetz equation (iGSE) [46] have been suggested to enhance the performance of OSE. However, all these calculations are based on the arbitrary waveforms. Moreover, comparative experimental results show poor correlation [41],[47], [48].

Winding loss has a significant effect on the efficiency of the DAB dc-dc converter and has gained the attention of researchers working on these converter types. Many analytical formulas have been derived to calculate winding loss, the most popular one is Dowell equation presented in [50]. It is based on one-dimensional (1-D) Maxwell equation, the analytical expression to calculate the AC resistance factor ( $F_R$ ) derived by Dowell. This is given by:

$$F_R = \Delta \frac{\sinh 2\Delta + \sin 2\Delta}{\cosh 2\Delta - \cos 2\Delta} + \frac{2(m^2 - 1)\Delta}{3} \frac{\sin h\Delta + \sin \Delta}{\cosh \Delta - \cos \Delta} \quad (5)$$

where  $\Delta$  is the normalized foil thickness with reference to the skin depth  $\delta$ , and  $m$  is the number of layers of each winding portion. Dowell has assumed parallel inter layered magnetic field to the symmetry axis of the magnetic component. However, its physical validity has been questioned by many researchers [53], [54]. Although a correction factor named as the porosity factor has been suggested to rectify the Dowell equation [55], [56], it is time consuming to create a model and solve it for a specific device while the formula derived is limited to a specific part of the winding [32].

Ferreira has presented a closed form expression by considering the exact solution of the magnetic field of a round conductor by taking into account the skin and proximity effects [51]. However, the porosity factor is not suggested, resulting in an inaccurate evaluation of AC resistance factor [52]. Moreover, many other analytical expressions have been presented in [57]–[60].

Analytical techniques are good to analyze magnetic components, however, these approaches consider 1-D magnetic field distribution and thus ignore many other phenomena which greatly contribute to the losses in transformer. First, analytical techniques neglect the orthogonal magnetic fields assuming constant field intensity at each conductor layer[61],[62]. Second, windings are assumed to be part of an infinite sheet with constant current density along the surface of each layer, thus ignoring the edge effect [61]. Third important factor is the power loss due to the fringing effect in the air gap. Usually gapped ferrite cores are used to design inductors, the introduction of the air gap causes fringing magnetic field at the air gap resulting in excess power loss in the copper windings. The lines of leaked magnetic flux counteract the useful current by inducing the eddy currents thus reducing the effective area of the conductor and increasing the ac resistance as well as the power loss[63], [64].

Recently, the finite element method has gained significant attention for modelling various nonlinear electromagnetic materials with sinusoidal or other forms of excitation. Since FEA tools can analyze such field distributions, their application provide a better alternative to analytical methods [61]–[64]. The FEM has been used to calculate transformer parameters as well as losses [63]–[65]. Moreover, FEM is usually used as a benchmark to prove the validity of the novel analytical equations to analyze the magnetic losses [65], [66], therefore FEM has been used in this research to analyze and calculate the accurate transformer losses. Many powerful software programs, such as ANSYS, COMSOL and FEMLAB have been developed for the calculation of different types of transformer losses and other transformer parameters such as leakage inductance and winding resistance.

A 2D or 3D FEM model is constructed, based on the real transformer dimensions and geometry. Transient electromagnetic field analysis is then used to calculate the losses in the transformer, which involves spatial and temporal discretization of the physical equations.

The FEM solves a set of Maxwell equations [66]–[69] in order to calculate the losses of the transformer for a given excitation and frequency. Domain decomposition along a time-axis is used to perform transient analysis to solve all time steps simultaneously. Properties of the material, as well as boundary conditions can be defined for the transformer model. Material properties are characterized by a B-H curve. Transient FEM analysis then calculates core and the winding loss of the transformer for a specific frequency.

#### IV. ANALYSIS AND PROPOSED SCHEME TO MINIMIZE TRANSFORMER LOSSES

As discussed in section-II, five modes arise in TPS modulation with a unique maximum value of current and a distinct waveform shape. This section discusses the FEM setup in ANSYS [70] to calculate the transformer losses for each mode of operation. It provides the analysis of the obtained results and proves that transformer losses not only depend on the magnitude of current but also on the harmonic content of the current. Hence, each mode will result in different operational losses.

##### A. FEM setup

ANSYS Maxwell 2D has been used to evaluate the transformer losses. The magnetic part of the DAB dc-dc converter which consists of the transformer and an additional inductor has been designed in ANSYS Maxwell as shown in Fig. 3. The transformer has been designed with axial symmetry RZ as shown in Fig. 4. An E59/31/22 core from EPCOS is used. The material of the core is N97, which is suitable for medium frequency. Concentric windings are used on the central leg of the rectangular core transformer. The turns ratio of the transformer is 1:1 with 20 turns on each winding. Similarly, the inductor has also been designed in ANSYS using an E59/31/22 core.

The average power losses at steady state for each mode must be calculated. Therefore, the transient solution setup

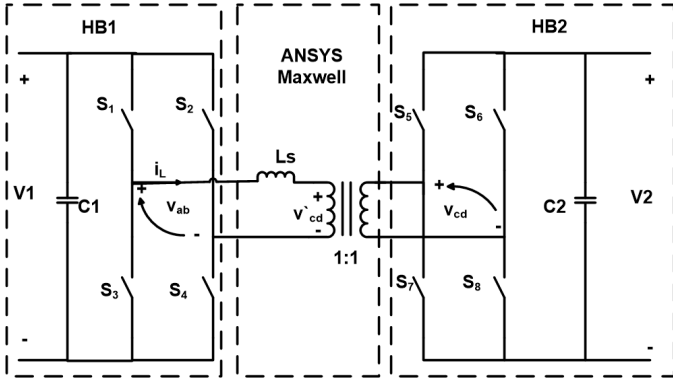


Fig. 3. Parts of DAB dc-dc converter built in ANSYS Maxwell

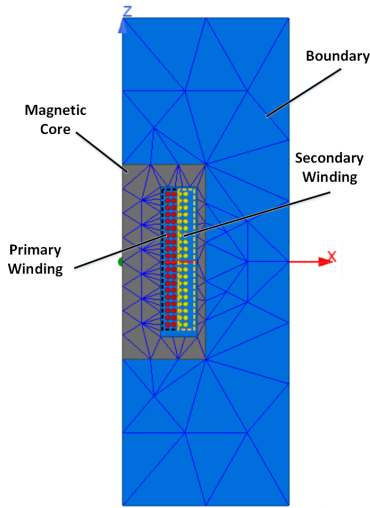


Fig. 4. Transformer model in ANSYS Maxwell with mesh

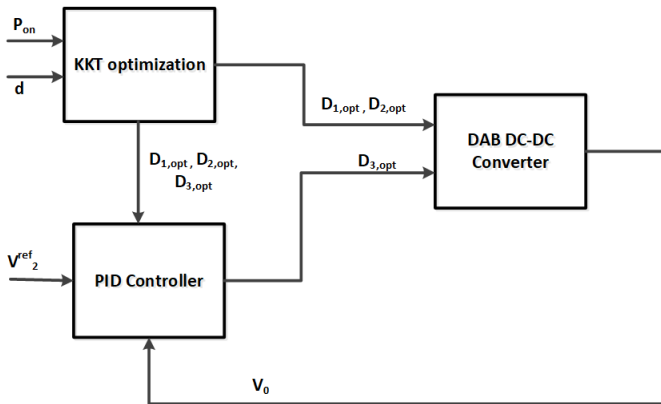


Fig. 5. Block diagram showing the operation of DAB converter

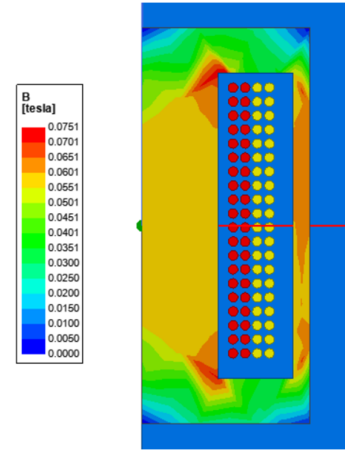


Fig. 6. Field distribution in transformer generated by current waveforms obtained from DAB dc-dc converter operating at  $P_o=241$  watts, Input Voltage  $V_1 = 230V$ , Output Voltage  $V_2^{ref} = 138V$ , Conversion ratio  $d=0.6$  and switching frequency  $f=20kHz$ .

TABLE III  
PARAMETERS OF DAB DC-DC CONVERTER USED IN SIMULATIONS

Parameters	Values
Input voltage $V_1$	230V
Reference output voltage $V_2^{ref}$	138V
Conversion ratio $d$	0.6
Input Capacitance $C_p$	480 $\mu F$
Output Capacitance $C_s$	150 $\mu F$
Inductance value $L_s$	226.6 $\mu H$
Design of Transformer core	ETD 59/31/22
Material of transformer core	N97
Number of turns	20:20
Transformer ratio	1:1
Switching frequency	20 kHz

is used. The transient solution setup performs the FEA for one specific frequency for a specified time interval in contrast to other solution setups available in ANSYS Maxwell, which examines frequency response by analyzing converter at different frequencies. The power ferrite core loss model is used to calculate the core losses. The B-H curve obtained from the data sheet of the N-97 has been used to calculate the coefficients of the core loss model.

The transformer and series inductor windings are excited with the ac current generated from a DAB dc-dc converter using data from a PLECS[71] simulation as well as with data

TABLE IV  
PER UNIT TRANSMISSION POWER RANGES FOR  $d=0.6$

Mode	Minimum power	Maximum power
Mode-IA	0	0.22608
Mode-IB	0.22608	0.2355
Mode-IIA	0	0.19922
Mode-IIB	0.19922	0.2355
Mode-IIIA	0	0.22608
Mode-IIIB	0.22608	0.314
Mode-IVA	0	0.22608
Mode-IVB	0.22608	0.471
Mode-VA	0	0.22078
Mode-VB	0.22078	0.2355

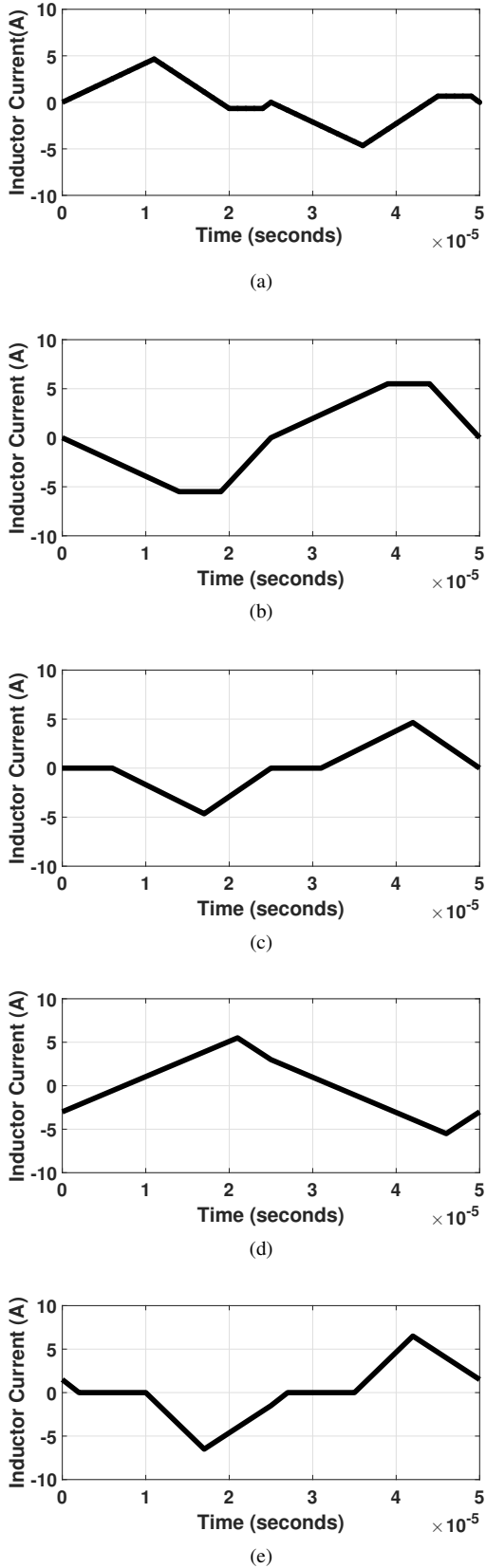


Fig. 7. Inductor current for  $P_o=241$  watts, Input Voltage  $V_1 = 230V$ , Output Voltage  $V_2^{ref} = 138V$ , Conversion ratio  $d=0.6$  and switching frequency  $f=20kHz$ . (a) Mode-1 (b) Mode-2 (c) Mode-3 (d) Mode-4 (e) Mode-5

from the experimental setup as discussed later in the article. Primary and secondary winding are excited separately from the primary and secondary currents, respectively, whereas the magnetizing current is the difference of the primary and secondary currents.

The parameters of DAB dc-dc converter are given in Table III. Fixed values of  $D_{1,opt}$  and  $D_{2,opt}$  are used, which are found from the UTPS calculations. Proportional, integral and differential (PID) control is used to regulate the output voltage by adjusting  $D_{3,opt}$ , whereas the initial value of  $D_{3,opt}$  is calculated from the UTPS theory. The operation of the converter is shown in Fig. 5.

The processing time required to conduct the FEA at one power setting for a specific time duration depends on the system model internal dynamics as well as mesh length and time step. Mesh length plays an important role, as there is a trade-off between the accuracy of the results and the time taken by the analysis. Accuracy of results increases by selecting a small mesh length, but the time to conduct FEA significantly increases. As a result, an optimal mesh length should be selected for better performance considering a reasonable processing time. In this work, a length of 5mm is assigned to the mesh. The transformer structure with the mesh is shown in Fig. 4. The time step selection is another crucial parameter, which also affects performance and processing time. Since the converter is operated at a switching frequency of 20kHz, which results in a waveform with a time period of  $50\mu s$ , the time step should be small enough to precisely sample the generated waveform in the converter simulation. Therefore, for this work, a minimum time step of  $0.25\mu s$  and a maximum time step of  $2\mu s$  have been selected.

### B. Analysis of the results and optimal modes of operation

The field distribution in the transformer and the current waveforms for each operating mode of UTPS for  $d=0.6$  and  $P_o=241$  watts, which lies in the range of transmission power range-A, are shown in Fig. 6 and Fig. 7, respectively. Transmission power ranges for  $d=0.6$  are given in Table IV; ranges of transmission power depends on the value of conversion ratios for a constant value of input voltage and series inductance. The maximum achievable per unit value of transmission power is  $\frac{d}{4\pi}$ , which is same as the maximum transmission power of SPS modulation and can only be obtained using the mode-IV modulation scheme. Each waveform has a different shape and as a result there is a different harmonic content in each case. The resultant winding loss, core loss and total losses in the transformer as well as current stress and data from frequency analysis for each mode are given in Table V. The lowest amount of winding loss occurs in mode-3, although mode-1 and mode-3 have the same current stress and peak current at the fundamental frequency. However, mode-1 has a higher total harmonic distortion (THD) than mode-3, thus the winding loss increases in mode-1. Therefore, it is clear that the winding loss of the transformer not only depends upon the current stress but also on the harmonic content of the current flowing through the transformer winding. Moreover, a comparison of the results for mode-2 and mode-5 shows

that although the current stress of mode-5 is greater than mode-2, the loss in mode-5 is less than mode-2 due to the lower fundamental frequency component of mode-5. A further inference can be obtained by analyzing mode-4 and mode-5. The fundamental component of current in mode-4 is greater than the fundamental component of current in mode-5. However, mode-5 results in high losses because it has a higher THD than mode-4.

The transformer losses for each mode with different transmission power setting have been calculated while keeping the voltage ratio and other parameters constant for comparison purposes. The same procedure has been repeated for different values voltage ratio upto  $d = 0.95$ . Winding loss, core loss and total losses are plotted against transmission power  $P_o$  as shown in Fig.8, Fig.9 and Fig.10, respectively. Although the same results were obtained for different voltage ratios the authors have included only one setting of the voltage ratio for brevity. Fig. 8(a) presents the winding loss for different transmission power  $P_o$  in range-A for each of the five modes in steady state operation of the DAB dc-dc converter. The minimum winding loss occurs in mode-3 if the transmission power is in range-A. However, mode-3 does not provide optimal control parameters if the transmission power is high i.e, it lies in range-B. The optimal control parameters can be obtained using mode-4, achieving the minimum winding loss as evident from Fig. 8(b).

The resultant core loss for each mode is shown in Fig. 9. This figure shows that the core loss is very low when compared to the winding loss. The total losses of the transformer, which are the sum of core and winding loss are shown in Fig.10. It shows that the minimum losses occur in mode-3 in the lower transmission range while mode-4 results in the minimum losses for the higher transmission range. Therefore, transformer efficiency can be improved by only minimizing the winding loss. Fig. 11 provides a comparison of the minimum and maximum amount of losses occurring in the transformer. This presents the minimum losses that occurs in mode-3 and mode-4 for low and high power ranges, respectively, and the maximum power losses that occurs in mode-2 and mode-5. This comparison shows that losses can be significantly minimized by carefully selecting the mode of operation.

The proposed optimal modulation scheme based on the results obtained from this research study, with the previously described setup and parameters, can be summarized in the flow chart in Fig. 12. Base values for the transmission power and current stress are calculated using the series inductance  $L_s$ , switching frequency  $f_s$  and input voltage  $V_1$  as given in Eq. 2. The next step is to check for the range of transmission power. If the transmission power is in the range  $0$  to  $\frac{(1-d)d^2\pi}{2}$  then mode-3 should be used to calculate the optimal control parameters to achieve minimum losses in the transformer. Otherwise the control parameters calculated using mode-4 should be used since they result in minimum losses at higher transmission power. It can also be inferred from Fig. 11 that 3-5% of the total power can be saved by using the presented scheme.

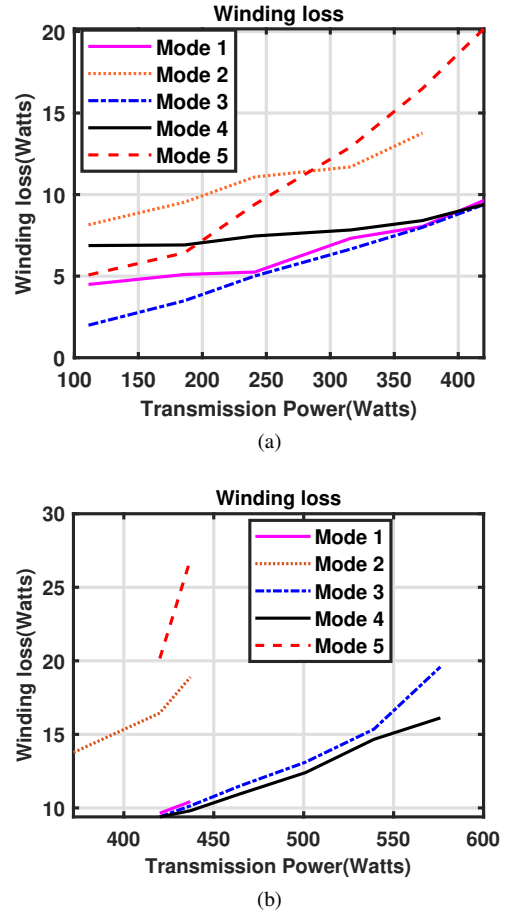


Fig. 8. Winding Losses of transformer in isolated bidirectional DAB converter with Input Voltage  $V_1 = 230V$ , Output Voltage  $V_2^{ref} = 138V$ , Conversion ratio  $d=0.6$  and switching frequency  $f=20kHz$ . (a) Power transmission range-A. (b) Power transmission range-B

## V. EXPERIMENTAL RESULTS

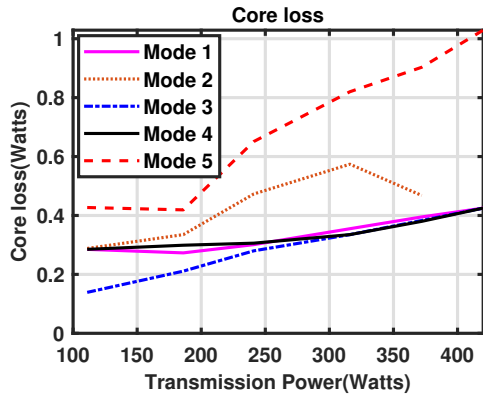
The proposed work has been validated by performing experiments on a 1kW laboratory prototype. The experimental prototype is shown in Fig. 13. The main components used in the design of the hardware are listed in Table VI. The series inductance consists of the leakage inductance of the transformer and an external inductor. IGBTs are used as switching devices to form the HBs. A TMS320F2837xD evaluation board from Texas Instruments, which communicates with a host computer, has been used as the digital control platform.

The transformer losses for each mode of UTPS modulation with different transmission power settings have been calculated while keeping the voltage ratio and other control parameters constant. The input voltage is maintained at 230 V and supplied from a DC source (SM300-10D). A DC electronic load is used to obtain different values of resistance at the output while regulating the output voltage to 138V.  $D_{1,opt}$ ,  $D_{2,opt}$  and  $D_{3,opt}$  are calculated from the UTPS modulation equations given in Table II [7]. A PID control has been implemented to regulate the output voltage at 138V by adjusting  $D_3$ . The total losses of the transformer, formed from the sum of core and winding losses are calculated by measuring the transformer input and output power when the converter achieves steady

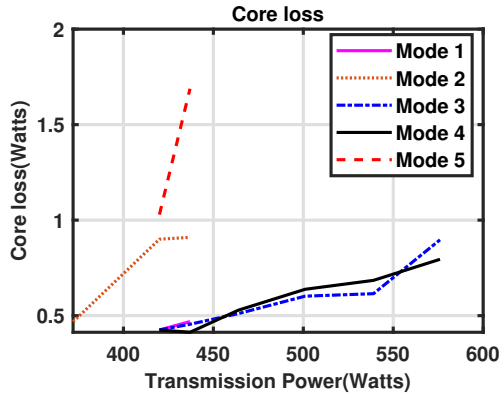


TABLE V  
HARMONIC ANALYSIS OF INDUCTOR CURRENT OF ALL MODES FOR  $P_o=241$  WATTS, SWITCHING FREQUENCY  $f=20$ KHZ, CONVERSION RATIO  $d = 0.6$  WITH INPUT VOLTAGE  $V_1 = 230V$  AND OUTPUT VOLTAGE  $V_2^{ref} = 138V$

Mode	Mode 1	Mode 2	Mode 3	Mode 4	Mode 5
Winding losses (W)	5.25	11.08	4.97	7.2549	9.4
Core losses (W)	0.301	0.473	0.256	0.306	0.651
Total losses (W)	5.5317	11.553	5.226	7.5609	10.051
Current Stress (A)	4.651	5.50	4.651	5.50	6
Fundamental component (A)	3.106	5.24	3.12	4.28	4.072
3rd Harmonic	37.08%	14.74%	33.14 %	13.73%	41.25%
5th Harmonic	2.60 %	3.92 %	6.13%	5.83%	7.35%
7th Harmonic	6.20%	1.92%	3.56 %	3.06 %	3.62%
9th Harmonic	1.36 %	0.63%	2.36%	1.65 %	3.23%
Total Harmonic Distortion	37.96 %	15.49%	34.14 %	13.73 %	42.30%



(a)

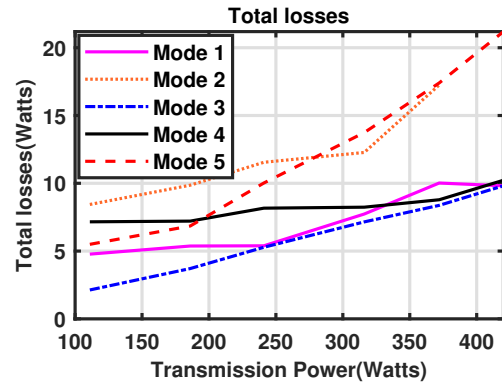


(b)

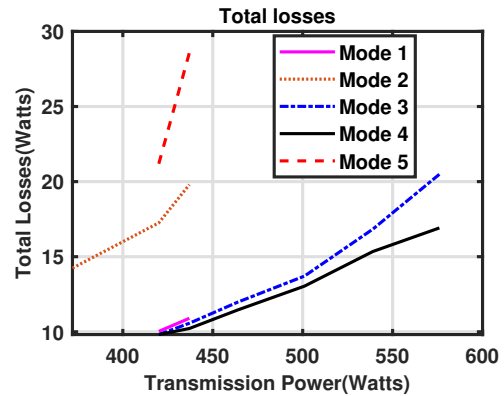
Fig. 9. Core Loss of transformer in isolated bidirectional DAB converter Input Voltage  $V_1 = 230V$ , Output Voltage  $V_2^{ref} = 138V$ , Conversion ratio  $d=0.6$ .(a) Power transmission range-A. (b) Power transmission range-B

state. The transformer losses are calculated for each mode using a different transmission power setting. The total losses are plotted against the transmission power for each mode. Current probes and an oscilloscope have been used to record the input and output current of the transformer, which is then used to carry out the analysis presented in the previous section.

Transformer losses in range-A for each mode are shown in Fig.14(a). The experimental data show that when the transmission power  $P_{on}$  is in range-A, then the minimum transformer losses occur when the converter is operating under mode-3,



(a)



(b)

Fig. 10. Total Losses of transformer in isolated bidirectional DAB converter with Input Voltage  $V_1 = 230V$ , Output Voltage  $V_2^{ref} = 138V$ , Conversion ratio  $d=0.6$  and switching frequency  $f=20$ KHz..(a) Power transmission range-A. (b) Power transmission range-B

while mode-2 and mode-5 result in the highest losses. Hence, this data validates the hypothesis presented, based on the FEA carried out to analyze the effect of harmonics on transformer losses.

Fig. 14b presents the total losses of the transformer for all modes when the transmission power  $P_{on}$  is in range-B. It shows that when the converter is operating under mode-4 it achieves the minimum losses while maximum losses occur in mode-5, thus reinforcing the proposed optimal modes to be used for



Fig. 11. Transformer losses in best and worst modes with Input Voltage  $V_1 = 230V$ , Output Voltage  $V_2^{ref} = 138V$ , Conversion ratio  $d=0.6$  and switching frequency  $f=20kHz$ .

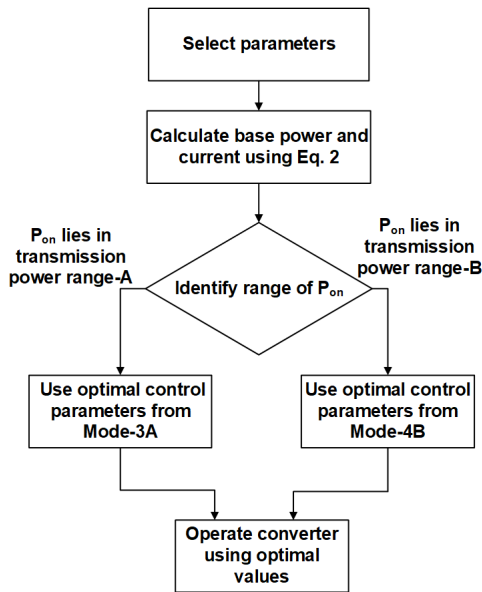


Fig. 12. Flow diagram presenting optimal modulation scheme to minimize transformer losses

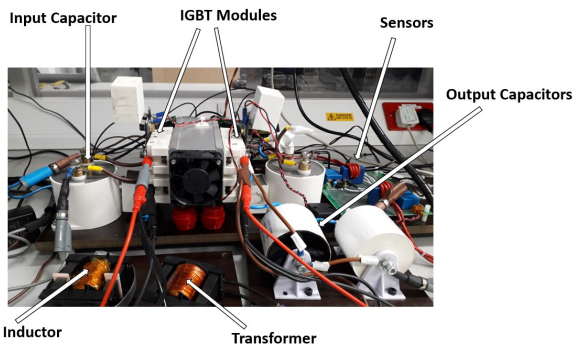
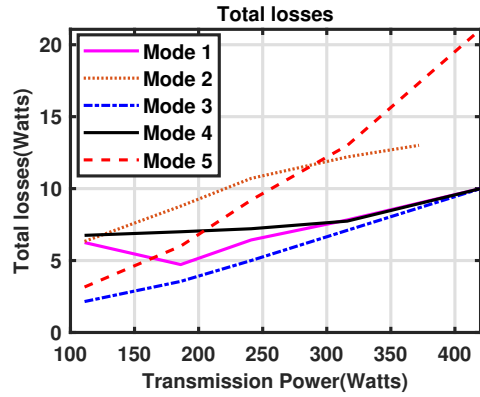
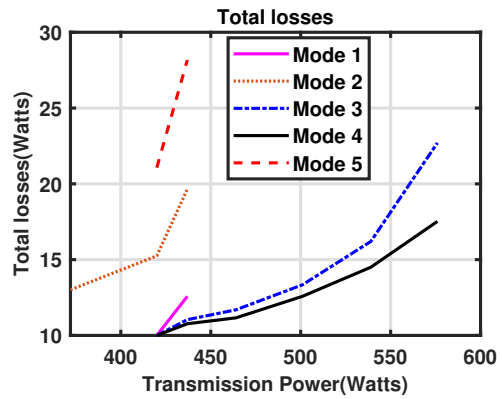


Fig. 13. Experimental setup



(a)



(b)

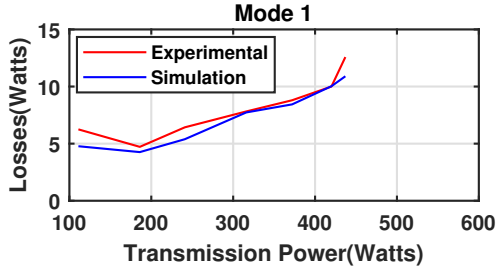
Fig. 14. Total losses of transformer in experimental isolated bidirectional DAB converter with Input Voltage  $V_1 = 230V$ , Output Voltage  $V_2^{ref} = 138V$ , Conversion ratio  $d=0.6$  and switching frequency  $f=20kHz$ . (a) Power transmission range-A. (b) Power transmission range-B



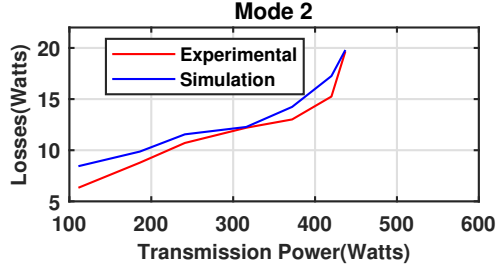
Fig. 15. Transformer losses in best and Worst modes(Experimental) with Input Voltage  $V_1 = 230V$ , Output Voltage  $V_2^{ref} = 138V$ , Conversion ratio  $d=0.6$  and switching frequency  $f=20kHz$ .

TABLE VI  
HARDWARE COMPONENTS

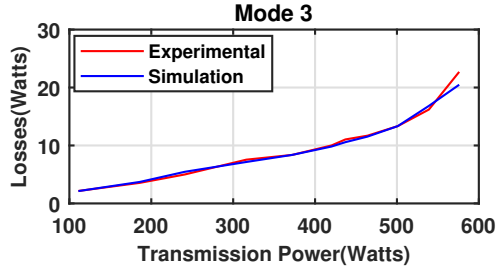
Component	Description	Parameters
Switching devices	SKM75GB176D	$V_{CES} = 1700V$ $I_C = 80A$
Input Capacitors	C4DE	480 $\mu F$
Output Capacitors	IEC 61071 DownCap DTR	100 $\mu F$ 50 $\mu F$
Magnetic components	Core ETD 59/31/22 N97 Litz Wire	$L_S = 226.6\mu H$
Voltage sensors	LV 25-p	$t_r = 25\mu S$
Current sensors	LA 55-p	$BW=25 \mu S$ 200kHz



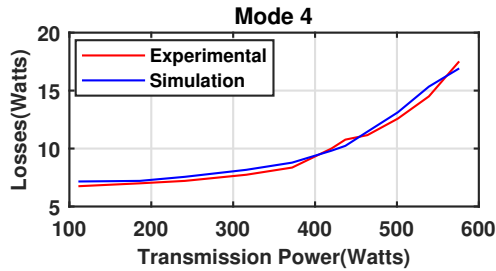
(a)



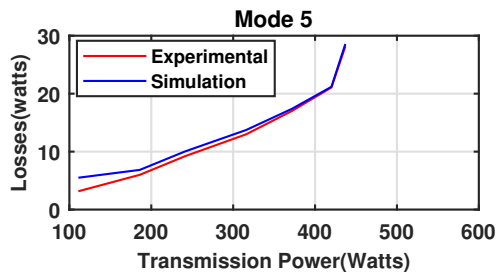
(b)



(c)



(d)



(e)

Fig. 16. Comparison of simulation and experimental results (a) Mode-1 (b) Mode-2 (c) Mode-3 (d) Mode-4 (e) Mode-5

minimizing the transformer losses. Similarly, Fig. 15 provides a comparison of operating modes resulting in minimum and maximum losses as previously discussed in Section-IV.

The comparisons of the simulation and experimental results for each mode of operation are provided in Fig.16(a-e).

## VI. EFFICIENCY OF DAB CONVERTER UNDER TPS

The losses in the converter mainly consist of losses occurring in switching devices and the losses in the transformer and inductor. The losses in switching devices can be further categorized as the switching losses and conduction losses [8].

The switching losses depend and can be controlled by applying modulation strategies that result in soft switching. The SPS modulation provides soft switching when conversion ratio is nearly equal to unity, however, it loses soft switching at larger voltage variations in the output. Conversely, TPS provides soft switching at an extended range of operation [6], [7] and [9]–[12]. For the TPS modulation used in this research work to consider the transformer losses, adapted from [7], has been analyzed and proven to be providing soft switching in [7].

The conduction losses in the switching devices depend on the current flowing in the converter and can be minimized by selecting a modulation scheme that results in minimum ac current through the inductor. For the discussed modulation scheme, it is shown in [7] as well as in this research that mode-3 and mode-4 have lowest current. The current waveforms are provided both using simulations Fig. 7 as well as experiment in Fig. 17 for converter operating at  $P_o=241$  watts, input Voltage  $V_1 = 230V$ , output Voltage  $V_2^{ref} = 138V$  and conversion ratio  $d=0.6$ . The analysis data of the current waveforms and the resultant losses are provided in Table V. It is apparent from the waveforms that mode-3 has lowest current. Similarly, analysis provided in Table V also confirms that the mode-3 results in minimum current thus minimizing conduction losses.

The other main part of the losses which is the focus of this research are the losses consumed in the transformer and inductor in the form of core losses and winding losses. These losses depend on the current stress as well as the harmonic content of the current waveform as proved in this research. This research has shown that mode-3 and mode-4 have minimum losses for low power and high power ranges, respectively.

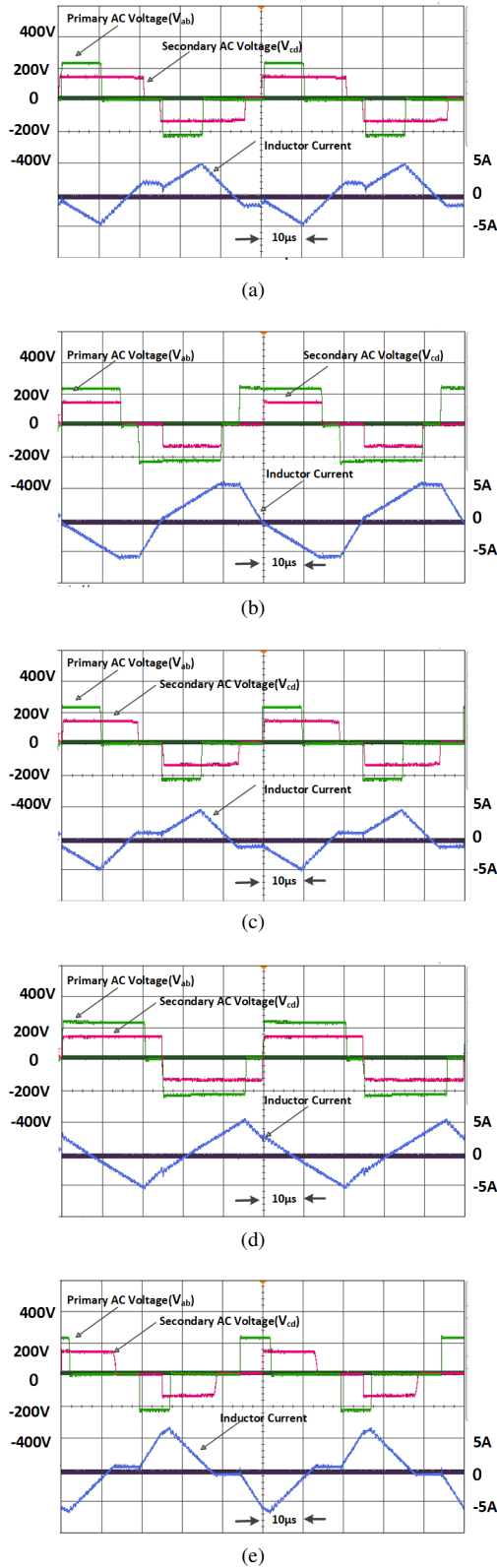


Fig. 17. Waveforms obtained from laboratory prototype of DAB dc-dc converter operating at  $P_o=241$  watts, Input Voltage  $V_1 = 230V$ , Output Voltage  $V_2^{ref} = 138V$ , Conversion ratio  $d=0.6$  and switching frequency  $f=20kHz$ . (a) Mode-1 (b) Mode-2 (c) Mode-3 (d) Mode-4 (e) Mode-5

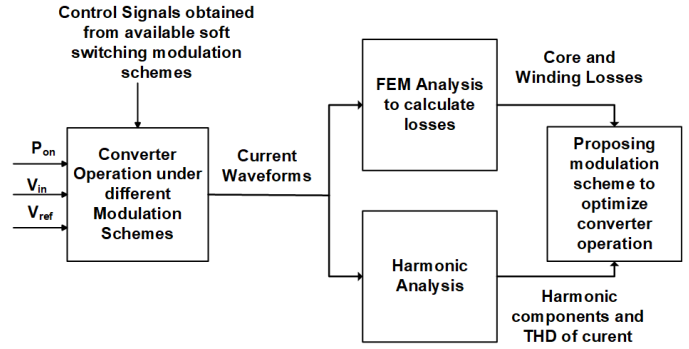


Fig. 18. Generalized framework to optimize transformer losses in isolated converters

## VII. PROPOSED GENERALIZED FRAMEWORK FOR OPTIMISATION OF TRANSFORMER LOSSES IN ISOLATED CONVERTERS

In this section, a generalized framework is proposed to optimize the operation of isolated converters by selecting the optimal modulation scheme which minimizes transformer losses. The block diagram given in Fig. 18 presents the proposed methodology to optimize the converter losses. As switching losses are the main losses occurring in the converter [8], the operating ranges that satisfy the soft switching criterion are generally selected [6]-[7] and [9]-[12]. In order to determine the optimal modulation scheme, the transformer losses must also be calculated and analyzed for different soft switching modulation schemes. FEM analysis can be used to calculate the transformer losses. To conduct FEA, the transformer used in the converter circuit can be modelled using any FEM software, such as ANSYS Maxwell, with the same core shape, material type and winding conditions as used in the design of the converter circuit. To analyze the losses under each operating mode, current waveforms are provided as input to the FEM model of the transformer. The converter needs to be operated under all available modulation scheme with the same parameters for all the schemes to generate the required current waveforms. The current waveforms can be obtained by simulating the converter in PLECS, MATLAB, ANSYS Simpler or any other suitable software and noting the input and output currents of the magnetic components. The current obtained using the experimental setup of the converter can also be used as the excitation input of the transformer.

Core and winding losses are then calculated using FEA while a harmonic analysis is performed to determine the THD and harmonic components of the current as shown in the two blocks in Fig.18. The applied current waveforms are sufficient to calculate the core loss in the transformer as magnetizing current, which is the difference of the primary and secondary current, can be used to calculate the core losses of the transformer in FEM setup [72] and [73]. The harmonic analysis of the ac current helps to understand and analyze the behaviour of the transformer in that specific converter. It is suggested that the transformer losses are analyzed at different power levels over the available range to obtain an accurate result. This data can be analyzed to select the optimal

modulation scheme that minimizes transformer losses in the isolated converter.

### VIII. CONCLUSION

The efficiency of power converters is a research area of high significance. In isolated converters, including the DAB dc-dc converter, transformer losses are one of the factors that contribute to reduced efficiency. The existing literature focuses on the transformer design to minimize losses. In this paper, it is shown that FEM can be used to analyze transformer losses for different operating conditions, and the results used to optimize converter operation. FEA is performed to investigate the impact of the applied modulation scheme on the transformer losses of a DAB dc-dc converter. The current waveforms and the resultant losses for every mode of UTPS modulation at the same transmission power are obtained. The analysis shows that each of the five operating modes of UTPS modulation provides the transformer with an excitation current having a distinct peak and harmonic content, and thus results in different transformer losses for the same required transmission power. Based on this extensive analysis, an optimal modulation scheme for minimum transformer losses has been proposed. Experiments carried out on DAB dc-dc converter prototype confirm the findings. A novel approach to minimize losses in DAB dc-dc converter that could also be extended to other isolated power converters in future work is also presented.

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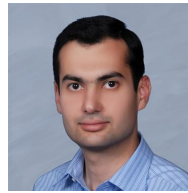
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