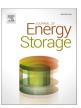
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# Research papers



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# Chance-constrained model predictive control-based operation management of more-electric aircraft using energy storage systems under uncertainty

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# ARTICLE INFO

#### ABSTRACT

Keywords: More-electric aircraft Operation management Chance constraints Model predictive control Multi-failure Multi-uncertainty Online Monte-Carlo simulation On more electric aircraft (MEA), reducing fuel consumption and guaranteeing flight safety are pursued by efficient operational management of the electrical power system (EPS). Considering the growing number of onboard electric loads and the increasing complexity of EPS architecture due to the integration of multiple power converters and energy storage systems (ESSs), system-level operation control is required to manage power distribution, load scheduling, and ESSs. In this paper, a chance-constrained stochastic model predictive control (CC-SMPC) method is proposed to improve both the system operation in terms of the system's cost and reconfiguration activities as well as the ability to cope with uncertainties due to fluctuating load demands. Both normal and faulty operating conditions are investigated with multi-failure cases, resulting in different uncertainty propagation paths. The system's operational and technical requirements are formulated as a set of deterministic and probabilistic constraints in the CC-SMPC model. To verify the effectiveness of the proposed strategy, a comprehensive comparison study is conducted. Two uncertainty/failure cases are taken into account and simulations are performed for both offline and online control strategies while the Monte-Carlo algorithm is used for scenario generation. The results are evaluated using the proposed evaluation framework, showing that the CC-SMPC achieves better performance compared to deterministic MPC (DMPC) in both cases. In an offline testing framework, comparing the performance of DMPC and CC-SMPC strategies shows that CC-SMPC reduces the power constraint violations for batteries and generators in all cases following the selected confidence level. In addition, in an online testing framework with 1 % violation probability, the following results are observed in the two cases: In the EPS normal condition, CC-SMPC reduces the total cost by 31.4 % and the overall constraint violation cost by 93 %; while in the EPS faulty condition, CC-SMPC reduces the total cost by 4.37 %, and the overall constraint violation cost by 96 %.

#### Nomenclature

#### Maximum output power of APU [kW] P<sub>jj'</sub><sup>HVmax</sup> Maximum power of HV buses [kW] Indices of EPS architecture Index for HV and LV buses respectively j, l P<sub>ic</sub><sup>HVCmax</sup> Maximum power of connection link between Index for DC/DC converter Index for HV and LV loads respectively HV bus i and DC/DC converter c [kW] $P_{cl}^{LVCmax}$ Maximum power of connection link between Number of APU $N^{HVB}$ DC/DC converter c and LV bus j [kW] Number of HV buses $P_1^{BATmax}$ Number of LV buses Maximum power for each battery l [kW] Number of DC/DC converters $P_{i'i}^{LVmax}$ Maximum power of LV buses [kW] Number of batteries Parameters of EPS Capacity of battery l [kWh] ESS limitations Power limitations Maximum output power of generator j [kW] (continued on next column)

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| (continued)                           |                                        |                                                                                                            |
|---------------------------------------|----------------------------------------|------------------------------------------------------------------------------------------------------------|
|                                       | HI, LO                                 | Upper/Lower boundary for battery state of                                                                  |
|                                       | ALIV                                   | charge                                                                                                     |
| Efficiency                            | $\eta_j^{AHV}$                         | Transmission efficiency in cables between the APU bus and HV bus <i>j</i>                                  |
|                                       | $\eta_{jj'}^{HV}$                      | Transmission efficiency in cables of HV bus                                                                |
|                                       |                                        | connection between bus $j$ and $j'$                                                                        |
|                                       | $\eta_{jc}^{HVC}$                      | Transmission efficiency in cables between the                                                              |
|                                       | $n_c$                                  | HV bus <i>j</i> and DC/DC converter <i>c</i> Efficiency of DC/DC converter <i>c</i>                        |
|                                       | $\eta_c \atop LVC \atop \eta_{cl}$     | Transmission efficiency in cables between the                                                              |
|                                       | BAT                                    | DC/DC converter <i>c</i> and LV bus <i>l</i>                                                               |
|                                       | $\eta_{l}^{BAT}$ $\eta_{ll}^{LV}$      | Charging/ Discharging efficiency of battery <i>l</i> Transmission efficiency in cables of LV bus           |
|                                       | . HVI neri                             | connection between bus <i>l</i> and <i>l'</i>                                                              |
| Load priority                         | $\lambda_{jp}^{HVLncri}$               | The priority of the $p$ th non-critical load on HV bus $j$                                                 |
|                                       | $\lambda_{jq}^{LVLncri}$               | The priority of the $q$ th non-critical load on LV bus $l$                                                 |
| Parameters of DMPC/                   | CC-SMPC                                | bus t                                                                                                      |
| Load power                            | $P_{jp}^{HVLcri}(k)$                   | The $p$ th critical load power on HV bus $j$ [kW]                                                          |
|                                       | $P_{jp}^{HVLncri}(k)$                  | The <i>p</i> th non-critical load power on HV bus <i>j</i> [kW]                                            |
|                                       | $P_{lq}^{LVLcri}(k)$                   | The $q$ th critical load power on LV bus $l$ [kW]                                                          |
|                                       | $P_{lq}^{LVLncri}(k)$                  | The $q$ th non-critical load power on LV bus $l$ [kW]                                                      |
| Failure                               | $\gamma_j^G(k)$                        | Indicator for the failure of generator $j$                                                                 |
|                                       | $\gamma_{jc}^{HVC}(k)$                 | Indicator for the failure of connection link                                                               |
|                                       | $\gamma_{cl}^{LVC}(k)$                 | between HV bus <i>j</i> and DC/DC converter <i>c</i><br>Indicator for the failure of connection link       |
|                                       | $\gamma^{C}(k)$                        | between DC/DC converter <i>c</i> and LV bus <i>l</i> Indicator for the failure of DC/DC converter <i>c</i> |
|                                       |                                        |                                                                                                            |
| Continuous decision v                 | $\gamma_l^{BAT}(k)$ ariables of DM     | Indicator for the failure of battery <i>l</i>                                                              |
| Power                                 | $P^{APU}(k)$                           | Power flowing from APU to the APU bus [kW]                                                                 |
|                                       | $P_j^{AHVp}(k)$                        | Power flowing from APU bus to the HV bus $j$                                                               |
|                                       | $P_j^{AHVn}(k)$                        | [kW] Power flowing from HV bus <i>j</i> to the APU bus                                                     |
|                                       | $P_j^G(k)$                             | [kW] Power flowing from generator <i>j</i> to                                                              |
|                                       | »HV a >                                | corresponding HV bus j [kW]                                                                                |
|                                       | $P_{jj'}^{HV}(k)$                      | Power flowing from HV bus $j$ to HV bus $j'$ [kW]                                                          |
|                                       | $P_{jc}^{HVC}(k)$                      | Power flowing from HV bus <i>j</i> to DC/DC converter <i>c</i> [kW]                                        |
|                                       | $P_{cl}^{LVC}(k)$                      | Power flowing from DC/DC converter <i>c</i> to LV                                                          |
|                                       | $P_l^{BAT}(k)$                         | bus <i>l</i> [kW]  Power of battery on LV bus <i>l</i> [kW]                                                |
|                                       | $P_{ll'}^{LV}(k)$                      | Power flowing from LV bus <i>l</i> to LV bus <i>l'</i> [kW]                                                |
| ESS                                   | $SOC_l(k)$                             | State of charge of battery on LV bus <i>l</i>                                                              |
|                                       | $\vartheta_l(k)$                       | Tolerance for upper bound of state of charge of                                                            |
|                                       | $\varepsilon_l(k)$                     | battery on LV bus <i>l</i> Tolerance for lower bound of state of charge of                                 |
|                                       |                                        | battery on LV bus l                                                                                        |
| Binary decision variab<br>Connections | oles of DMPC/0<br>S <sup>APU</sup> (k) |                                                                                                            |
| Connections                           |                                        | Connection status of the link between APU and APU bus                                                      |
|                                       | $S_j^{AHV}(k)$                         | Connection status of the link between APU bus and HV bus $j$                                               |
|                                       | $S_j^G(k)$                             | Connection status of the link between generator $j$ and corresponding HV bus $j$                           |
|                                       | $S_{jj'}^{HV}(k)$                      | Connection status of the link between HV bus <i>j</i> and HV bus <i>j'</i>                                 |
|                                       | $S_{jp}^{HVLncri}(k)$                  | Connection status of the $p$ th non-critical load power on HV bus $j$                                      |
|                                       | $S_{jc}^{HVC}(k)$                      | Connection status of the link between HV bus $j$ and DC/DC converter $c$                                   |
|                                       | $S_{cl}^{LVC}(k),$                     | Connection status of the link between DC/DC                                                                |
|                                       | -RAT.                                  | converter c and LV bus l                                                                                   |

 $S_l^{BAT}(k)$ 

(continued on next column)

Connection status of the link between LV bus l

and its corresponding battery

#### (continued)

|                    | $S_{ll'}^{LV}(k)$     | Connection status of the link between LV bus $l$                                                                                               |
|--------------------|-----------------------|------------------------------------------------------------------------------------------------------------------------------------------------|
|                    | T T T                 | and LV bus <i>l'</i>                                                                                                                           |
|                    | $S_{lq}^{LVLncri}(k)$ | Connection status of the $q$ th non-critical load                                                                                              |
|                    |                       | power on LV bus l                                                                                                                              |
|                    | $S^{Phy}(k)$          | Vector of physical connections allowing bidirectional power, including $S_j^{AHV}(k)$ , $S_{jj}^{HV}(k)$ , $S_l^{BAT}(k)$ and $S_{lj}^{IV}(k)$ |
|                    | $S^h(k)$              | Vector of physical connections related with                                                                                                    |
|                    |                       | failure isolations, including $S_j^G(k)$ , $S_l^{BAT}(k)$ , $S_l^{HVC}(k)$ , and $S_c^{LVC}(k)$ .                                              |
| Indicator of Power | $s_i^{AHVp}(k)$       | Indicator for power flow direction from APU                                                                                                    |
| direction          | , i                   | bus to the HV bus j                                                                                                                            |
|                    | $s_i^{AHVn}(k)$       | Indicator for power flow direction from HV bus j                                                                                               |
|                    | ,                     | to the APU bus                                                                                                                                 |
|                    | $s_{jj}^{HV}(k)$      | Indicator for power flow direction HV bus j to                                                                                                 |
|                    | 2                     | HV bus j'                                                                                                                                      |
|                    | $s_{i'j}^{HV}(k)$     | Indicator for power flow direction HV bus j' to                                                                                                |
|                    | **                    | HV bus j                                                                                                                                       |
|                    | $s_{ll'}^{LV}(k)$     | Indicator for power flow direction from LV bus $l$                                                                                             |
|                    |                       | to LV bus l'                                                                                                                                   |
|                    | $s_{l'l}^{LV}(k)$     | Indicator for power flow direction from LV bus                                                                                                 |
|                    |                       | l' to LV bus l                                                                                                                                 |
|                    | $s_{ll}^{LV}(k)$      | Indicator for power flow direction from LV bus l                                                                                               |
|                    |                       | to LV bus l'                                                                                                                                   |
| DMPC/CC-SMPC con   | troller parame        | eters                                                                                                                                          |
| H                  |                       | Prediction horizon [min]                                                                                                                       |
| k                  |                       | Time intervals, $k \in \mathbb{Z}_{\geq 0}$                                                                                                    |
| $T_s$              |                       | Sampling time [min]                                                                                                                            |
| N                  |                       | Total time steps                                                                                                                               |

#### 1. Introduction

# 1.1. Background and motivation

Among transportation systems, the fastest developments have been observed in the aircraft industry, going from nothing to providing fast, safe, and the most far-reaching travel in only a little more than a century [1]. Environmental issues have been the main focus in the development of all transportation systems in recent years, and this speed of growth in air transportation came with its own environmental problems, since the aviation industry is currently responsible for approximately 2.5 % of global human-induced  ${\rm CO}_2$  emissions. Therefore, aircraft require more environmentally friendly power systems to meet current CO2 emission targets. For example, Airbus targets a 50 % reduction in net aviation carbon emissions from 2005 levels by 2050 [2]. To meet these requirements, the concept of more electric aircraft (MEA) has been put forward as the main development tendency for future aircraft, to make air travel more efficient and environmentally friendly.

MEA replace the traditional hydraulics and pneumatics systems with electrical systems to achieve better dynamic responses and higher efficiency, to reduce fuel consumption [3,4]. However, this brings new challenges to the design, control, and operational management of the onboard electrical power system (EPS). Recently, the concepts of flexible power transmission and intelligent application of energy storage (ES) are being explored for onboard EPS. Inspired by this, complex architectures are studied, which integrate generators, high-voltage (HV) and low-voltage (LV) buses, modular power electronic converters, and switches, as well as multiple energy storage systems (ESSs) and different types of loads with higher electrical power demands [5,6]. To cope with the increasingly complex onboard EPS, this paper proposes an optimization-based control strategy for onboard operational management of the MEA, including power distribution scheduling, load management, and energy storage (ES) control. The proposed control system is designed to achieve several control targets while satisfying several technical and operational constraints. In addition, in the EPS of the MEA there exist several safety issues that impose further critical operating requirements, such as faulty components/connections and uncertainties of load demands during the flight. The controller should be able to cope

with different failure scenarios, continuing to supply the safety-related loads. Furthermore, the EPS operational management is also subject to other sources of uncertainties that also need to be appropriately handled. Therefore, a system-level controller is required to meet these critical requirements and guarantee flight safety, while achieving optimized EPS performance under various fault and uncertainty scenarios.

#### 1.2. Literature review

Various controller designs have been proposed in the literature for system-level operational management of the EPS in the MEA, more electric vehicles (MEV), and terrestrial power grids. Although the operational requirements of an MEA are different from those of an MEV and a terrestrial grid, the latter two inspire the research for MEA applications. Rule-based methods in [7-10], and instantaneous optimization methods in [11–14] provide operational solutions based on only the past and current information of the system and cannot account for future system changes, to avoid adverse operating conditions in advance [15]. To avoid worst-case costly operating scenarios, model predictive control (MPC) has been drawing the attention of the power system community for optimal control and operational management of electrical systems [16]. Since MPC accounts for the system's future trajectory when deriving the optimal operating strategy, the worst-case situations can be avoided by taking corrective actions in advance. This benefits aircraft applications that must comply with strict safety requirements. In [17], MPC is used for optimal load management in an aircraft power distribution system while considering generator faults. A hierarchical MPC framework for hybrid propulsion systems is proposed in [18] to decouple the slow and fast response dynamic energy management problems. The MPC method is also applicable for decentralized control. In [19], a distributed MPC is proposed for controlling the engine and the connected electrical power distribution system of an MEA power system.

The abovementioned conventional MPC methods are deterministic, not taking the uncertainty explicitly into account, and are known as deterministic MPC (DMPC). Although the inherent feedback mechanism of DMPC, stemming from its receding horizon mechanism, makes the system more robust compared to other control methods [20,21], the uncertainty of the system can still cause the DMPC to be less effective in providing reliable control strategies than systems which explicitly account for uncertainty. For example, in [17], when DMPC is applied for optimal load management of an aircraft power distribution system, the uncertainty in load demand leads to poor performance due to the imprecise load predictions. Therefore, the authors in [22] propose a chance-constrained stochastic MPC (CC-SMPC) to solve the same onboard load management problem, considering either load demand uncertainty or contactor failures, but not taking both into account simultaneously. In addition, a simple conventional EPS architecture is considered, where the propagation of uncertainty is simplified, with independent loads and buses. Regarding terrestrial power grid applications, in [23-27], CC-SMPC is proposed for the coordinated energy management of microgrids. Although the multi-uncertainty scenario is discussed in these papers, a common grid is applied to compensate for all of the uncertainty, avoiding the interactions of flexible configurations, power scheduling, and uncertainty propagation. In the most recent studies, chance-constrained methods are proposed for unit commitment in [28,29], optimal scheduling of combined cooling, heating, and power-based microgrid in [30], and optimal energy-reserve scheduling of a wind-photovoltaic-hydrogen energy system in [31]. However, there remains a research gap for the management of onboard EPS architectures for MEA in more recent studies, such as the MEA EPS in [8]. Compared to the studies in [28-31] for terrestrial grids, novel onboard EPSs require further research on EPS operation strategies, simultaneously considering technical and safety requirements, the potential for multiple failures, and the propagation of the effects of uncertainty.

Rather than directly formulating uncertainty with chance constraints, some studies solve the optimization problem for several

| 1   1   1   1   1   1   1   1   1   1                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              | Reference  | Application <sup>(1)</sup> |    | Control target <sup>(2)</sup> | et <sup>(2)</sup> |     |    | Model type <sup>(3)</sup> | Failure | Failure and uncertainty case study | ainty cas | e study |                            |                                  | Results evaluation | aluation                |            |            |
|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------|----------------------------|----|-------------------------------|-------------------|-----|----|---------------------------|---------|------------------------------------|-----------|---------|----------------------------|----------------------------------|--------------------|-------------------------|------------|------------|
| MICHA   1                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |            |                            |    |                               |                   |     |    |                           | Failure |                                    | Uncerta   | tinty   |                            | Combined failure and uncertainty |                    |                         |            |            |
| MIEA         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x                                                                                           |            |                            | SS | L                             | PF                | ESS | ΓM |                           | Single  | Multi                              | Single    | Multi   | Uncertainty                |                                  | Number o           | f uncertainty scenarios | Performanc | ş          |
| MEA         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X                                                                                            |            |                            |    |                               |                   |     |    |                           |         |                                    |           |         | propagation <sup>(4)</sup> |                                  | Online             | Offline                 | Operating  | $CV^{(5)}$ |
| MEA         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X                                                                                            | [17]       | MEA                        | `  | `                             | `>                | `   | `  | D                         | `>      | ×                                  | ×         | ×       | ×                          | ×                                | Single             | ×                       | `          | ×          |
| MEA         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X                                                                                            | [18]       | MEA                        | ×  | ×                             | `                 | `   | `  | D                         | ×       | ×                                  | ×         | ×       | ×                          | ×                                | Single             | ×                       | `          | ×          |
| MEA         /                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      | [19]       | MEA                        | ×  | ×                             | `                 | `   | ×  | D                         | ×       | ×                                  | ×         | ×       | ×                          | ×                                | Single             | ×                       | `          | ×          |
| RG         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X                                                                                             | [22]       | MEA                        | `  | `                             | `                 | `   | `  | CC                        | ×       | `                                  | ×         | `       | ×                          | ×                                | Single             | ×                       | `          | ×          |
| RG         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X                                                                                             | [25]       | RG                         | ×  | ×                             | `                 | `   | ×  | CC                        | ×       | ×                                  | ×         | `       | ×                          | ×                                | Single             | ×                       | `          | ×          |
| RG         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X         X                                                                                             | [27]       | RG                         | ×  | ×                             | `                 | `   | ×  | CC                        | ×       | ×                                  | ×         | `       | ×                          | ×                                | Single             | Multiple                | `          | `          |
| RG         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x         / x | [28]       | RG                         | ×  | `                             | `                 | ×   | `  | CC                        | ×       | ×                                  | ×         | `       | `                          | ×                                | ×                  | Multiple                | `          | `          |
| RG         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x                                                                                             | [29]       | RG                         | `  | ×                             | `                 | ×   | `  | CC                        | ×       | ×                                  | ×         | `       | ×                          | ×                                | ×                  | Multiple                | `          | `          |
| RG         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x                                                                                             | [30]       | RG                         | ×  | ×                             | `                 | `   | ×  | CC                        | ×       | ×                                  | ×         | `       | ×                          | ×                                | Single             | A few                   | `          | ×          |
| RG         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x                                                                                             | [31]       | RG                         | ×  | ×                             | `                 | `   | `  | CC                        | ×       | ×                                  | ×         | `       | ×                          | ×                                | ×                  | A few                   | `          | ×          |
| RG       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x                                                                                                                                                                                                                 | [34]       | RG                         | ×  | ×                             | `                 | `   | ×  | SB                        | ×       | ×                                  | `         | ×       | ×                          | ×                                | Single             | ×                       | `          | ×          |
| RG       /       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x       x                                                                                                                                                                                                                 | [36]       | RG                         | ×  | ×                             | `                 | `   | ×  | SB                        | ×       | ×                                  | ×         | `       | `                          | ×                                | Single             | ×                       | `          | ×          |
| RG       / x / v / v / sB       x x x x / x / x / x x         RG       / v / v / v / sB       x x x / x / x / x x         RG       / x x / v / x / sB       x x x / x / x / x / x / x         MEA       x x x / x / x / x / x / x / x / x / x /                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    | [37]       | RG                         | `  | ×                             | `                 | `   | `  | SB                        | ×       | ×                                  | ×         | `       | ×                          | ×                                | ×                  | A few                   | `          | ×          |
| RG                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 | [38]       | RG                         | `  | ×                             | `                 | `   | `  | SB                        | ×       | ×                                  | ×         | `       | ×                          | ×                                | ×                  | A few                   | `          | ×          |
| RG     / x x / x x x x x x x x x x x x x x x x                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     | [38]       | RG                         | `  | `                             | `                 | `   | `  | SB                        | ×       | ×                                  | ×         | `       | `                          | ×                                | ×                  | A few                   | `          | `          |
| MEA         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x         x                                                                                            | [40]       | RG                         | `  | ×                             | `                 | `   | `  | SB                        | ×       | ×                                  | ×         | `       | ×                          | ×                                | ×                  | A few                   | `          | ×          |
| MEA / / / / CC X / X / / / Multiple                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                | [41]       | MEA                        | ×  | ×                             | `                 | `   | ×  | SB                        | ×       | ×                                  | ×         | `       | ×                          | ×                                | Single             | ×                       | `          | ×          |
|                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    | This paper | MEA                        | `  | `                             | `,                | `   | `  | CC                        | ×       | `                                  | ×         | `       | `                          | `                                | Multiple           | Multiple                | `          | `          |

a (1) In application, RG indicates residential grid. (2) In control targets, SS indicates power supply switching; TS indicates transmission switching; PF indicates power flow; ESS indicates energy storage systems; LM indicates load management. (3) In model types, D indicates deterministic model; CC indicates chance constrained model; SB indicates scenario based model. (4) Uncertainty propagation indicates that the uncertainty and in a multi-bus EPS, which allows different EPS configurations and corresponding uncertainty propagations. (5) CV indicates analysis and evaluations of constraints violation.

predefined uncertain scenarios at each time step, with each scenario integrating a DMPC model, which is named scenario-based (SB) SMPC. This method is applied for operational scheduling and hourly reconfiguration of the distribution systems in [32], the unit commitment and economic dispatch of power systems while considering the uncertainty of load and renewable energy sources in [33], and energy management of the ESS and distributed energy sources in [34–36]. In the most recent studies, scenario-based models are adopted for the scheduling and pricing strategies of smart grids. For example, SB models are proposed for the unit commitment of energy hubs in [37,38], for the scheduling of smart homes in [39], and for the scheduling of retailers with optimal prices in [40]. In these studies, the uncertainties of demands and different types of energy sources are also considered. In particular, the uncertainty of the market price is considered in [39] and [40]. In addition, in [41], a SB-SMPC strategy is proposed for coordinated engine and power management of the MEA. SB-SMPC methods heavily rely on the selection of predefined uncertain scenarios at each time step. More scenarios normally indicate a better representation of uncertainty; however, this can result in a dramatic increase in the computational time for large-scale systems or systems that contain a large number of decision variables - when fewer scenarios are generated, the desired performance cannot be guaranteed.

# 1.3. Research gap

By reviewing the above research, it can be found that, compared to the SB-SMPC, CC-SMPC is usually better for accurately formulizing uncertainties and saving computational time. However, there is still a large research gap in the literature regarding the operational control of an EPS (especially for MEA applications) considering multi-failure and multi-uncertainty scenarios with CC-SMPC in both methodology and evaluation aspects. Particularly, Table 1 systematically compares the literature related to MEA EPS and studies of other EPS within three years. From the literature review and Table 1, the research gap is concluded in detail below. To fill this gap, a CC-SMPC strategy for the EPS of an MEA is proposed in this paper to address the following issues.

Firstly, combining both normal and multi-failure scenarios simultaneously with multi-uncertainty issues under chance constraints has not been studied, despite this being increasingly important, but complex, as the MEA EPS develops. On the one hand, there are few studies focused on MEA EPSs, and the EPSs studied are conventional. However, future MEA EPSs tend to allow more flexible configurations and utilize the ESS for power scheduling, with modular converters and buses. This introduces various novel operational constraints, requiring appropriate formulizations to cope with both normal and failure scenarios, which have not been studied before. In addition, flexible configurations, power scheduling, and uncertainty propagation interact with each other, requiring these interactions to be modeled and analyzed to provide reliable decisions for the MEA EPS, which has been lacking in the previous studies.

Further, the performance of the CC-SMPC design should be properly evaluated, to demonstrate the online cost with testing scenarios which follow uncertainty distributions. This is required to verify the effectiveness of the proposed strategy for real-world applications. In the reviewed studies, online testing was conducted with only one uncertainty profile, however, this method lacks the verification of the impacts of the uncertainty distribution when the uncertainty profile changes. These impacts are not considered in the existing evaluation framework, leading to the incomplete performance evaluation for CC-SMPC compared with other methods. The analysis of the results for SMPC is mostly conducted with offline simulations or using only a limited number of online scenarios. However, the MPC mechanism for real-time operation management is based on the online scheme in practice, i.e., updating the EPS status and applying the optimized control strategy every time step. In addition, for time accumulated variables such as state of charge (SOC) of ESSs in SMPC, offline simulation is ineffective since the uncertainty accumulates with time and leads to system divergence.

#### 1.4. Contribution

In this paper, a CC-SMPC strategy is proposed to improve the performance of a novel modular-converter-based EPS with multiple ESSs on a MEA. This strategy is compatible with different EPS configurations. In addition to achieving multiple optimization objectives, the proposed CC-SMPC strategy is capable of coping with both normal and abnormal operating conditions. During the optimization, the CC-SMPC strategy simultaneously considers various uncertainty sources and multiple potential failures of system components. More specifically, this paper contributes to the following aspects:

- 1) It firstly proposes a DMPC strategy based on mixed-integer linear programming (MILP) for the novel multi-converter-based EPS on MEA. The goal is to minimize the power losses, the load shedding, and the switching activities in the system to avoid transients, and at the same time, maximize the energy stored in the ESSs. The EPS operation in both normal and faulty conditions is modeled by formulating several technical and operational constraints. These constraints are related to the: system configuration; bidirectionality of power flows; ESS; health status of system components; and potential failures.
- 2) A CC-SMPC model is proposed in the presence of uncertainty of load power demand on HV buses (HVBs) and LV buses (LVBs), again with potential failures. The probabilistic constraints on power limitations and ESS SOC are formulated based on the uncertainty propagations in the system, considering the power balance, components' health status, and system configuration. The proposed CC-SCMP model includes two main novelties. Firstly, different uncertainty sources are taken into account. The uncertainty on both HVBs and LVBs is studied in this paper, rather than focusing on the uncertainty of load power demand on a single bus. Secondly, the EPS configurations and power schedules are optimized for the minimum costs in both normal and faulty conditions under different uncertainty sources. In the meantime, the uncertainty can propagate differently in the system considering the interaction with the EPS configurations and power scheduling. These issues are addressed and formulated in the framework using mixed-integer quadratic programming (MIQP) optimization problems.
- 3) A comprehensive Monte-Carlo-based offline and online simulation is conducted for both DMPC and CC-SMPC methods, and the results are evaluated and compared using the proposed multi-dimensional evaluation framework. In this comparison, the multi-dimensional evaluation framework calculates operating costs, constraint violation costs, and total costs. In particular, instead of using a single index of average violation quantities, the constraint violation costs here consist of the average and maximum violation percentages and quantities.

The rest of this paper is organized as follows. The system configuration and the CC-SMPC controller framework are introduced in Section 2. Section 3 is dedicated to the mathematical formulation of the DMPC and modeling of the objective functions and constraints. In Section 4, the model presented in Section 3 is extended to CC-SMPC. In Section 5, simulation results for both DMPC and CC-SMPC methods are presented and evaluated for both normal and faulty operating conditions. The paper is concluded in Section 6.

# 2. System description and CC-SMPC framework

# 2.1. EPS description and assumptions

Fig. 1 presents a novel EPS architecture for an aircraft based on modular DC/DC power converters connecting the HV-side and LV-side

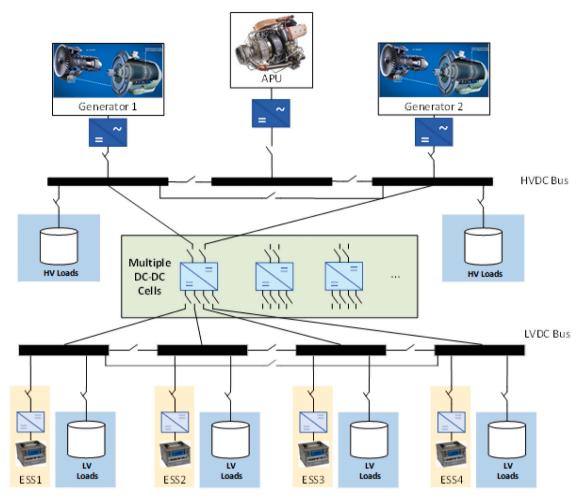


Fig. 1. A Novel EPS architecture for an aircraft based on modular converters and multiple ESSs.

of the system. In this architecture, two generators, with associated rectifiers, are connected to the corresponding HVDC buses, each of which supplies several HV loads. In addition, one auxiliary power unit (APU) is connected to the APU bus to support the system power demand under emergency conditions. For transferring power from the HV side to the LV side, several DC/DC power converters, called cells, can be connected to any HV/LV bus. Each LV bus is connected to an ESS comprised of one lithium-ion battery and one bidirectional DC/DC power converter, as well as several LV loads.

Two operating scenarios are considered for the EPS, namely the normal case and the faulty case, which are defined according to the health status of the EPS components. In the normal case, there is no failure in the system while in the faulty case, at least one of the system components is out of service. Accordingly, the role of the system components will change in different cases to ensure the power supply requirements of the HV/LV loads. A detailed description of the EPS components' roles is given in Table 2.

In an MEA system, the onboard loads usually change over time, according to different flight stages, including ground, take off, climb, cruise, descent, loiter, and landing [42]. The EPS loads include both critical loads and non-critical loads. The critical loads, such as flight control systems, essential environment control, and communication systems should be supplied in all scenarios. The non-critical loads can be shed, if needed, in failure scenarios which lead to a power shortage in the system. For the non-critical loads, load shedding can be conducted following load priorities, which means loads with higher priority are less preferred for load shedding. In this paper, the non-critical loads are classified into high priority loads (including the ice protection unit and

**Table 2** EPS components' roles in normal and faulty operation cases.

| EPS component         | Normal case                                                                                                                                                                                                                | Faulty case                                                                                                                                                                                                                                                                                      |
|-----------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Generators/<br>APU    | The HV/LV loads are supplied<br>by the two main generators. In<br>addition, the generator output<br>power is used to charge the<br>batteries according to the ESS<br>operating strategy.                                   | In a generator failure scenario,<br>the APU can be used to supply<br>the loads. The APU starting<br>time is assumed to be<br>negligible.                                                                                                                                                         |
| ESS                   | The ESS is used to stabilize the LV bus voltage and help to compensate for the power deviations between the loads and the power transferred by cells.                                                                      | ESSs can be used to supply LV loads when there is insufficient available power from the HV side, for instance in the case of failures in generators, transmission lines, and cells.                                                                                                              |
| Transmission<br>links | The LV buses are indirectly supplied by the generators, receiving power from HV buses via the transmission links. Each DC/DC cell can be connected to any HV or LV bus by controlling the on/off status of the contactors. | The multiple-cell architecture increases the flexibility and the reliability of the power transmission. For example, a failure in one contactor will not lead to the cell's unavailability for power transmission as the connections can be reconfigured to compensate for the failed contactor. |

the sheddable parts of the environment control), mid priority loads (such as some galley loads), and low priority loads (such as lights and entertainment loads). Load prediction techniques can be deployed to

predict the aircraft load under different flight stages. In [43,44], online machine learning-based methods are adopted for load prediction in residential microgrid systems, which can be extended to be applied to MEA EPS by combining the updates with the flight stages, height, and historical data, which is the future study of the authors. In contrast, this paper, a simplified load prediction method is adopted in which load prediction is obtained by sampling the historical load without adjustment for real-time flight power changes.

This paper proposes a system-level operational management strategy for MEA based on the CC-SMPC strategy coping with the uncertainty in the aircraft power consumption at both HV and LV sides under normal and abnormal operating conditions. The following assumptions are considered for designing the CC-SMPC-based control strategy.

- 1) This paper concentrates on the system-level MPC controller to optimize the long-term performance of the system. The transient/short-term system behaviors are dealt with by another layer of control, which is beyond the scope of this paper.
- 2) The loads at both HV and LV sides are assumed to have underlying predictable components and uncertain deviations from these, which can be modeled by a normal probability distribution function (PDF), as shown in [32–34].
- 3) When a failure happens in a component, it is assumed that the component will not be recovered within the flight time. In addition, three types of component failures are considered in this work, including one generator failure, one ESS failure, and different transmission link failures including failures in contactors and cells.

# 2.2. Chance-constrained SMPC framework

Fig. 2 represents the proposed CC-SMPC-based control framework for controlling the system operation. As illustrated in the figure, at each time step k, the CC-SMPC-based control system receives the current status of the external system, to update the internal system state and constraints of the controller. An online optimization problem is solved with multi objectives to obtain an optimized control sequence for k, k+1, ..., k+N-1, where N indicates the total time steps contained in the finite prediction horizon H. However, only the decisions for the first sample of the optimized control sequence are applied, and the horizon is shifted to the next time step k+1.

As presented in Fig. 2, at each time step k, the load prediction  $\mathbf{P}^{\mathrm{Load}}(k,k+1,...N-1)$  for the entire horizon is updated and sent to the controller, where  $\mathbf{P}^{\mathrm{Load}}$  indicates the matrix of predicted average and standard deviation (SD) values of all loads. In addition, based on the new measurements of the external system, received at the current time step, the internal system status is updated and shared with the controller. The

status information includes the failure status matrix  $\gamma$  consisting of all failures in the system, the connection status matrix S' consisting of the on/off status of all switches, and the SOC matrix consisting of SOC of all ESS. This information is used for updating the system dynamics and resetting the feasible region of the optimization problem included in the controller for the entire prediction horizon. After the optimization problem is solved, the CC-SMPC-based controller directly provides the reference values of the configuration/connection matrix S and power references of all cells  $P^{Cell}$  to the external system. Since the generators/APU and ESS are operated in voltage control mode, following the decided configurations, cells' power, and loads' power, other power flows in the system are determined based on Kirchhoff's Current Law.

#### 3. Problem formulation of DMPC

As represented in Fig. 2, the MPC-based controller solves an online optimization problem over the prediction horizon at each step to find the optimal operating strategy considering the desired performance and system constraints. Hence, it is essential to formalize the system operation rules and targets in the form of a simplified mathematical model that can be efficiently solved. In this section, the proposed model for the conventional MPC is formulized in the MILP framework, which can be solved using available solvers, such as CPLEX and GUROBI at each time step.

#### 3.1. Objective functions

For the optimal operational management of the EPS, the following four control targets are considered.

 Minimizing the total power from generators: Reducing the total output power of the generators is the first control goal in our formulation because this indicates less fuel consumption for the flight. Since the generator power is used for supplying loads and charging batteries, reducing the power losses for these two power consumers is a key objective.

$$J_{P^{G}} = \sum_{k=0}^{N-1} \frac{\sum_{j}^{N^{HVB}} P_{j}^{G}(k) + P^{APU}(k)}{N^{G} \bullet P^{Gmax}}$$
(1)

2) Maximizing the energy stored in the ESS: For ESS management, it is preferred to keep the aircraft battery at a high SOC value, within the target range, thereby being prepared for the abnormal conditions which may need the energy. To this aim, it is preferred to avoid

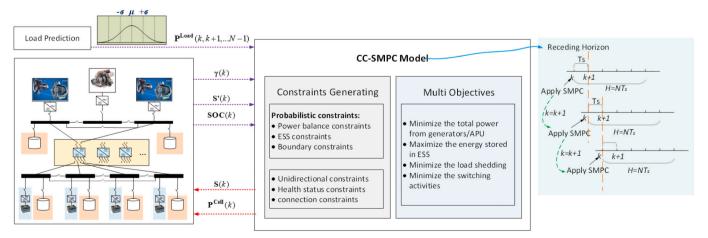


Fig. 2. Flowchart of the proposed online CC-SMPC optimization framework.

battery discharging, while battery charging is encouraged. The following cost function is therefore adopted for this target.

$$J_{pbat} = \sum_{k=0}^{N-1} \frac{\sum_{l}^{NLVB} - P_{l}^{bat}(k)}{N^{G} \bullet P_{l}^{batmax}}$$

$$\tag{2}$$

4) *Minimizing the switching activities*: Switching activities include the following changes: starting/stopping the APU, configuration changes in the transmission system (e.g. bus connections, cell connections,

$$J_{SL} = \sum_{k=0}^{N-1} \frac{\sum_{j}^{N^{HVB}} \sum_{p}^{N^{HVLncri}} \lambda_{jp}^{HVLncri} \left( -S_{jp}^{HVLncri}(k) \right) + \sum_{l}^{N^{LVB}} \sum_{p}^{N^{LVLncri}} \lambda_{jp}^{LVLncri} \left( -S_{jp}^{LVLncri}(k) \right)}{\sum_{j}^{N^{HVLncri}} \lambda_{jp}^{HVLncri}} \lambda_{jp}^{HVLncri} + \sum_{l}^{N^{LVB}} \sum_{p}^{N^{LVLncri}} \lambda_{jp}^{LVLncri}} \lambda_{jp}^{LVLncri}$$
(3)

3) Minimizing the load shedding: In EPS normal flight scenarios, the power demand should be completely satisfied at all times, as generators are designed to supply the entire aircraft load. When the EPS is in a failure case, the power supply might be insufficient to support all of the non-critical loads. Therefore, load shedding of lower priority can be conducted to maintain safe system operation. The cost function in (3) is related to minimizing the total load shedding by penalizing the shedding of loads, considering load priorities.

etc.), and connecting/disconnecting loads. Switching activities are preferred to be avoided in the EPS of the aircraft as they might lead to unnecessary transients and can negatively affect the components' lifetimes. Cost functions (4)–(6) are adopted to minimize various types of switching activities, which are allocated with different appropriate weighting factors.

$$J_{\delta APU} = \sum_{k=0}^{N-1} |S^{APU}(k+1) - S^{APU}(k)| \tag{4}$$

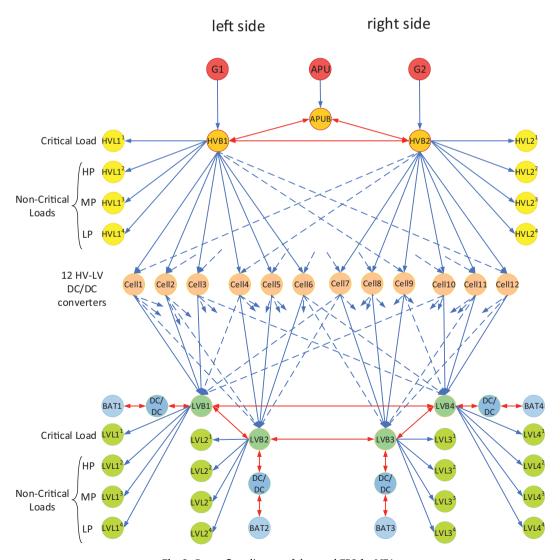


Fig. 3. Power flow diagram of the novel EPS for MEA.

$$J_{\delta tran} = \sum_{k=0}^{N-1} \sum |S^{tran}(k+1) - S^{tran}(k)|$$
 (5)

$$J_{\delta L} = \sum_{k=0}^{N-1} \sum_{k=0} |S^{L}(k+1) - S^{L}(k)|$$
 (6)

where  $S^{tran}$  denotes the vector of all power transmission lines switches. Similarly,  $S^L$  denotes the vector of all load switches.

Finally, a multi-objective cost function is formed by combining all the cost functions introduced in (1)–(6) with different weighting factors  $w_{P^G}$ ,  $w_{P^{but}}$ ,  $w_{SL}$ ,  $w_{\delta APU}$ ,  $w_{\delta tran}$ , and  $w_{\delta L}$  as follows.

$$Obj = w_{PG}J_{PG} + w_{Pbat}J_{Pbat} + w_{SL}J_{SL} + w_{\delta APU}J_{\delta APU} + w_{\delta tran}J_{\delta tran} + w_{\delta L}J_{\delta L}$$
(7)

#### 3.2. Constraints

According to the system descriptions in Section 2, the system's operational and technical requirements can be described as a set of constraints, including the power balance, power direction, connection restrictions, and battery dynamics. To better represent the potential power flow in the system, the power flow diagram in Fig. 3 is extracted from the system architecture shown in Fig. 1. System buses and components, such as generators, converters, batteries, and loads, are demonstrated as nodes. The potential power flows are demonstrated by lines with arrows, which show the direction of the power flow. The blue arrows demonstrate the unidirectional power flow, while the red arrows represent the bidirectional power flow.

#### 3.2.1. Power balance constraints

For each bus node in Fig. 3, the power flow should comply with Kirchhoffs Current Law for a given voltage [45], assuming no losses within the bus itself. For the converter nodes, the power flowing out of each node is less than the power flowing into it because of the converter efficiency. In addition, power losses across the transmission lines are considered by adding a transmission efficiency parameter to the model.

 Power balance for APU bus node: As represented in Fig. 3, the power from the APU is unidirectional, while the power from/to HV buses is bidirectional. The power flow in each direction is represented by a non-negative variable, hence the power balance equation can be written as follows:

$$P^{APU}(k) - \sum_{i}^{N^{HVB}} P_{j}^{AHVp}(k) + \sum_{i}^{N^{HVB}} \eta_{j}^{AHV} P_{j}^{AHVn}(k) = 0$$
 (8)

According to (8), the total power flowing from the APU and HV buses to the APU bus should be equal to the power that flows from APU bus to the HV buses.

2) Power balance for HV bus nodes: The input power for an HV bus includes the power from a generator, APU bus, and adjacent HV buses, while the output power includes the power to HV loads, cells, and adjacent HV buses as follows. Accordingly, the first three terms in (9) indicate the input power for an HV bus, while the following three terms indicate the output power.

$$\begin{split} P_{j}^{G}(k) + \left( \eta_{j}^{AHV} P_{j}^{AHVp}(k) - P_{j}^{AHVn}(k) \right) + \left( \sum_{j \neq j'} \left( \eta_{jj'}^{HV} P_{jj}^{HV}(k) - P_{jj'}^{HV}(k) \right) \right) \\ - \sum_{c}^{N^{C}} P_{jc}^{HVC}(k) - \sum_{p}^{N^{HVLncri}} P_{jp}^{HVLcri}(k) - \sum_{p}^{N^{HVLncri}} S_{jp}^{HVLncri}(k) P_{jp}^{HVLncri}(k) = 0 \end{split} \tag{9}$$

3) Power balance for cells' nodes: The cells operate in buck mode to transfer power from the HV side to the LV side, and the output power of each cell is less than its input power considering the power losses in the nodes (converter efficiency). This is presented in the following equation in which the first term is related to power flowing from HV buses to a DC/DC converter and the second term shows the power flowing from the DC/DC converter to LV buss.

$$\eta_{c} \sum_{j}^{N^{HVB}} \eta_{jc}^{HVC} P_{jc}^{HVC}(k) - \sum_{l}^{N^{LVB}} P_{cl}^{LVC}(k) = 0$$
 (10)

Power balance for LV bus nodes: For each LV bus, the battery operates in voltage control mode to maintain the voltage constant. Therefore, the power of the battery is determined by the power of the cells, adjacent LV buses, and LV critical and non-critical loads. The following equation shows the power balance for LV bus *l*.

$$\begin{split} &\sum\nolimits_{\rm{c}}^{{\rm{N}}^{C}} {\rm{\eta }}_{{\rm{cl}}}^{{\rm{LVC}}} {\rm{P}}_{{\rm{cl}}}^{{\rm{LVC}}}(k) + \sum\nolimits_{l \ne l^{'}} {\left( {{\rm{\eta }}_{{\rm{ll}}^{'}}^{{\rm{LV}}}{\rm{P}}_{l^{'}}^{{\rm{LV}}}(k) - {\rm{P}}_{{\rm{ll}}^{'}}^{{\rm{LV}}}(k)} \right) - {\rm{P}}_{\rm{l}}^{{\rm{BAT}}}(k)} \\ &- {\sum\nolimits_{q}^{{N^{{\rm{LVLcri}}}}} {P_{{\rm{lq}}}^{{\rm{LVLcri}}}(k)} - \sum\nolimits_{q}^{{N^{{\rm{LVLncri}}}}} {S_{{\rm{lq}}}^{{\rm{LVLncri}}}(k)P_{{\rm{lq}}}^{{\rm{LVLncri}}}(k)} = 0 \end{split} \tag{11}$$

In (11),  $\eta_{cl}^{LVC}$  and  $\eta_{ll'}^{LV}$  represent the transmission efficiency of cables between the DC/DC converters and LV buses and transmission efficiency in LV cables between buses l and l', respectively.

#### 3.2.2. ESS constraints

The SOC of the batteries is related to the energy stored in a system [46]. For each battery, the SOC at time step k + 1 can be estimated from the battery power and SOC value at the previous time step k, as shown below.  $\eta_l^{BAT}$  indicates the charging/discharging efficiency of the battery, which is related to the battery power [46], [47].

$$SOC_{l}(k+1) = SOC_{l}(k) + T_{s}\eta_{l}^{BAT}P_{l}^{BAT}(k)/B_{l}^{cap}$$

$$\tag{12}$$

In (12),  $P_l^{BAT}(k)$  represents the battery power, which is assumed to be positive/negative during charging/discharging, respectively. Battery  $SOC \in [0,1]$ , where SOC = 1 indicates a fully charged battery, while SOC = 0 corresponds to a depleted battery. In the aircraft system, SOC is preferred to be kept within a target range [LO, HI] following operational requirements. Therefore, the upper and lower bounds are defined in (13), where LO = 0.3 and HI = 0.9 are considered in this study, which can be presented as ESS constraints in the optimization problem.

$$LO \le SOC_l(k) \le HI$$
 (13)

#### 3.2.3. Boundary constraints

Each component and transmission link has its maximum power limitation for safe operation. For the potential unidirectional power flow, when its physical transmission path is connected, the power should not exceed its limits. Otherwise, the power is restricted to 0. Similarly, for the potential bidirectional power flow in bus interconnections, the power is limited by the direction indicator, i.e. only the selected power direction can reach its maximum power limitation. This set of constraints is presented below.

$$S(k) \times P_{min} \le P(k) \le S(k) \times P_{max}$$
(14)

where P(k) denotes the vector of power flow variables, and S(k) denotes the vector of binary variables of physical connections and power flow direction indicators. In (14),  $P_{max}$  and  $P_{min}$  denote the vector of the corresponding power limitations.

# 3.2.4. Unidirectional constraints

For the potential bidirectional power flow in bus interconnections, two non-negative variables are introduced to present the power flow in each direction separately. The unidirectional constraints in (15) combined with (14) are used to avoid power flow in both directions simultaneously. Eqs. (15) and (14) indicate that when the physical transmission link is connected, the power can be transferred in one of the directions with the given maximum value while the maximum power in the opposite direction is restricted to 0. When the physical transmission link is disconnected, power flow in both directions is set to zero. More modeling details and examples for boundary and unidirectional

constraints are discussed in our previous study in [48].

$$s^{p}(k) + s^{n}(k) \le S^{Phy}(k) \tag{15}$$

In (15),  $s^p(k)$  and  $s^n(k)$  are vectors of binary variables that denote positive and negative direction indicators respectively. For example,  $s^p(k)$  includes the direction indicator  $s_j^{AHVp}(k)$ ,  $s_{jj}^{HV}(k)$ , and  $s_{ll}^{LV}(k)$ , while  $s^n(k)$  includes the direction indicator  $s_j^{AHVn}(k)$ ,  $s_{jj}^{HV}(k)$ , and  $s_{ll}^{LV}(k)$ . In addition,  $s^{Phy}(k)$  is the vector of their corresponding physical connections.

# 3.2.5. Health status constraints

As mentioned in Section 2, several failure scenarios for system components such as generators, ESS, and cells, are considered in this study. To force the MPC controller to cope with the failure scenarios, the health status constraints in (16) are taken into account. These constraints indicate that the components can be connected when they are in the normal condition; otherwise, they will be forcibly disconnected in the case of failure.

$$S^h(k) \le \gamma(k) \tag{16}$$

Where  $S^h(k)$  denotes the vectors of connections status and  $\gamma(k)$  denotes the failure status matrix consisting of all failures in the system.

#### 3.2.6. Operational connection constraints

The power contactors should be connected properly to provide the system with all possible operation topologies and prevent unexpected connection conditions. The connection constraints are proposed mainly for APU connection, interconnection of buses, and the cells' connections as follows.

 In the generators' normal condition, APU is not used. In contrast, when any of the main generators fails, APU can be connected to the APU bus to further supply the HV buses:

$$S^{APU}(k) \le \left(1 - \prod_{j}^{N^{HVB}} \gamma_j^G(k)\right) \tag{17}$$

In (17), the multiplication operator in the right-hand side means that in case all generators are in healthy situation ( $\gamma_j^G(k) = 1$ ), connection of APU to the system is not allowed.

2) The HV bus interconnections are only allowed when any generator failure occurs. This is to stabilize the HV bus voltage and maintain critical HV loads. Besides, HV buses should be connected without creating loops:

$$\sum\nolimits_{j}^{N^{HVB}} S_{j}^{AHV}(k) + S_{jj}^{HV}(k) \le N^{HVB} \bullet \left(1 - \prod\nolimits_{j}^{N^{HVB}} \gamma_{i}^{G}(k)\right) \tag{18}$$

$$\sum\nolimits_{j}^{N^{HVB}} S_{j}^{G}(k) + S^{APU}(k) + \sum\nolimits_{j}^{N^{HVB}} S_{j}^{AHV}(k) + S_{jj}^{HV}(k) \leq N^{HVB} + N^{APU} \tag{19}$$

As presented in (18), if all values of  $\gamma_i^G(k^{HL})$  are 1, no HV/APU bus interconnection is allowed; otherwise, the maximum number of  $N^{HVB}$  bus interconnections can be adopted to connect the HV and APU buses. In addition, to avoid the loop, the number of faulty generators indicates the maximum number of bus interconnections required, as presented in (19).

 Each HV bus should be supplied by only one healthy power source, either one main generator or the APU, which is combined with constraints in (18)–(19):

$$1 \le S_i^G(k) + S_i^{AHV}(k) + S_{ii}^{HV}(k) \le 2$$
 (20)

As presented in (20), if  $S_j^G(k^{HL}) = 0$ , indicating a faulty generator j, then at least one bus interconnection (any of  $S_i^{AHV}(k^{HL})$  and  $S_{ij}^{HV}(k^{HL})$ )

will be adopted to supply the HV bus i.

4) Although the cells are flexible to be connected with any HV/LV bus, during the system operation, one cell cannot be connected with more than one HV and LV bus. These constraints are modeled by following equations:

$$0 \le \sum_{i}^{N^{HVB}} S_{jc}^{HVC}(k) \le 1 \tag{21}$$

$$0 \le \sum_{l} S_{cl}^{LVC}(k) \le 1 \tag{22}$$

5) The LV bus interconnections are only allowed when a battery failure occurs. This is to stabilize the LV bus voltage and maintain critical LV loads. LV buses should be connected without creating loops:

$$\sum_{l}^{N^{LVB}} S_{ll'}^{LV}(k) \le \left(N^{BAT} - 1\right) \left(1 - \prod_{j}^{N^{HVB}} \gamma_{l}^{BAT}(k)\right)$$
 (23)

$$\sum_{l}^{N^{LVB}} S_{l}^{BAT}(k) + \sum_{l}^{N^{LVB}} S_{ll}^{LV}(k) \le N^{BAT}$$
 (24)

In (23), the multiplication operator in the second term of the right-hand side means that when all batteries are in a healthy situation ( $\forall l \in \{1,...,N^{LVB}\}$ ,), LV bus interconnection is not allowed. Otherwise, a maximum number of  $N^{BAT}-1$  bus interconnections can be adopted, to connect the  $N^{BAT}$  LV buses. In addition, the number of faulty batteries indicates the maximum number of LV bus interconnections required to compensate, as presented in (24).

6) Each LV bus should be supplied with only one healthy battery, which is combined with constraints in (23)–(24):

$$S_{l}^{BAT}(k) + S_{l(l-1)}^{LV}(k) + S_{l(l+1)}^{LV}(k) \ge 1$$
 (25)

As presented in (25), if  $S_l^{BAT}(k) = 0$ , indicating a faulty battery l, then at least one LV bus interconnection will need to be adopted to supply the LV bus l.

# 4. Chance-constrained model predictive control

In the DMPC model discussed in Section 3, the power consumption of loads at both HV and LV sides is assumed to be equal to the predicted values without any deviations. However, uncertainty is an inseparable part of real systems and load forecasting is quite likely to be imperfect. Moreover, although most of the MEA loads are predictable, it is difficult to precisely predict the power consumption behavior of all parts of the system. This uncertainty in the system can lead to performance degradation of the conventional MPC methods that are based on the certainty equivalence principle. Hence, to overcome this limitation and explicitly take into account the uncertain nature of random parameters, CC-SMPC is used. In CC-SMPC, constraints with random variables are replaced with probabilistic constraints, which means instead of guaranteeing the satisfaction of system constraints at all time instances, it is guaranteed that the probability of satisfying the probabilistic constraints is higher than a pre-specified confidence level.

In this study, the CC-SMPC model is developed taking into account the load uncertainty at both HV and LV sides. Furthermore, multiple failures in generators, batteries, and transmission links from HV buses to LV buses are considered (e.g. failures in cells). This leads to the change of the model from a MILP problem to a Mixed-Integer Nonlinear Programming (MINLP) problem. Since solving an optimization problem with probabilistic constraints is a difficult task, probabilistic constraints are replaced with their deterministic counterparts using the probability distribution knowledge of the uncertain parameters. It should be mentioned that the proposed model is applicable for the EPS management in both normal and faulty scenarios.

#### 4.1. Uncertainty on the HV side with potential failures

Due to the forecasting errors, loads are commonly described with a Gaussian PDF with its mean value equal to the forecasted load [49]. In this study, Gaussian PDFs in (26) and (27) are assumed to represent the uncertainty of power demands of the HV loads, with mean values of  $\overline{P}_{jp}^{HVLcri}$  and  $\overline{P}_{jp}^{HVLcri}$  and SDs of  $\sigma_{P_{jp}^{HVLcri}}$  and  $\sigma_{P_{jp}^{HVLcri}}$ , for critical and noncritical loads, respectively.

$$P_{jp}^{HVLcri}(k) = \mathcal{GN}\left(\overline{P}_{jp}^{HVLcri}(k), \sigma_{P_{in}^{HVLcri}}(k)\right)$$
 (26)

$$P_{jp}^{HVLncri}(k) = \mathcal{SN}\left(\overline{P}_{jp}^{HVLncri}(k), \sigma_{P_{jp}^{HVLncri}}(k)\right)$$
 (27)

It should be mentioned that the uncertainty in each HV load affects only the associated HV bus and is compensated for by the generators/APU, while the power transferred to the cells is deterministic. This assumption is reasonable as generators and APU operate in the voltage control mode to compensate for the uncertainty, within their power capacity. Therefore, following the uncertainty propagation calculation rules in [50], the mean value of the generator power and the power of the HV interconnection buses is calculated according to (28). The SD of the generator output power is calculated using (29). The formulation indicates that when one generator is in a faulty condition, the other generator is required to compensate for load uncertainties on both HV buses if the buses are connected. Otherwise, if the HV buses are not connected, the generator in the normal condition only compensates for

In the case of generator failure, the APU can be connected to the system to replace the failed generator. Similar to the formulations in (28) and (29), the mean value of the APU output power is calculated using (30), which is based on (8). According to (28), the APU only compensates for the HV load uncertainties when it is connected to one failed bus, hence the SD value is calculated by (31).

$$\overline{P}^{APU}(k) = \sum_{j}^{N^{HVB}} \overline{P}_{j}^{AHVp}(k) - \sum_{j}^{N^{HVB}} \eta_{j}^{AHV} \overline{P}_{j}^{AHVn}(k)$$
(30)

Since the generator/APU output power is uncertain, the deterministic boundary constraints for its power limitation in (14) need to be replaced with the two probabilistic constraints introduced in (32), where  $\rho_j^{Pg}$  indicates the probability for the boundary violation. By adopting the linearization methods in [51], the probabilistic constraints in (32) are linearized using (33) and (34) for generators and APU respectively, where  $\sigma_{Pp}^{2g}(k)$  is calculated by (29), and  $\sigma_{PAPU}^{2g}(k)$  is calculated by (31). In (33) and (34), erf<sup>-1</sup> is the inverse error function.

$$Pr\left(-P_j^{Gmax} \le P_j^{G,APU}(k) \le P_j^{Gmax}\right) \ge 1 - \rho_j^{Pg}$$
 (32)

$$\sigma_{pj}^{2}(k) = \gamma_{j}^{G}(k) \bullet \begin{pmatrix} \left( \sum_{p}^{N^{HVLncri}} \sigma_{pjlVLcri}^{2}(k) + \sum_{p}^{N^{HVLncri}} S_{jp}^{HVLncri}(k) \sigma_{pjlVLncri}^{2}(k) \right) \\ + \frac{S^{HV}(k)}{\eta^{HV^{2}}} \left( \sum_{p}^{N^{HVLcri}} \sigma_{pjlVLcri}^{2}(k) + \sum_{p}^{N^{HVLncri}} S_{jp}^{HVLncri}(k) \sigma_{pjlVLncri}^{2}(k) \right) \\ + \frac{1 - S^{APU}(k)}{\left( \prod_{j}^{N^{HVB}} \eta_{j}^{AHV} \right)^{2}} \left( \sum_{p}^{N^{HVLncri}} \sigma_{pjlVLcri}^{2}(k) + \sum_{p}^{N^{HVLncri}} S_{jp}^{HVLncri}(k) \sigma_{pjlVLncri}^{2}(k) \right) \end{pmatrix}$$

$$(29)$$

the load uncertainties on its corresponding HV bus. Moreover, the generator in the faulty condition should be removed from the uncertainty propagation model.

$$\overline{P}_{j}^{G}(k) + \left(\eta_{j}^{AHV}\overline{P}_{j}^{AHVp}(k) - \overline{P}_{j}^{AHVn}(k)\right) + \left(\sum_{j \neq j} \left(\eta_{jj}^{HV}\overline{P}_{jj}^{HV}(k) - \overline{P}_{jj}^{HV}(k)\right)\right)$$

$$= \sum_{c}^{N^{C}} P_{jc}^{HVC}(k) + \sum_{p}^{N^{HVLcri}} \overline{P}_{jp}^{HVLcri}(k) - \sum_{p}^{N^{HVLncri}} S_{jp}^{HVLncri}(k) \overline{P}_{jp}^{HVLncri}(k)$$
(28)

$$-P_{j}^{Gmax} + \sqrt{2}\sigma_{P_{j}^{G}}(k)erf^{-1}\left(1 - 2\rho_{j}^{P_{g}}\right) \leq \overline{P}_{j}^{G}(k)$$

$$\leq P_{j}^{Gmax} - \sqrt{2}\sigma_{P_{j}^{G}}(k)erf^{-1}\left(1 - 2\rho_{j}^{P_{g}}\right)$$

$$(33)$$

$$-P_{j}^{Gmax} + \sqrt{2}\sigma_{P^{APU}}(k)erf^{-1}\left(1 - 2\rho_{j}^{P_{g}}\right) \leq \overline{P}^{APU}(k)$$

$$\leq P_{j}^{Gmax} - \sqrt{2}\sigma_{P^{APU}}(k)erf^{-1}\left(1 - 2\rho_{j}^{P_{g}}\right)$$

$$(34)$$

$$\sigma_{p^{APU}}^{2}(k) = S^{APU} \bullet \begin{pmatrix} \frac{1 - \gamma_{j}^{G}(k)}{\eta_{j}^{HV^{2}}} \left( \sum_{p}^{N^{HVLori}} \sigma_{p^{jhVLori}}^{2}(k) + \sum_{p}^{N^{HVLncri}} S_{jp}^{HVLncri}(k) \sigma_{p^{jhVLncri}}^{2}(k) \right) \\ + \frac{1 - \gamma_{j}^{G}(k)}{\eta_{j}^{HV^{2}}} \left( \sum_{p}^{N^{HVLori}} \sigma_{p^{jhVLori}}^{2}(k) + \sum_{p}^{N^{HVLncri}} S_{j}^{HVLncri}(k) \sigma_{p^{jhVLncri}}^{2}(k) \right) \end{pmatrix}$$

$$(31)$$

(38)

#### 4.2. Uncertainty on the LV side with potential failures

Similarly to the HV side uncertainty analysis, the uncertain parameters of the LV loads are assumed to follow a Gaussian PDF, with the mean value of  $\overline{P}_{lq}^{LVLncri}$  and  $\overline{P}_{lq}^{LVLncri}$  and SD of  $\sigma_{P_{lq}^{LVLnc}}$  and  $\sigma_{P_{lq}^{LVLncri}}$ , for critical and non-critical loads, respectively, as shown in (35) and (36).

$$P_{lq}^{LVLcri}(k) = \mathcal{GN}\left(\overline{P}_{lq}^{LVLcri}(k), \sigma_{P_{lq}^{LVLcri}}(k)\right)$$
(35)

$$P_{lq}^{LVLncri}(k) = \mathcal{M}\left(\overline{P}_{lq}^{LVLncri}(k), \sigma_{P_{lq}^{LVLncri}}(k)\right)$$
(36)

Since the power transferred to the cells is deterministic, batteries are used to compensate for the LV loads' uncertainty. In a normal operation scenario, the LV buses will not be interconnected (according to con-

By adopting linearization methods in [51], the probabilistic constraints can be replaced with two deterministic inequality constraints using the mean and SD values as shown in (43) and (44).

$$-P_{l}^{batmax} + \sqrt{2}\sigma_{P_{l}^{BAT}}(k)erf^{-1}\left(1 - 2\rho_{l}^{Pbat}\right) \leq \overline{P}_{l}^{BAT}(k)$$

$$\leq P_{l}^{batmax} - \sqrt{2}\sigma_{P_{l}^{BAT}}(k)erf^{-1}\left(1 - 2\rho_{l}^{Pbat}\right)$$
(43)

$$\begin{split} LO + \sqrt{2}\sigma_{SOC_l}(k+1)erf^{-1}\left(1 - 2\rho_l^{SOC}\right) &\leq \overline{SOC_l}(k+1) \\ &\leq HI - \sqrt{2}\sigma_{SOC_l}(k+1)erf^{-1}\left(1 - 2\rho_l^{SOC}\right) \end{split}$$

The complete CC-SMPC model includes the constraints in (8)–(25) as well as constraints (35)–(44). The objective function is similar to the

$$\sigma_{p_{lq}^{BAT}}^{2}(k) = \gamma_{j}^{BAT} \bullet \begin{pmatrix} \left( \sum_{q}^{N^{LVLcri}} \sigma_{p_{lq}^{LVLcri}}^{2}(k) + \sum_{q}^{N^{LVLncri}} S_{lq}^{LVLncri}(k) \sigma_{p_{lq}^{LVLncri}}^{2}(k) \right) \\ + \frac{S_{l-1}^{LV}}{\eta_{l(l+1)}^{LV}} \left( \sum_{q}^{N^{LVLcri}} \sigma_{p_{l(l+1)q}^{LVLcri}}^{2}(k) + \sum_{q}^{N^{LVLncri}} S_{(l+1)q}^{LVLncri}(k) \sigma_{p_{l(l+1)q}^{LVLncri}}^{2}(k) \right) \\ + \frac{S_{l-1}^{LV}}{\eta_{l(l-1)}^{LV}} \left( \sum_{q}^{N^{LVLcri}} \sigma_{p_{l(l-1)q}^{LVLcri}}^{2}(k) + \sum_{q}^{N^{LVLncri}} S_{(l-1)q}^{LVLncri}(k) \sigma_{p_{l(l-1)q}^{LVLncri}}^{2}(k) \right) \end{pmatrix}$$

straints (23)–(24)), and the LV loads' uncertainty for each LV bus is independent of that of adjacent LV buses. When one battery fails, the LV bus interconnection is required to maintain the bus voltage, using an adjacent healthy battery. Consequently, the connected adjacent battery will also compensate for the load uncertainties on the connected LV buses. Based on the power balance equation in (11), the mean and SD ( $\sigma_{P_i^{NAT}}(k)$ ) values of the batteries' power are calculated using (37) and (38), respectively, taking into account the uncertain load power propagation from the adjacent LV bus node.

$$\begin{split} \overline{P}_{l}^{BAT}(k) &= \sum\nolimits_{c}^{N^{C}} \eta_{cl}^{LVC} P_{cl}^{LVC}(k) + \sum_{l \neq i} \left( \eta_{ll'}^{LV} P_{l'1}^{LV}(k) - P_{ll'}^{LV}(k) \right) \\ &- \sum\nolimits_{q}^{N^{LVLcri}} \overline{P}_{lq}^{LVLcri}(k) - \sum\nolimits_{q}^{N^{LVLncri}} S_{lq}^{LVLncri}(k) \overline{P}_{lq}^{LVLncri}(k) \end{split}$$
(37)

Since the battery charge level is dependent on the integration of the battery power over time, the uncertain battery power will lead to a nondeterministic battery charge level. According to the battery SOC dynamic equation in (12), the mean and SD values of SOC can be calculated by (39) and (40), respectively.

$$\overline{SOC_l}(k+1) = \overline{SOC_l}(k) + \frac{\overline{P_l}^{BAT}(k)}{P_l^{cap}} T_s \eta_l^{BAT}$$
(39)

$$\sigma_{SOC_{l}}^{2}(k+1) = \sigma_{SOC_{l}}^{2}(k) + \left(T_{s}\eta_{l}^{BAT}\right)^{2}\sigma_{P^{BAT}}^{2}(k) / B_{l}^{cap^{2}}$$
(40)

Accordingly, the constraints related to the SOC target range (13) as well as the boundary constraints of the battery power (14) are replaced with probabilistic constraints in (41)–(42). Satisfying the probabilistic constraints ensures that the probability of not violating the constraints will be higher than a pre-specified confidence level corresponding to constraint violations of  $\rho_1^{SOC}$  and  $\rho_2^{Pbat}$ .

$$Pr(LO \le SOC_l(k+1) \le HI) \ge 1 - \rho_l^{SOC} \tag{41}$$

$$Pr\left(-P_l^{batmax} \le P_l^{BAT}(k) \le P_l^{batmax}\right) \ge 1 - \rho_l^{Pbat} \tag{42}$$

DMPC model in (1)–(7), where the uncertain variables are replaced with their mean values.

# 5. Simulation results

In this section, simulation results of system operation are evaluated and compared for two cases, i.e. the normal case, and the faulty case, when adopting the proposed CC-SMPC and the DMPC strategies. For each case, both offline (single independent evaluations) and online (evaluation within a receding horizon approach) comparison results are presented and analyzed. For the offline test, the CC-SMPC and DMPC strategies are applied to the system for a flight duration of T = 150 min, considering the forecasted loads of the system at both the HV and LV sides. Afterwards, the performance of both control strategies is evaluated under the realization of a large number of random load scenarios. The offline results provide quick verification for the CC-SMPC for power limitation violation analysis as the offline tests are quicker to execute and hundreds of load scenarios can be considered. However, offline testing is not applicable for the assessment of SOC violation and costs of system operation, because the SOC is a time accumulating variable. Therefore, offline testing leads to accumulating SOC deviations due to the lack of ability to take corrective actions. In contrast, online testing is applicable for the evaluation of all violation analyses and the real cost of system operation. However, the computational time is considerably increased for testing hundreds of load scenarios. Hence, for the offline tests, 100 uncertain load profiles are tested in which the maximum acceptable probability of constraint violations varies from 0.01 to 0.2, to verify the feasibility of the model. In the second step, the online DMPC and CC-SMPC techniques are applied for 50 uncertain load profiles with a selected probability, which compares the total cost of system operation and the penalty costs of constraint violations.

# 5.1. Introduction of the evaluation framework

The predicted load profile (mean value of each load's power) in Fig. 3 is presented in Table 6 in Appendix A. The maximum load power is 95 kW when the aircraft enters the cruising stage, while the rated generator

power is 97 kW, which is designed to cover all of the MEA loads. The rated power for each DC/DC converter is 3 kW, and the battery capacity of all batteries is 4kWh.

For comparing the online DMPC and CC-SMPC performance, an evaluation framework for comparing the system operation costs and constraint violation costs is proposed in the following, which evaluates the system performance for  $N^{Scen}=50$  load scenarios over the whole flight duration T=150 min. The operation costs are calculated based on the controller objectives, which contain power losses, stored energy, load shedding, and switching activities.

The power loss for each single time step of each testing scenario equals the difference between the total supplied power in (45) and the total load power (47). The power losses for all of the scenarios during the entire flight are evaluated by (45), calculating the average power loss, which is weighted by the weighting factor  $\nu_{Ploss}$ .

$$Q_{ploss} = v_{ploss} \left( \frac{1}{N^{Scen} T} \sum_{n=1}^{N^{Scen}} \sum_{k=0}^{T} \left( P_n^{source}(k) - P_n^{load}(k) \right) \right)$$
(45)

$$P_{n}^{source}(k) = \sum_{j}^{N^{HVB}} P_{j}^{G}(k) + P^{APU}(k) - \eta_{l}^{BAT} \sum_{l}^{N^{LVB}} P_{l}^{BAT}(k)$$
 (46)

configuration changes in the transmission system ( $J_{\delta tran}$ ), and loads on/off status ( $J_{\delta L}$ ), respectively.

$$Q_{SL} = v_{SL} \frac{1}{N^{Scen}} T \sum_{n=1}^{N^{Scen}} J_{SL}$$

$$\tag{49}$$

$$Q_{\delta} = \frac{1}{N^{Scen}T} \left( v_{\delta APU} \sum_{n=1}^{N^{Scen}} J_{\delta APU} + v_{\delta tran} \sum_{n=1}^{N^{Scen}} J_{\delta tran} + v_{\delta L} \sum_{n=1}^{N^{Scen}} J_{\delta L} \right)$$
(50)

As demonstrated in Section 4, the uncertainty in the system can lead to potential violations of three constraints, including the violation of SOC target range, the violation of maximum power bounds for batteries, and the violation of maximum power bounds for generator/APU output power. To better evaluate the violation cost, instead of using only one average value or maximum value, a new index is proposed that combines four dimensions. The violation cost is calculated based on the maximum percentage of constraint violation in all scenarios over all time steps  $(Q_{mp})$ , the average percentage of constraint violation over all time steps with violation occurrence ( $Q_{*ap}'$ ), the maximum constraint violation value ( $Q_{mq}$ ), and the average constraint violation value across all time steps with violation occurrence and scenarios ( $Q_{*aq}'$ ). For example, (51)-(54) are adopted for the SOC violation cost based on the aforementioned evaluation scheme, and the overall cost is calculated by their combination with weights presented in (55). In this work, it is assumed that each type of violation is weighted equally, which indicates that  $v_{SOCmp} = v_{SOCmp} = v_{SOCmq} = v_{SOCap} = 0.25$ . Similarly, the violation

$$Q_{SOC_{aq}}^{'} = \frac{\sum_{\left\{SOC_{n,l}(k) \in S: \left(SOC_{n,l}(k) > HI \lor SOC_{n,l}(k) < LO\right)\right\}} \left(\left|SOC_{n,l}(k) - HI\right| + \left|SOC_{n,l}(k) - LO\right|\right)}{\sum_{\left\{n \in S: \left(SOC_{n,l}(k) > HI \lor SOC_{n,l}(k) < LO\right)\right\}}}$$

$$(54)$$

$$\begin{split} P_{n}^{load}(k) &= \sum_{p}^{N^{HVLncri}} P_{jp}^{HVLcri}(k) + \sum_{p}^{N^{HVLncri}} S_{jp}^{HVLncri}(k) P_{jp}^{HVLncri}(k) \\ &+ \sum_{q}^{N^{LVLcri}} P_{lq}^{LVLcri}(k) + \sum_{q}^{N^{LVLncri}} S_{lq}^{LVLncri}(k) P_{lq}^{LVLncri}(k) \end{split} \tag{47}$$

The ESS performance is evaluated by SOC level in (48) for all load scenarios, which indicates that a better performance for the SOC average value is one that is closer to the upper bound within the target range.

$$Q_{SOC} = v_{SOC} \left( \frac{1}{N^{Scen} T} \sum_{n=1}^{N^{Scen}} \sum_{k=0}^{T} \sum_{l}^{N^{BAT}} |HI - SOC_{n,l}(k)| \right)$$
(48)

The performances for load shedding and switching activities are evaluated by similar methods, which are used in calculating the objective functions (3)–(6), considering the entire flight duration. Based on this, the load shedding and switching activity evaluations for all test scenarios are conducted by calculating the average load shedding for each scenario using (49) and the average switching activities using (50), with  $\nu_{SL}$ ,  $\nu_{\delta APU}$ ,  $\nu_{\delta tran}$ , and  $\nu_{\delta L}$  being the weighting factors for the relative importance of load shedding ( $J_{SL}$ ), starting/stopping the APU ( $J_{\delta APU}$ ),

cost for battery power  $Q_{pba'}$  (including  $Q_{pballmp'}$ ,  $Q_{pballmp'}$ ,  $Q_{pballmp'}$ ,  $Q_{pballmp'}$ ,  $Q_{pballmp'}$ ,  $Q_{pballmp'}$ , and generator/APU output power  $Q_{pga'}$  (including  $Q_{pganlmp'}$ ,  $Q_{pganlmp'}$ ,  $Q_{pganlmp'}$ ,  $Q_{pganlmp'}$ , can be calculated by the same method (for the sake of brevity, detailed functions are omitted).

$$Q'_{SOCmp} = \frac{max\{n : n \in S : \left(SOC_{n,l}(k) > HI \lor SOC_{n,l}(k) < LO\right)}{N^{Scen}}, k$$

$$\in \{1, 2, ..., T\}\}$$
(51)

$$Q_{SOCap}^{'} = \frac{\sum \left\{ n \in S : \left( SOC_{n,l}(k) > HI \lor SOC_{n,l}(k) < LO \right) \right\}}{N^{Scen} \sum \left\{ k \in S : \left( SOC_{n,l}(k) > HI \lor SOC_{n,l}(k) < LO \right) \right\}}$$
(52)

$$Q'_{SOCmq} = max \left\{ SOC_{n,l}(k) : SOC_{n,l}(k) \in S : \left( SOC_{n,l}(k) > HI \lor SOC_{n,l}(k) \right.$$

$$\left. < LO \right), k \in \left\{ 1, 2, ..., T \right\} \right\}$$
(53)

Table 3
Constraint violation comparisons of CC-SMPC and DMPC methods for Case 1 offline tests.

| Case    | ρ    | Battery power                   | er constraint violati             | ion                                        |                                | Generator po          | Generator power constraint violation   |                                             |                             |  |  |
|---------|------|---------------------------------|-----------------------------------|--------------------------------------------|--------------------------------|-----------------------|----------------------------------------|---------------------------------------------|-----------------------------|--|--|
|         |      | $\overline{Q_{P^{bat}mp}}'$ [%] | $Q_{P^{bat}ap}{}^{\prime} \ [\%]$ | $Q_{P^{bat}mq}{}^{\prime} \ [\mathrm{kW}]$ | $Q_{P^{bat}aq}{}'\ [	ext{kW}]$ | $Q_{P_8^{en}mp}'$ [%] | $Q_{P^{\mathrm{gen}}a\mathrm{p}'}$ [%] | $Q_{P^{\mathrm{gen}}mq}{}' \ [\mathrm{kW}]$ | $Q_{P^{\otimes m}aq'}$ [kW] |  |  |
|         | 0.01 | 3                               | 1.94                              | 0.2318                                     | 0.0776                         | 0                     | 0                                      | 0                                           | 0                           |  |  |
| CC CMPC | 0.05 | 8                               | 4.69                              | 0.2940                                     | 0.0739                         | 5                     | 4.41                                   | 1.3088                                      | 0.7068                      |  |  |
| CC-SMPC | 0.1  | 14                              | 8.95                              | 0.4262                                     | 0.0745                         | 12                    | 11.6                                   | 2.0288                                      | 0.7457                      |  |  |
|         | 0.2  | 24                              | 17.23                             | 0.4821                                     | 0.0921                         | 22                    | 20.21                                  | 2.9007                                      | 1.0991                      |  |  |
| DMPC    | N.A  | 56                              | 42.61                             | 0.7743                                     | 0.1235                         | 35                    | 27.34                                  | 3.6729                                      | 1.2533                      |  |  |

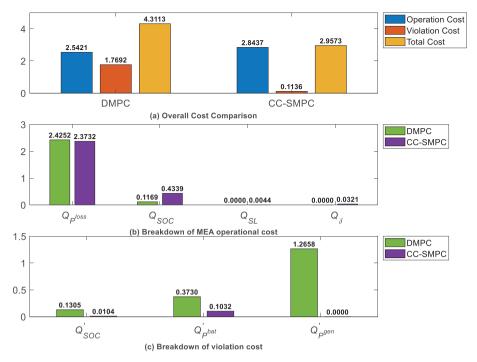


Fig. 4. Cost comparison of online DMPC and CC-SMPC for Case 1.

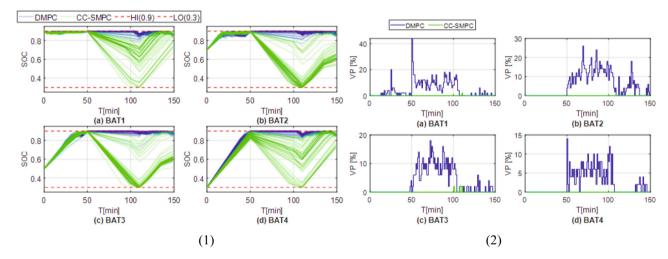


Fig. 5. SOC results when adopting online DMPC and CC-SMPC for Case 1: (1) SOC changes; (2) Violation Percentage across all uncertain scenarios.

$$\begin{cases} Q'_{SOC} = v_{SOCmp} Q'_{SOCmp} + v_{SOCap} Q'_{SOCap} + v_{SOCmq} Q'_{SOCmq} + v_{SOCap} Q'_{SOCaq} \\ v_{SOCmp} + v_{SOCap} + v_{SOCmq} + v_{SOCap} = 1 \end{cases}$$

$$(55)$$

# 5.2. Simulation results and analysis

In the following subsections, simulation results for each case mentioned in Section 4 are evaluated and discussed.

# 5.2.1. Simulation results for Case 1

# 1) Offline scenario tests

Table 3 presents the offline simulation results for constraint violation comparisons of battery and generator power with both CC-SMPC and DMPC methods for Case 1. For the CC-SMPC approach, the test is

conducted with different violation probabilities of 1 %, 5 %, 10 %, and 20 %, with 100 uncertain load scenarios being applied for each violation probability. For the DMPC, the same uncertain load scenarios are used. According to Table 3, when  $\rho$  increases, the constraint violation indices for the battery and generator power increase by approximately the same percentage, which verifies the effectiveness of the proposed probabilistic approach for Case 1. In addition, compared to offline DMPC, all offline CC-SMPC with the selected  $\rho$  reduces the power constraint violations for batteries and generators.

# 2) Online scenario tests

Since generators are the only power supplies to the HV loads, to avoid generator overload situations due to the uncertain behavior of loads on the HV side, the CC-SMPC-based controller allocates less power to be transferred from the HV side to the LV side. This means that more energy from batteries will be used for supplying LV loads during peak

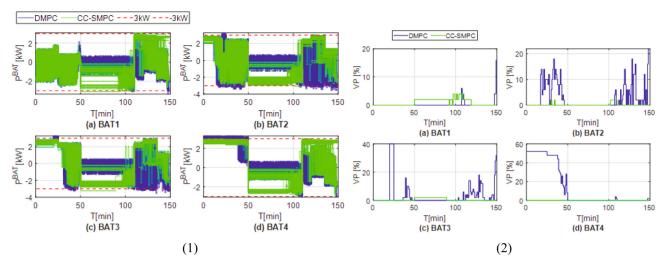


Fig. 6. Battery power results when adopting online DMPC and CC-SMPC for Case 1: (1) Battery power changes; (2) Violation Percentage among all uncertain scenarios.

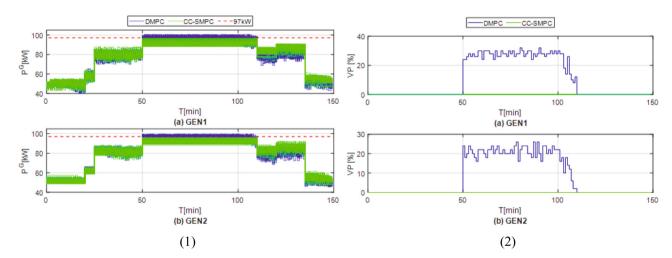


Fig. 7. Generator power results when adopting online control of DMPC and CC-SMPC for Case 1: (1) Generator power changes; (2) Violation Percentage among all uncertain scenarios.

**Table 4** Failure events during the flight.

| Time of fault occurrence | 19 min      | 49 min  | 79 min | 109 min   |
|--------------------------|-------------|---------|--------|-----------|
| Failed Component         | Generator 1 | Cell 10 | Cell 3 | Battery 2 |

intervals. In the worst scenarios, such as low SOC conditions and high load power demand, load shedding can also be triggered. These actions lead to a higher operational cost due to low SOC levels and the

additional cost imposed by load shedding and switching activities. The performance of the DMPC and CC-SMPC methods is evaluated using 50 uncertain load scenarios. Fig. 4 demonstrates the operation cost and the violation cost for both control strategies. In general, the CC-SMPC method leads to a higher operation cost, 12 % more than the cost of the DMPC method. According to the breakdown of the operation cost represented in Fig. 4(b), the CC-SMPC strategy has an almost 3 times higher SOC cost compared to the DMPC method and a small amount of cost due to the load shedding and switching activities. However, compared to the DMPC method, the CC-SMPC reduces the total violation

**Table 5**Constraint violation comparisons of CC-SMPC and DMPC for Case 2 offline tests.

| Case    | $\rho$ | Battery power                  | er constraint violatio       | on                              |                                       | Generator/A                    | PU power constrain             | t violation             |                       |
|---------|--------|--------------------------------|------------------------------|---------------------------------|---------------------------------------|--------------------------------|--------------------------------|-------------------------|-----------------------|
|         |        | Q <sub>Pbatmp</sub> '<br>Max % | $Q_{P^{bat}ap}{}'$<br>Mean % | $Q_{P^{bat}mq}{}^{\prime}$ [kW] | $rac{Q_{P^{bat}aq}'}{[\mathrm{kW}]}$ | Q <sub>Psen</sub> mp'<br>Max % | Q <sub>P∞nap</sub> ′<br>Mean % | $Q_{P^{gen}mq}' \ [kW]$ | $Q_{P^{gen}aq}'$ [kW] |
|         | 0.01   | 3                              | 1.86                         | 0.2283                          | 0.0627                                | 0                              | 0                              | 0                       | 0                     |
| OC CMPC | 0.05   | 8                              | 4.92                         | 0.2684                          | 0.0927                                | 5                              | 3.52                           | 1.2834                  | 0.7009                |
| CC-SMPC | 0.1    | 14                             | 11.23                        | 0.5197                          | 0.0887                                | 12                             | 5.51                           | 2.0106                  | 0.7237                |
|         | 0.2    | 24                             | 15.46                        | 0.4942                          | 0.0889                                | 21                             | 7.84                           | 2.8913                  | 0.9843                |
| DMPC    | N.A    | 57                             | 31.38                        | 0.7473                          | 0.1110                                | 47                             | 12.97                          | 4.5762                  | 1.4077                |

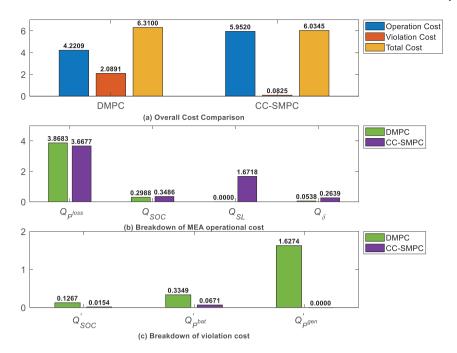


Fig. 8. Cost comparison of online DMPC and CC-SMPC for Case 2.

cost by 93 %, which contains a 92 % reduction in SOC violation, 72 % reduction in battery power violation, and 100 % reduction in generator power violation cost. This indicates that the DMPC method requires corrective control actions to cope with the constraint violation situations, which will lead to additional operating costs. However, this is less likely to be an optimal solution compared to the CC-SMPC method, which directly provides the solutions which explicit account for the violation probability of uncertain parameters. According to the results, CC-SMPC reduces the total cost by 31.4 % compared to the DMPC method. Fig. 5 to Fig. 7 represent the simulation results of both control techniques in all scenarios during different flight stages.

Fig. 5 presents the SOC changes over time, along with the violation percentage for both DMPC and CC-SMPC strategies when each controller is applied for 50 uncertain scenarios. Fig. 6 shows the corresponding battery power. The SOC value for batteries 1 to 4 vary from high to low levels of 0.9, 0.7, 0.5, and 0.3, respectively. As can be seen in both figures, before t=50 min, when the system loads are not in their peak interval, batteries 2–4 can be charged with generator power, while battery 1 is not charged as its initial value has been set to the maximum

SOC. However, compared to the CC-SMPC method, during the charging period, the DMPC method is more likely to charge the battery with higher power values exceeding the charging power limitation (3 kW in this paper). For example, battery 4 is charged with the maximum power of around 3.3 kW by the DMPC method in 52 % of scenarios before t =45 min. Likewise, for batteries 2 and 3, during t = 20 to t = 25 min, the charging power reaches 3.3 kW in 40 % and 14 % of scenarios, respectively. In addition, when the SOC becomes close to the upper bound, the battery is discharged in some time steps, to keep the SOC value within the target range. Compared to CC-SMPC, the DMPC method tends to discharge the battery with discharging power exceeding its maximum possible power limit, which indicates that the constraint violation would require corrections by adding a local controller in real situations. For example, batteries 2 and 3 have around 20 % overdischarge scenarios with a maximum power of 3.6 kW after t = 100min. Compared to the battery power violation, although DMPC causes SOC violation in 10 %–40 % of scenarios from t = 50 min to t = 100 min, the violation level is low (rising to approximately 0.902) with respect to the upper bound (HI = 0.9) which is due to the MPC online feedback

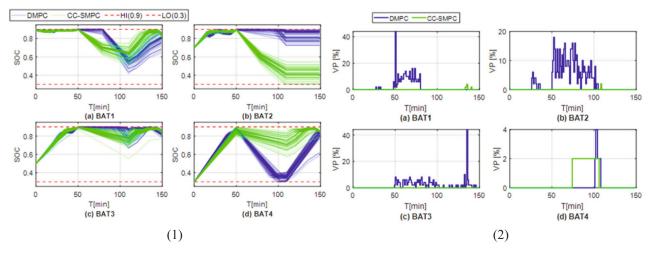


Fig. 9. SOC results when adopting online DMPC and CC-SMPC for Case 2: (1) SOC changes; (2) Violation Percentage among all uncertain scenarios.

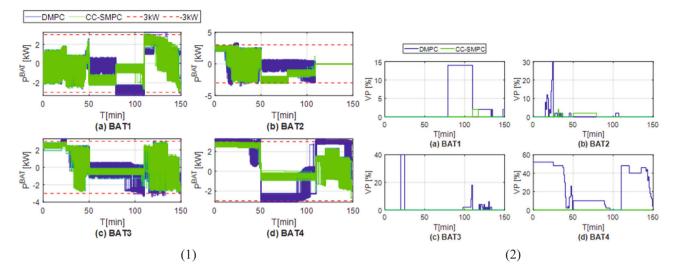


Fig. 10. Battery power results when adopting online control of DMPC and CC-SMPC for Case 2: (1) Battery power changes; (2) Violation Percentage among all uncertain scenarios.

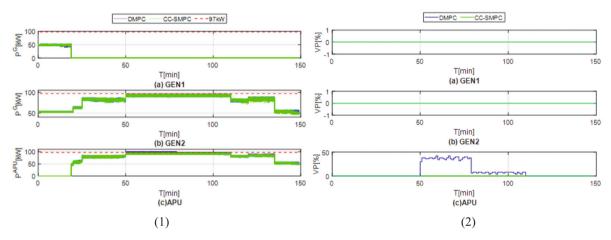


Fig. 11. Generator power results when adopting online control of DMPC and CC-SMPC for Case 2: (1) Generator power changes; (2) violation percentage among all uncertain scenarios.

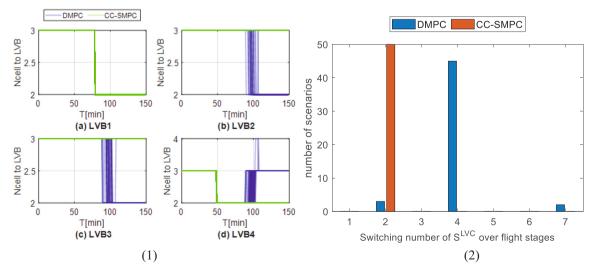


Fig. 12. Connections of cells to LV buses when adopting online DMPC and CC-SMPC for Case 2: (1) number of cells connected to each LV bus in all flight stages; (2) number of switching activities related to the connections of cells to LV bus.

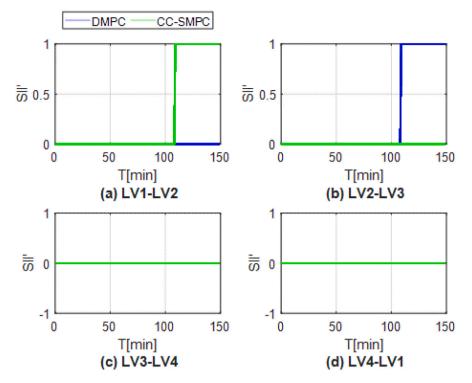


Fig. 13. Connections of LV bus interconnections with online DMPC and CC-SMPC for Case 2.

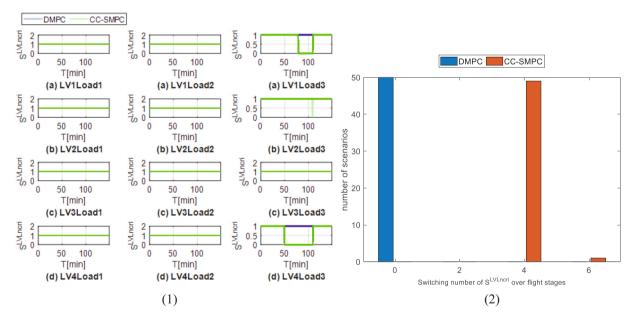


Fig. 14. Connection of LV noncritical loads when adopting online DMPC and CC-SMPC for Case 2: (1) load shedding in all flight stages; (2) number of switching activities of all LV noncritical loads.

strategy. Both DMPC and CC-SMPC online methods will correct for the SOC violation once it has occurred, however, predicting and avoiding the violation before it occurs would be far more preferable.

Fig. 7 presents both the output power of both generators and the violation percentage for the aforementioned 50 scenarios when adopting the DMPC and CC-SMPC methods. The figure shows that the power of the generators reaches its peak value from t=50 to t=110 min. Compared to the DMPC method, the CC-SMPC strategy controls generator operation without exceeding their maximum power limitations, discharging the batteries when necessary during this period. Although the DMPC strategy relies less on the batteries' power to avoid costs

imposed by low SOC levels, it violates the generators' power limitation in 30 % of scenarios, with a maximum power of around 100.3 kW for each generator. This indicates two potential drawbacks of the application of the DMPC method. One solution is to add another layer of control, such as a rule-based control layer, to work out how to compensate to satisfy the operation costs. This is likely to result in sub-optimal solutions. Another solution would be to design larger generators, with more power redundancy, leading to an increase in the generators' weight, which is not desirable in aircraft applications. Therefore, the CC-SMPC method can be considered to show its superiority compared to the DMPC method for Case 1 by avoiding both drawbacks.

#### 5.2.2. Simulation results for case 2

In Case 2, in addition to considering the load uncertainty at both HV and LV sides, the following failure scenarios are also assumed during different flight stages: failure events for Generator 1, Cell 10, Cell 3, and Battery 2. In this case, in contrast to the previous cases which studied only normal system operation, the DMPC and CC-SMPC strategies are required to provide feasible solutions to cope with the components' failures. The failures listed in Table 4 are included in both offline and online testing and are assumed to occur at the specified time instances.

# 1) Offline scenario tests

Table 5 presents the offline simulation results for constraint violation comparison of battery and generator power with both CC-SMPC and DMPC methods for Case 2. In the failure scenarios, the APU is started in all tests with both the CC-SMPC and DMPC methods to avoid the load shedding caused by generator failure (which is further presented in the online results). The CC-SMPC and DMPC methods provide different configurations and load shedding results when cells and battery failures occur. The load shedding leads to lower load demands in the system, which leads to reducing the average constraint violation  $Q_{P^{bat}ap'}$  and  $Q_{P^{bat}ap'}$  in total for both CC-SMPC and DMPC compared to Case 1. According to Table 5, the values are consistent with the change of probability, which presents fewer violations when the probability decreases. Similar to Case 1, the DMPC method leads to higher violations compared to the CC-SMPC technique with different  $\rho$  values.

#### 2) Online scenario tests

Fig. 8 illustrates both operating costs and constraint violation costs for the DMPC and CC-SMPC strategies evaluated by the framework introduced in Section 5.1. Each control method is applied to 50 uncertain load scenarios considering the components' failure events, while the failures occur following the aforementioned sequence. Based on the chosen evaluation weighting coefficients, the CC-SMPC approach has a 41 % higher total operation cost compared to the DMPC method, as the CC-SMPC-based solution strategy involves load shedding and switching activities for disconnection/reconnection for the loads, which is associated with higher weighting factors than the transmission switches in this study. However, compared to the DMPC method, the CC-SMPC total cost for constraint violation is reduced by 96 %, with each type of violation cost being reduced by at least 80 %. Moreover, the violation of the generator output power has been avoided 100 % of the time. In general, by adopting the CC-SMPC method the total cost has been reduced by 4.37 %. The detailed simulation results of both DMPC and SPMC strategies for all 50 testing scenarios are now presented and discussed.

Fig. 9 to Fig. 11 illustrate the SOC level of the four batteries, the batteries' power, and generators/APU power. Before the generator failure occurs (at t=19 min), the system operates in normal mode, which shows the same results as Case 1. According to Fig. 11, after the failure of generator 1, the APU is started to replace the failed generator, to continue supplying the system by closing the switches at the link of the HV bus and APU bus. This has a few impacts on battery power (Fig. 10) and SOC changes (Fig. 9) before other failures occur, although the power loss slightly increases due to the bus interconnection. Before t=49 min, similarly to Case 1 with the same configuration and load connection, the CC-SMPC performs better than DMPC by avoiding the battery power violation which occurs in 30 % to 50 % of scenarios in DMPC.

At t=49 min, cell number 10 fails, which leads to the power shortage from the HV side, particularly for LV bus 4, while the system enters the high power demand interval. During this period, the DMPC and CC-SMPC strategies provide different solutions for supplying the loads at LV bus 4. The DMPC-based solution strategy tries to discharge battery number 4 with a discharging power that exceeds the maximum allowable discharging power in 10 % of scenarios, with a maximum

value of 3.2 kW. This results in decreasing the battery SOC level but avoids load shedding. On the other hand, the CC-SMPC-based solution strategy involves shedding the load with the lowest priority at the LV bus to avoid the battery power violation and keep the battery SOC at higher levels, as presented in Fig. 14. Compared to the CC-SMPC-based control in Case 1 during the same time interval, shedding the load at LV bus 4 also saves power, which is then available for charging battery 3, which is also supplied by generator 2. Since unnecessary switching activities should be avoided, the load shedding remains for 60 min and the connection status of the cells to LV buses is not changed before the occurrence of the next failure. In addition, from t = 50 min to t = 79 min, the DMPC strategy relies on the APU to supply LV buses 1 and 2, and APU maximum power violation occurs in approximately 40 % of scenarios, with the maximum violation power being 4.5 kW. On the other hand, as shown in Fig. 11- (c) the CC-SMPC method avoids 100 % of these violations.

At t = 79 min, cell number 3 fails, which leads to a further power shortage on the LV side, while LV bus 1, connected to this cell, is impacted directly by this failure. According to Fig. 12, after the occurrence of this failure, the number of cells connected to bus 1 is reduced from 3 to 2 in both DMPC and CC-SMPC methods. However, when adopting the DMPC strategy, the LV loads on LV bus 1 don't experience any load shedding, instead the connected battery is over-discharged in 14 % of scenarios with the maximum power of 3.3 kW, and the APU is overloaded in 8 % of scenarios with the maximum power of 98.3 kW. The CC-SMPC strategy sheds the lowest priority load at this LV bus, which avoids all of the power excess situations and keeps the SOC level 10 % higher than the SOC in the DMPC method. This is presented in Fig. 14 (1-a). In addition, comparing the SOC changes for this period in Fig. 9, with the DMPC method, battery 4 has been discharged close to the lower bound during all cruising stages, and additional transmission switching activities are conducted to avoid the SOC dropping below the lower bound. In contrast, the CC-SMPC method avoids these switching activities because the lowest priority load for battery 4 remains shed. In summary, by predicting that the maximum power may be exceeded, a situation which is important to avoid if possible, the CC-SMPC method sheds the lowest priority load to avoid this happening. This also results in greater SOC available later in the flight.

At t=109 min, when Battery 2 fails, both DMPC and CC-SMPC methods disconnect this battery and stop charging/discharging it. When adopting the DMPC method, LV bus 2 and LV bus 3 are connected to supply the load at LV bus 2, while the CC-SMPC method connects LV bus 2 with LV bus 1, as presented in Fig. 13. This difference is mainly caused by the average SOC level, which is accumulated differently in previous steps. At this stage, all loads which were shed by the CC-SMPC method are re-connected to the system, which causes additional switching activities. Fig. 14 (2) shows that among 50 scenarios, the CC-SMPC control leads to 48 scenarios with 4 LV loads switching activities, and 2 scenarios with 6 LV loads switching activities. However, the DMPC method again leads to a battery power limit violation in 48 % of scenarios, as well as the violation of the SOC upper bound in 44 % of scenarios, which is reduced to 2 % with the CC-SMPC method.

As a conclusion for Case 2, the proposed CC-SMPC method is capable of coping with multiple failure scenarios. Compared to the DMPC method, when power shortages happen in the system because of failures, the CC-SMPC method results in more conservative operating strategies, such as more load shedding but less SOC reduction compared to the DMPC method. In addition, similarly to Case 1, the CC-SMPC method performs better in controlling the system with fewer constraint violations.

# 6. Conclusions

This paper studied the optimal operational control of an EPS integrated with modular converters and multiple ESSs on an MEA in both normal and faulty situations, targeting the improvement of the

performance of the system in terms of power losses, energy storage, and load shedding as well as reducing the number of switching activities. For dealing with the uncertainty of load power demand on both HV and LV buses, the operational management problem is formulated in the framework of CC-SMPC. Two sources of uncertainty are studied, including the uncertainty of load power demand on both LVBs and HVBs. Based on the uncertainty propagation in normal/faulty conditions, different probabilistic constraints are proposed for each case. Moreover, a comprehensive Monte-Carlo-based offline and online simulation study is conducted for both DMPC and CC-SMPC methods, and the results are evaluated with the proposed multi-dimension evaluation framework. Based on the comparison of the operational costs and the violation costs in normal and faulty cases, the proposed CC-SMPC improves the violation costs and the overall costs compared to the DMPC technique in both cases.

# CRediT authorship contribution statement

Xin Wang: Conceptualization, Methodology, Software, Formal analysis, Visualization, Investigation, Validation, Writing – original draft, Writing – review & editing. Najmeh Bazmohammadi: Methodology, Software, Formal analysis, Data curation, Investigation, Writing – review & editing. Jason Atkin: Resources, Methodology, Writing –

review & editing, Supervision. **Serhiy Bozhko:** Resources, Supervision, Project administration, Funding acquisition, Writing – review & editing. **Josep M. Guerrero:** Supervision, Project administration, Writing – review & editing.

# **Declaration of competing interest**

The authors declare that they have no known competing financial interests or personal relationships that could have appeared to influence the work reported in this paper.

# Data availability

Data will be made available on request.

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Appendix A

Table 6

Mean value load profile based on flight stages with indices in Fig. 3.

| Load types [kW]   | Load indices                          | Ground<br>(20 min) | Take off<br>(5 min) | Climb<br>(25 min) | Cruise<br>(60 min) | Descent<br>(10 min) | Loiter<br>(15 min) | Landing<br>(15 min) |
|-------------------|---------------------------------------|--------------------|---------------------|-------------------|--------------------|---------------------|--------------------|---------------------|
| HV Critical Loads | HVL1 <sup>1</sup> , HVL2 <sup>1</sup> | 10.01              | 29.58               | 22.20             | 21.40              | 22.20               | 27.21              | 30.00               |
| LV Critical Loads | LVL1 <sup>1</sup> , LVL4 <sup>1</sup> | 1.78               | 1.94                | 2.07              | 3.00               | 2.07                | 2.07               | 1.94                |
|                   | LVL2 <sup>1</sup> , LVL3 <sup>1</sup> | 3.00               | 2.50                | 2.50              | 2.50               | 2.50                | 2.50               | 3.00                |
| HV HP Loads       | HVL1 <sup>2</sup> , HVL2 <sup>2</sup> | 4.89               | 8.34                | 24.00             | 24.00              | 24.00               | 24.00              | 0.87                |
| LV HP Loads       | LVL1 <sup>2</sup> , LVL4 <sup>2</sup> | 1.40               | 1.30                | 1.30              | 1.30               | 1.60                | 0.80               | 0.80                |
|                   | LVL2 <sup>2</sup> , LVL3 <sup>2</sup> | 0.50               | 0.50                | 0.50              | 0.50               | 1.50                | 1.00               | 2.20                |
| HV MP Loads       | HVL1 <sup>3</sup> , HVL2 <sup>3</sup> | 15.00              | 3.42                | 14.16             | 14.60              | 14.16               | 14.16              | 3.42                |
| LV MP Loads       | LVL1 <sup>3</sup> , LVL4 <sup>3</sup> | 0.50               | 0.35                | 0.35              | 2.00               | 0.35                | 0.35               | 0.35                |
|                   | LVL2 <sup>3</sup> , LVL3 <sup>3</sup> | 0.70               | 0.70                | 1.50              | 2.50               | 0.50                | 0.50               | 0.50                |
| HV LP Loads       | HVL1 <sup>4</sup> , HVL2 <sup>4</sup> | 6.00               | 6.00                | 6.00              | 18.00              | 6.00                | 6.00               | 6.00                |
| LV LP Loads       | LVL1 <sup>4</sup> , LVL4 <sup>4</sup> | 1.00               | 0.70                | 1.50              | 2.20               | 0.50                | 0.50               | 1.50                |
|                   | LVL2 <sup>4</sup> , LVL3 <sup>4</sup> | 2.00               | 1.50                | 2.00              | 3.00               | 2.00                | 2.00               | 2.00                |

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