

Defect-Assisted High Photoconductive UV–Visible Gain in Perovskite-Decorated Graphene Transistors

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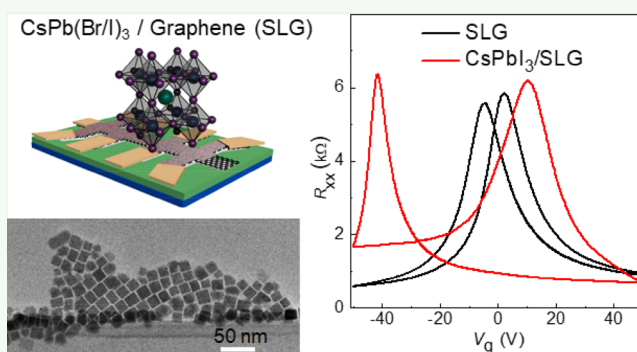
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Supporting Information

ABSTRACT: Recent progress in the synthesis of high stability inorganic perovskite nanocrystals (NCs) has led to their increasing use in broadband photodetectors. These NCs are of particular interest for the UV range as they have the potential to extend the wavelength range of photodetectors based on traditional materials. Here we demonstrate a defect-assisted high photoconductive gain in graphene transistors decorated with all-inorganic cesium lead halide perovskite NCs. The photoconductive gain in the UV–vis wavelength range arises from the charge transfer between the NCs and graphene and enables observation of high photoconductive gain of 10^6 A/W. This is accompanied by a giant hysteresis of the graphene resistance that is strongly dependent on electrostatic gating and temperature. Our data are well described by a phenomenological macroscopic model of the charge transfer from bound states in the NCs into the graphene layer, providing a useful tool for the design of high-photoresponsivity perovskite/graphene transistors.

KEYWORDS: graphene, UV photodetectors, inorganic perovskite nanocrystals, charge transfer, defect levels



INTRODUCTION

Rapid progress in semiconductor science and technology has led to the development of novel device concepts which combine two-dimensional (2D) van der Waals (vdW) crystals, such as graphene, with an adjacent layer of an optically active 0D, 2D, or 3D material.^{1–4} For example, a record high photoresponsivity ($R > 10^9$ A/W) has been achieved by depositing photoresponsive 0D nanocrystals (NCs) of II–VI or IV–VI QDs (CdSe, CdS, PbS, etc.) on the surface of single layer graphene (SLG).^{4–6} These hybrid structures are of particular interest for near-infrared (NIR) graphene-based complementary metal-oxide-semiconductor (CMOS) image sensors.⁷ More recently, organic⁸ and inorganic⁹ perovskite nanocrystals with high optical quantum yield have been used in combination with graphene^{10–13} to achieve photoresponsivity of up to 10^9 A/W and detectivity of 10^9 Jones. These NCs were also proposed for extending the photoresponsivity of graphene devices to the UV region.^{9,10}

Harnessing the potential of the perovskite NC/graphene hybrid materials requires a fundamental understanding of the charge transfer processes that occur at their interface. It is well-known that the electrical properties of graphene are extremely

sensitive to the presence of photoexcited charges generated in the adjacent layers above and/or below the SLG.¹⁴ Hence it provides a suitable platform for studies of photoexcited charge transfer.¹⁴ Despite a growing number of experimental reports on the application of perovskite NC/graphene in optoelectronic devices,^{8–10} one of the key challenges is understanding and controlling the charge transfer at the interface of these heterostructures.¹⁵

Here, we study the optoelectronic properties of graphene decorated with a layer of inorganic cesium lead halide nanocrystals (CsPbX_3 , where $X = \text{Br}$ or I). These hybrid devices have electrical and optical properties significantly different from those of pristine graphene or those decorated with traditional colloidal semiconductor nanocrystals. Our experimental data and modeling indicate that the optoelectronic properties of these devices are defined by the presence of deep trap energy levels ($E_x \approx 0.3$ eV) in the perovskite NCs. These NCs are responsible for large gate-voltage-induced

Received: October 11, 2019

Accepted: December 10, 2019

Published: December 10, 2019

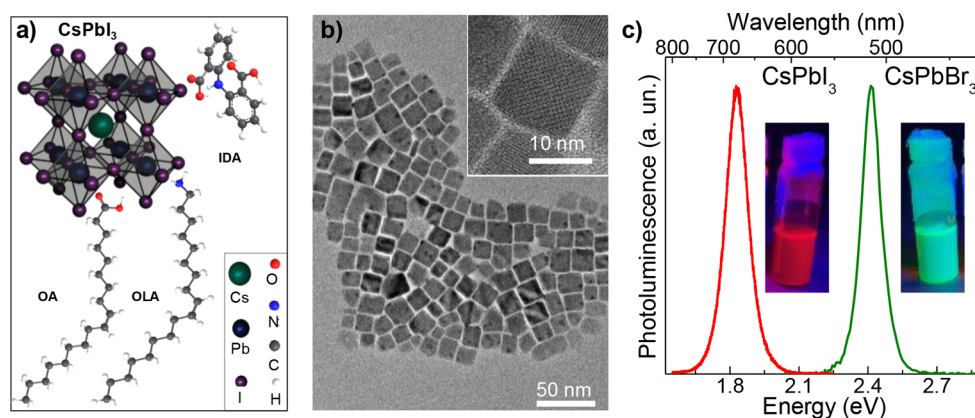


Figure 1. (a) A schematic diagram of the unit cell of CsPbI₃ nanocrystals capped with a mixture of oleic acid (OA), oleylamine (OLA), and iminodiacetic acid (IDA). (b) Representative TEM and HR TEM images of CsPbI₃ nanocrystals. (c) PL spectra and images of CsPbBr₃ and CsPbI₃ perovskites excited by unfocused UV light (365 nm).

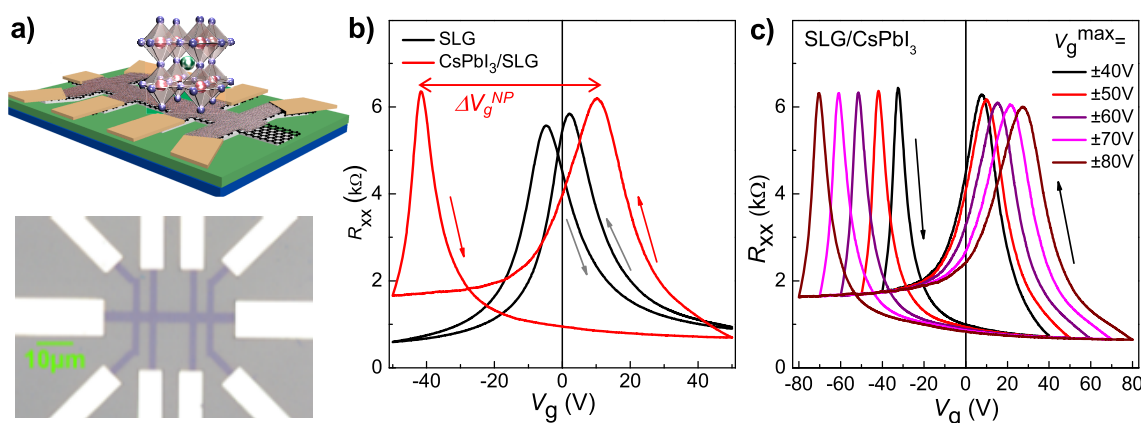


Figure 2. (a) A schematic diagram of the layer structure (top) and optical microscope image (bottom) of the Si/SiO₂/hBN/graphene/perovskite Hall bar device. (b) Room temperature dependence of the longitudinal resistance, R_{xx} , on applied gate voltage, V_g , of a pristine graphene device and of the same device after deposition of the CsPbI₃ nanocrystals. Arrows indicate directions of the V_g sweeps, sweep rate = 0.3 V/sec, $T = 295$ K. The top horizontal arrow represents the size of the ΔV_g^{NP} . (c) R_{xx} as a function of the maximum applied gate voltage, V_g^{\max} , for a series of sequential V_g sweeps with V_g^{\max} increasing from 40 to 80 V, sweep rate = 0.3 V/sec, $T = 295$ K.

hysteresis of the resistance and high photoresponsivity of these hybrid devices. A mechanism for this large hysteresis is proposed here. The high photoresponsivity ($R \approx 10^7$ A/W) in the UV–vis (250 nm to ~ 700 nm) arises from a large internal gain due to efficient transfer of photogenerated electrons from the NCs to SLG and a long lifetime (~ 1 s) of photoexcited holes that are trapped in the NCs. Our results offer a novel route for engineering the interface properties of hybrid materials, relevant for their exploitation in silicon-compatible optoelectronics.

RESULTS AND DISCUSSION

1. Electrical Properties of Perovskite Decorated Graphene Transistors. Cesium lead halide perovskite nanocrystals were synthesized following the method reported in ref 16 and were capped with a mixture of oleic acid (OA), oleylamine (OLA), and iminodiacetic acid (IDA) (Figure 1a). The nanocrystals have cubic shape with an average size of 12.9 ± 1.7 nm (Figure 1b). The composition of the NCs is chosen to tune their optical properties: CsPbI₃ and CsPbBr₃ NCs emit light at wavelengths of $\lambda = 680$ and 515 nm, respectively (Figure 1c). The NCs were deposited onto CVD-grown graphene by drop casting. The single layer graphene (SLG) was placed on a hBN/SiO₂/Si substrate (SiO₂ layer thickness

285 nm; monolayer of hBN). The structures were then processed into two device geometries: Hall bars for measurements of carrier concentration and mobility (Figure 2a) and two-terminal diodes with short (~ 5 μ m) channels for optoelectronic characterization.

We now compare the gate voltage characteristics of devices based on pristine graphene and perovskite-decorated graphene. The pristine SLG devices have a resistance maximum $R_{xx} \approx 6$ k Ω close to $V_g = 0$ V and exhibit only a small hysteresis loop, which we quantify in terms of the difference in the gate voltage value at charge neutrality (Dirac) point, $\Delta V_g^{NP} = 5$ V, between the forward (from negative to positive bias) and reverse sweeps of V_g (Figure 2b and Supporting Information section S11). The pristine SLG devices have a low electron carrier concentration of $n \approx 2 \times 10^{11}$ cm⁻² at $V_g = 0$ V and carrier mobility $\mu_e \approx 6000$ cm² V⁻¹ s⁻¹ at room temperature.

Deposition of the perovskite NCs leads to a significant increase of the hysteresis of the graphene charge neutrality point V_g^{NP} , that is, the gate voltage at which $R_{xx}(V_g)$ reaches its maximum value. Our measurements confirm that a film of perovskite nanocrystals has resistance >10 G Ω , which is not affected by applied gate voltage and/or exposure to light (see Supporting Information, section S12). As shown in Figure 2b for SLG/CsPbI₃, at room temperature a hysteresis of $V_g^{NP} \approx 60$

V is observed for a gate voltage sweep over the range of $V_g^{\max} = \pm 50$ V. The hysteresis of $R_{xx}(V_g)$ is strongly affected by the maximum applied gate voltage V_g^{\max} (Figure 2c). The value of the hysteresis, ΔV_g^{NP} , increases with increasing V_g^{\max} and reaches unexpectedly high values above 100 V. The observed dependence of the ΔV_g^{NP} on V_g^{\max} and the large hysteresis indicate that the NC layer is accumulating a significant amount of charge. Due to the large hysteresis, it is not possible to define an exact value of the V_g^{NP} for the perovskite decorated layer; hence change of carrier concentration in the SLG due to surface doping effect of the perovskite NCs cannot be assessed quantitatively. We note that the hysteresis is reproducible and the overall device performance is fully reversible (Supporting Information, section S13).

The observed ΔV_g^{NP} hysteresis is strongly temperature-dependent (Figure 3a). The $R_{xx}(V_g)$ of SLG/CsPbI₃ Hall bar

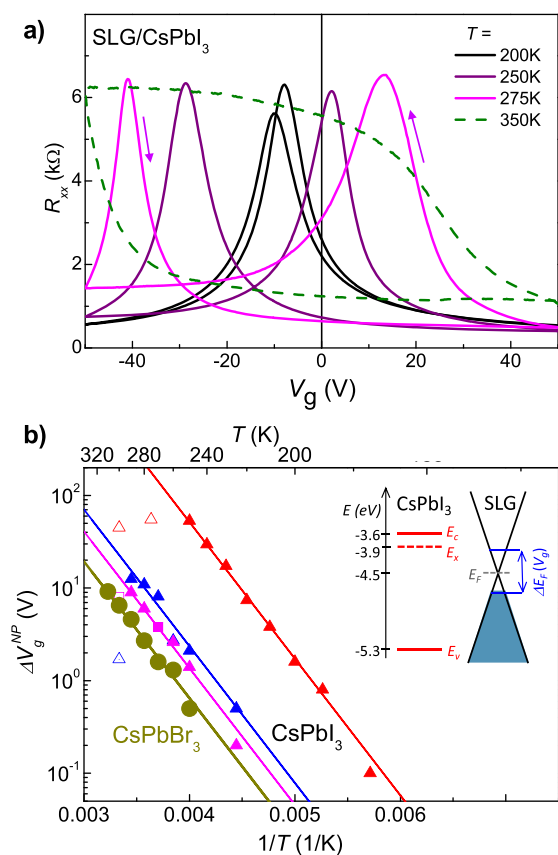


Figure 3. (a) $R_{xx}(V_g)$ dependences of the SLG/CsPbI₃ device measured at different temperatures. (b) Data points: ΔV_g^{NP} as a function of temperature for a Hall bar CsPbI₃/SLG device (red triangles) and three 2-terminal devices: two CsPbI₃/SLG devices (blue and magenta triangles) and one CsPbBr₃/SLG device (yellow circles). Solid lines represent a fit with $\Delta V_g^{\text{NP}} \approx \exp(-E_x/kT)$ with $E_x = 0.3$ eV. Inset: Band diagram of the CsPbI₃/SLG heterostructure. The blue arrow indicates a region where graphene Fermi energy, E_F , is changed by the applied gate voltage. The red dashed line denoted with E_x represents the energy level of charge trapping defects.

devices were measured over the range of temperatures from 2 to 350 K. The size of the hysteresis decreases with decreasing temperature and quenches at $T \approx 200$ K, where the observed $R_{xx}(V_g)$ characteristics resemble those of pristine graphene, indicating that no charge transfer is taking place at $T < 200$ K. At $T > 320$ K, a significant broadening of the forward/reverse

loop is observed. This is likely due to structural changes in the perovskite NCs, which is also evident in the irreversible decrease of room temperature photoluminescence (PL) intensity following exposure of the NCs to $T > 400$ K. We note that the room temperature optical properties of the NCs, such as PL, are not significantly affected by exposure to temperatures in the range from $2 \text{ K} < T < 400 \text{ K}$. This leads us to conclude that the dramatic $\Delta V_g^{\text{NP}}(T)$ dependence is related to the charge transfer between the NC layer and the SLG, rather than the temperature-dependent properties of the perovskite NCs.

The temperature dependence of the ΔV_g^{NP} hysteresis between $200 \text{ K} < T < 300 \text{ K}$ has an exponential form and is well described by $\Delta V \approx \exp(-E_x/kT)$, with activation energy of $E_x = 0.3$ eV. This activation energy has the same value for both CsPbI₃ and CsPbBr₃ NCs (Figure 3b), suggesting the presence of a common deep energy level defect (inset Figure 3b). We note that the activation energy is significantly larger, $E_x = 0.6$ eV, for Cs(Pb_{0.67}Sn_{0.33})I₃ NCs (see Supporting Information section S13). Thus, we infer that these defect energy levels correspond to Pb-related defects, which were recently reported to account for PL intermittency in cesium lead halide nanocrystals.¹⁷ In order to understand the nature of the ΔV_g^{NP} hysteresis, we consider a numerical two capacitor (2C) model, described below.

2. Modeling of Charge Transfer in Perovskite Decorated SLG. The small hysteresis of $R_{xx}(V_g)$ is typically observed in gated SLG devices and usually associated with the gate capacitance and/or charging of impurities.^{18,19} In pristine SLG devices, the amount of hysteresis is very small (Figure 2b) and is independent of the V_g sweep range (V_g^{\max}). In perovskite-decorated SLG we observe an anomalously large hysteresis, ΔV_g^{NP} , which is not observed in other hybrid graphene heterostructures.^{6,7,23} We envisage that the slow V_g -dependent charge transfer between the NCs and SLG results from charge accumulation in the NC layer.

In our numerical model, we treat our CsPbI₃/SLG/SiO₂/Si device as a double capacitor (2C-model): the first capacitor is that of the gate/SiO₂/SLG layers and the second capacitor is the “effective” capacitance of a layer of perovskite nanocrystals (see inset in Figure 4b and Figure S5). The resistance of the NC layer is very high ($>10 \text{ G}\Omega$; see Figure S2), hence, the charge transfer between the neighboring NCs is negligibly small. In this model we consider charge transfer only from the layer of NCs adjacent to the SLG; the only fitting parameter is the time constant, τ_{per} , which describes the rate at which the charge in the perovskites decays.

Our numerical calculations of the $R_{xx}(V_g)$ hysteresis at different V_g^{\max} (Figure 4a) demonstrate a behavior similar to that observed experimentally (Figure 2c). By fitting the measured $\Delta V_g^{\text{NP}}(V_g^{\max})$ dependences by means of the 2C model (Figure 4b) we obtain a good agreement between the experimental data and numerical simulations with a value of the fitting parameter $\tau_{\text{per}} = 130$ s. However, the symmetric behavior with respect to positive/negative V_g predicted by this model is not consistent with the measured data. The experimental results (Figure 2c) reveal a weaker hysteresis at positive V_g . We attribute this discrepancy to the assumption used in our model we disregard which capacitor plate, bottom gate, or top SLG with NCs plate, has positive/negative polarity. In our device, the charging of the top plate of the capacitor (SLG with NCs) depends on the polarity of the applied gate voltage as it shifts the Fermi energy in the SLG,

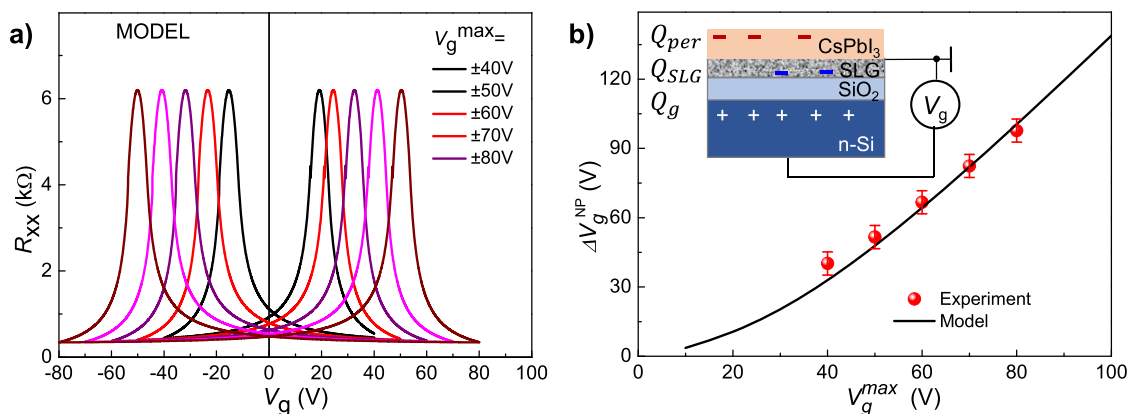


Figure 4. (a) Numerical simulation using 2C-model of $R_{xx}(V_g)$ with increasing V_g^{\max} . The fitting parameter used is $\tau_{\text{per}} = 130$ s. (b) Data points represent measured values of ΔV_g^{NP} as a function of the V_g^{\max} (see Figure 2c). The solid line is a fit based on the two-capacitor 2C-model with $\tau_{\text{per}} = 130$ s. The inset shows schematically the distribution of charge between the perovskite layer, Q_{per} , the graphene layer, Q_{SLG} , and the gate, Q_g , used in the 2C-model.

over the range ± 0.3 eV for ± 100 V (Supporting Information section SI4).

The gate voltage sweep generates an incremental increase in charge $\Delta Q = C\Delta V_g$ on the top of the perovskite/SLG electrode corresponding to the sum of free carrier charge (electrons or holes) in the SLG and the charge bound to the localized states of the NCs, with activation energy, E_x . We assume that most of this charge is stored in the NC layer and is slowly released into the SLG (with an effective time constant of ~ 100 s). The charge capacitance of the perovskite NC layer is estimated from the measured position of the SLG neutrality point hysteresis, $\Delta V_g^{\text{NP}} > 100$ V, to be greater than 7×10^{12} cm^{-2} (see Figure 2c). Assuming that the average size of our CsPbI_3 NCs with ligands included is $a \approx 15$ nm¹⁶ and the surface filling factor is approximately 1, we estimate the NC density to be approximately 4×10^{11} cm^{-2} . Hence the number of individual charges per single NC adjacent to the SLG is greater than 10. We estimate that a perovskite nanocrystal contains ~ 400 of CsPbX_3 unit cells, each containing a single Pb atom, per one side of a NC cube, thus providing an extremely large maximum charge capacity, limited only by Coulomb repulsion of the charges within the perovskite NC. The electrostatic energy which arises when $N = 10$ electrons are accommodated within a single perovskite NC can be estimated using the expression $U = \frac{3}{5} \frac{e^2 N^2}{4\pi\epsilon\epsilon_0 a}$, where $a = 7$ nm is an effective radius of a NC and $\epsilon = 10$ is the dielectric constant of CsPbI_3 .²⁰ The estimated value is $U \approx 0.6$ eV, which is significantly lower than the CsPbI_3 work function of ~ 3.6 eV.²¹

We propose that the large number of charges trapped within a single perovskite NC is responsible for the anomalously large ΔV_g^{NP} hysteresis and slow charge transfer processes. Indeed, our model is in good agreement with the observed dependence of hysteresis ΔV_g^{NP} on the applied gate voltage (Figure 4b). A similar charge transfer between SLG and an adjacent dielectric layer was recently reported in SiC/SLG²² and in InSe/graphene^{23,24} heterostructures. In both heterostructures, the Fermi level in SLG is pinned to the energy level of the ionized donor impurities in the adjacent SiC or InSe dielectric layer. However, in contrast to our perovskite-decorated devices, no large temperature- and V_g sweep range- dependence of the pinning effect was reported for those devices.^{22–24} This suggests that charge transfer processes in our 0D/2D CsPbI_3 /

SLG hybrid devices are significantly slower than those in 2D/2D heterostructures.

3. Light-Induced Charge Transfer and Photoresponsivity. The charge transfer is affected by exposure to light. We observed a significant reduction of the hysteresis under positive gate voltage and under light illumination, with V_g^{NP} always remaining in the negative V_g region. In addition, the negative V_g section of the $R_{xx}(V_g)$ curves is similar for both light and dark conditions (inset in Figure 5a). This behaviour can be explained by the effect of light on the gate-induced charge trapped in the perovskite NCs. The light generates electron–hole pairs in the NCs and the photoexcited electrons are quickly transferred to the SLG, while the less mobile photoexcited holes remain trapped in the NCs. This difference in relaxation times of photoexcited electrons and holes leads to the asymmetry of the $R_{xx}(V_g)$ shown in Figure 5a. We assume that photoexcited holes can recombine with gate-induced negative charges in the perovskite NCs (Figure 5b). The V_g -dependence of the resistance indicates the transfer of both photoexcited electrons and holes for $V_g > 0$ V, and the preferential transfer of photoexcited electrons for $V_g < 0$ V (see corresponding band diagrams in Figure 5b and in Figure S8). Our macroscopic capacitance model accounts well for these experimental findings. This process can be described in terms of a significant reduction of the τ_{per} parameter under illumination and at applied positive V_g . Under negative V_g conditions, photoexcited holes do not interact with positive gate-induced charge, leading to no significant changes in the $R_{xx}(V_g)$ hysteresis measured under dark and light conditions (section SI5).

The shift of V_g^{NP} toward negative values under light illumination (Figure 5a) indicates a transfer of photoexcited electrons from the perovskite NCs to the graphene (Figure 5b), similar to that reported previously for quantum dot decorated SLG^{4,5,25} and other perovskite NC decorated structures.¹⁰ This behavior is in contrast to the transfer of photoexcited holes suggested for mixed-cation lead mixed-halide hybrid perovskite deposited on similar CVD graphene/SiO₂/Si substrates.¹⁵

The SLG/perovskite devices are photosensitive over the wavelength range of optical absorption of the NCs. The photocurrent spectrum is quite similar to that of optical absorption (Supporting Information SI5): it follows quite

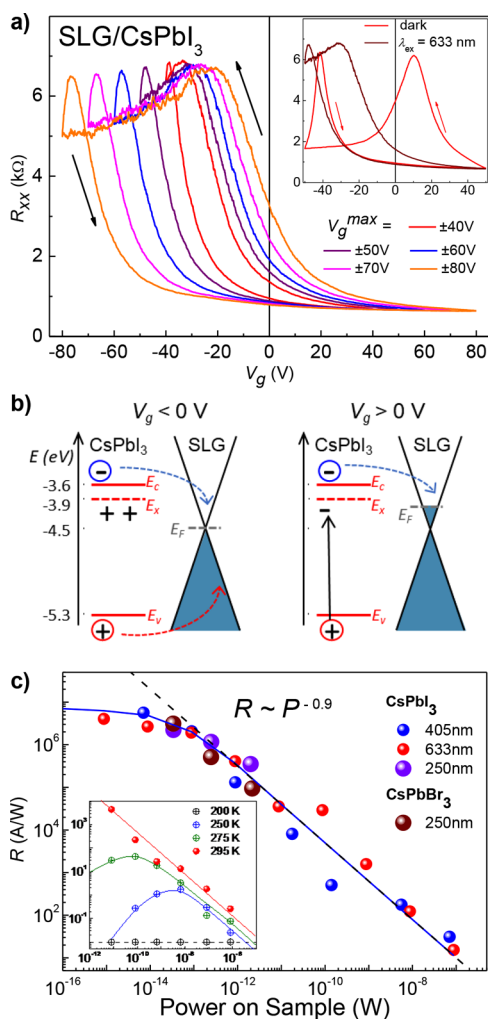


Figure 5. (a) R_{xx} as a function of the maximum applied gate voltage, V_g^{\max} , for a series of sequential V_g sweeps with V_g^{\max} increasing from 40 to 80 V, under excitation with a HeNe laser ($\lambda = 633$ nm, $P = 310$ W/m²). Inset: $V_g^{\max} = \pm 50$ V sweep in the dark and under laser light. (b) A schematic diagram of the CsPbI₃/SLG band structure. Arrows indicate transfer of photoexcited charges from perovskites to the SLG (dashed blues and red arrows) for negative (left) and positive (right) gate voltages. The vertical black arrow indicates recombination of photogenerated holes with negative charge stored in the perovskite layer. (c) UV–vis photoresponsivity of CsPbI₃/SLG and CsPbBr₃/SLG devices excited at $\lambda = 633$, 405, and 250 nm. The dashed line is a fit using $R \approx P^{-0.9}$. The blue line is a guide to the eye. Inset: photoresponsivity of CsPbI₃/SLG at different temperatures measured at 10^{-11} W < P < 10^{-6} W region. The dashed line represents the level of noise.

closely that of absorbance. Also, a large photocurrent is observed in the UV region (<400 nm). Pristine graphene has a small optical absorbance (~2%) and consequently its photoresponsivity is very low (<0.1 A/W). Hence, the optical response of these SLG/perovskites heterostructures is determined mainly by the optical absorption of the perovskite NCs and by the charge transfer between the NC layer and graphene. The SLG devices decorated with both types of perovskite NCs, CsPbBr₃, and CsPbI₃, have very high photoresponsivity of $R > 10^6$ A/W in the UV–vis range (250 nm–633 nm) at room temperature (Figure 5c). The highest responsivity $R \approx 5 \times 10^6$ A/W is observed in the low power limit where the noise equivalent power NEP $\approx 10^{-15}$ W.

The responsivity exceeds that recently reported for SLG devices decorated with similar perovskite nanocrystals.^{9,26} In particular, we note the higher responsivity of our devices in the UV range. With increasing excitation power, the photoresponsivity decreases as $R \sim P^{-x}$, where $x = 0.90 \pm 0.02$ (Figure 5c), which is similar to that reported for CsPbCl₃⁹ and for colloidal quantum dots.⁵

We now examine the temperature dependence of the power law as $R \sim P^{-x}$ at power on the sample above 10^{-11} W. As can be seen in the inset in Figure 5c, with decreasing temperature we observe a deviation from the power law. For example, at $T = 250$ K, the power dependence of the responsivity is described by $R \sim P^{-0.9}$ only up to the value of $P = 10^{-9}$ W. At $T = 200$ K, the responsivity decreases, reaching a value of $R < 1$ A/W, which is similar to that of pristine graphene. This observation corroborates our measurements of the temperature dependence of the gate voltage-induced hysteresis ΔV_g^{NP} , where at $T < 200$ K, no effect of the NC layer on the electrical properties of graphene is observed (Figure 3a). We attribute the observed temperature dependence of $R(P)$ to the interplay of two opposing effects: the $R \sim P^{-0.9}$ increase of $R(P)$ at the high power range and the fast exponential quench of $R(T)$ at low P . The weaker $R(T)$ dependence at large excitation power can be explained by the heating of the device. At low excitation powers ($P < 10^{-8}$ W) and low temperatures ($T < 250$ K) charge transfer between the NCs and graphene appears to be blocked. This results in quenching of both the photoresponsivity R and the gate voltage hysteresis ΔV_g^{NP} . There is only significant change of the NC/SLG properties that are directly dependent on the NC-SLG charge transfer, such as the NC photoluminescence²⁷ or the SLG carrier concentration and mobility over a wide temperature range.

4. Temporal Response of Perovskite/SLG Devices.

The temporal response of the devices was measured at room temperature to explore the possible mechanisms underlying charge transfer and device performance. A reproducible response to optical excitation is observed for all samples with ON/OFF response times of ~ 2 s (Figure 6a). The temporal relaxation of the photocurrent cannot be fitted to a single exponential dependence, indicating the presence of several processes responsible for charge relaxation in these heterostructures, with characteristic times in the range from 0.5 to 5 s. The studies of temporal relaxation following electrical excitation by the applied gate voltage revealed a longer characteristic relaxation time of ~ 120 s (Figure 6b). Note that this time is very close to the τ_{per} parameter used in our 2C-model to fit the $\Delta V_g^{\text{NP}}(V_g^{\max})$ data (Figure 4b).

The temporal response of the device following optical excitation is shown in Figure 6a. The observed exponential decay of the resistance has characteristic times of ~ 1 s. The relaxation of the charge generated by applied gate voltage has a significantly slower decay time of ~ 100 s (Figure 6b). We ascribe this difference to the relaxation of fast optically excited, high energy charge carriers compared to slow gate-voltage-induced charge transfer. Despite significant differences in the values of the relaxation times, the optical responsivity R and gate voltage hysteresis ΔV_g^{NP} have very similar temperature dependences (Figure 6c). This leads us to conclude that the processes responsible for the ΔV_g^{NP} hysteresis also define the optical responsivity observed in these materials, that is, both light- and gate-induced charge trapping processes are controlled by the same kind of charge traps with an activation energy $E_x \approx 0.3$ eV.

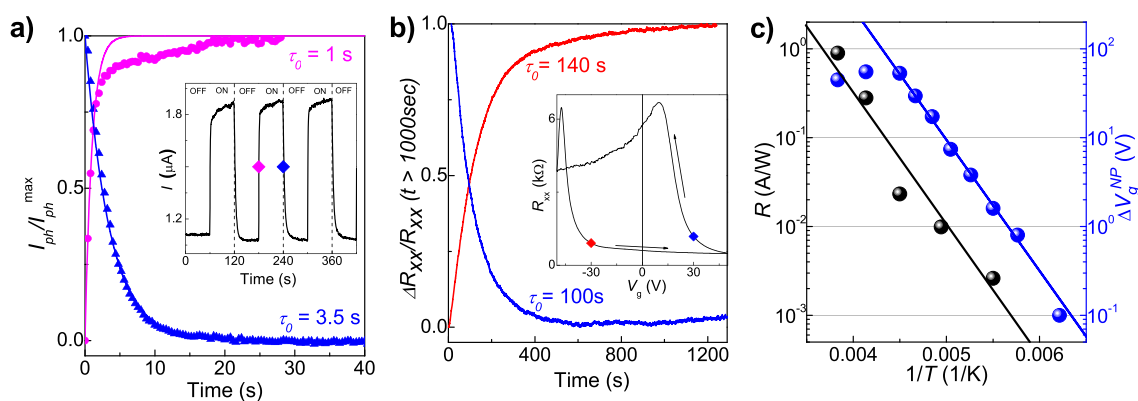


Figure 6. (a) Temporal response of the photocurrent on rising and falling slope (ON/OFF) at point indicated by magenta (ON) and blue (OFF) points in the inset for the SLG/CsPbI₃ device. Inset: light on/off time dependence of the photocurrent for the SLG/CsPbI₃ device at $T = 295$ K ($V_{sd} = 20$ mV, $V_g = 0$ V, $P = 325$ W/m², $\lambda = 633$ nm). (b) Temporal response and (inset) R_{xx} as a function of V_g for CsPbI₃/SLG device at the V_g -sweep positions indicated by the red and blue dots (inset), $T = 295$ K, $I_{sd} = 0.5$ μ A, sweep rate 0.3 V/sec. (c) Data points: $\Delta V_g^{NP}(T)$ (blue) and $R(T)$ (black) dependences of SLG/CsPbI₃ device. Solid lines are the fit with $\Delta V \approx \exp(-E_x/kT)$, where $E_x = 0.3$ eV.

The response time of these devices is independent of temperature. However, at low temperatures ($T < 270$ K) the perovskites/SLG photodetectors become less stable, introducing telegraph noise and an unstable photoresponse. The relatively large ratio of the lifetime of the photoexcited holes trapped in the NCs, typically $\tau_{NC} > 1$ s and the electron transit time in graphene channel, $\tau_e < 1$ ns, give rise to high photoresponsivity ($R > 10^6$ A/W at $T = 295$ K) due to high photogain.^{5,9} The measured response time of ~ 1 s in our devices (Figure 6a) is comparable to that previously reported for QD-decorated graphene photodetectors^{4,5} and is significantly shorter than that of CsPbCl₃/graphene devices without ligand treatment (>100 s⁹). We propose that the shorter ligands used in our NCs (IDA replacement) enable more efficient charge transfer between the NC layer and the SLG, and hence shorter response times.

CONCLUSIONS

We have described how inorganic perovskite (CsPbX₃) nanocrystals can be used as a photosensitive layer in CsPbX₃/graphene planar photon detectors. These detectors have stable performance and record high ($>10^6$ A/W) photoresponsivity in the UV–vis (250–633 nm) range. The CsPbX₃ NC decorated graphene devices exhibit an anomalous charging effect resulting in a large (>100 V) hysteresis in the gate voltage dependence of the resistance, $R_{xx}(V_g)$. The temperature dependence of the optoelectronic and transport properties of these devices has been studied for the first time and reveals fast quenching of the device performance in the temperature range below ambient conditions ($T = 200$ – 300 K). This phenomenon is consistent with the presence of a deep (~ 0.3 eV) defect level in all CsPbX₃ perovskites which we have investigated. The precise nature of this defect level is unknown and requires further studies, but is likely to be associated with the Pb atoms. These CsPbX₃/graphene devices offer good prospects for the development of ultrasensitive 0D/2D UV photon detectors.

MATERIALS AND METHODS

Synthesis and Characterization of Perovskite NCs. We have synthesized cesium lead halide CsPbX₃ perovskite NCs with improved properties by postsynthesis replacement of oleic acid ligands with iminodibenzoic acid: the NCs have a long shelf life in solution (>2

months), are stable under different environmental conditions, and have a high quantum yield of up to 90% in the visible spectral range.¹⁶ Two different perovskites, CsPbI₃ and CsPbBr₃, have been tested in order to optimize device stability, and maximize optical absorption and photoresponse in the UV range.

Device Fabrication. In these devices we utilized commercial CVD monolayer graphene/CVD monolayer hBN heterostructures on 285 nm SiO₂/Si substrates purchased from Graphene Supermarket. The 0D/2D devices were fabricated using a combination of electron beam lithography and liquid deposition of nanocrystals (NCs) in two different planar geometries: a multiterminal Hall bar and a planar diode with a short channel length. The Hall bar geometry is used for magneto-transport measurements, enabling the precise measurement of carrier concentration and mobility (Hall effect and 4-terminal conductivity measurements). The short two-terminal diodes are used for measurements of photocurrent and photoresponsivity with a short electron transit time and reduced edge effects due to a smaller number of carrier traps at the edges of 2D layers.

Electrical and Optical Measurements. Electrical measurements of the $R_{xx}(V_g)$ and photocurrent measurements vs light power or time were performed in the DC mode using Keithley-2400 source-meters and Keithley-2010 multimeters. Wavelength dependent measurements of the photocurrent and photoresponsivity were performed using the lock-in technique with the light passed through a monochromator modulated by a chopper with a frequency of 17 Hz. For the fixed wavelength excitation, we used a HeNe laser (633 nm) or solid-state lasers (405 and 532 nm). A deuterium lamp equipped with a short-pass filter was used as a UV (250 nm) source. Photoluminescence studies were conducted using a Horiba Jobin Yvon micro-PL system, equipped with a frequency-doubled Nd: YVO₄ laser ($\lambda = 532$ nm), HeNe laser ($\lambda = 633$ nm), and Si charge-coupled device (CCD) camera. The optical absorption spectra were measured using an Edinburgh Instruments FLS980 fluorescence spectrometer equipped with an integrating sphere module.

ASSOCIATED CONTENT

Supporting Information

The Supporting Information is available free of charge at <https://pubs.acs.org/doi/10.1021/acsaelm.9b00664>.

Description of the properties of pristine graphene devices and additional data on temperature dependence of the hysteresis; detailed description of the numerical modeling of charging effect in the perovskite/SLG; and additional data for the photosensitivity of the SLG/perovskite devices (PDF)

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Notes

The authors declare no competing financial interest.

ACKNOWLEDGMENTS

This work was supported by the Defence Science and Technology Laboratory (DSTL); the Engineering and Physical Sciences Research Council [Grant Nos. EP/K503800/1]; and the Faculty of Science, University of Nottingham. We thank the University of Nottingham Propulsion Futures Beacon for funding towards this research. E.E.V. acknowledges support from the Russian Science Foundation (17-12-01393). We acknowledge access to the facilities provided by the Nanoscale and Microscale Research Centre (nmRC) and Dr Michael W. Fay with transmission electron microscopy. We also benefit from useful discussions with colleagues from the DSTL.

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