2-DOF Decoupled Discrete Current Control for AC Drives at Low Sampling-to-Fundamental Frequency Ratios

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Abstract—In high performance drive systems, wide bandwidth and reference tracking accuracy of current control loop are fundamental requirements. The conventional PI controller provides robustness against the machine parameter mismatching and zero steady-state error, but its dynamic performance degrades at high-speed due to the bandwidth limitation. In this paper, a new control structure of discrete PI controller with a deadbeat response is proposed, which combines the advantage of conventional PI controller with deadbeat characteristic. The proposed controller shows a decoupled tracking performance of up to 15% of the switching frequency, while also providing an extra control freedom of the disturbance rejection, which effectively improves the system stability. Experiments show a reduction of oscillation by 30% compared to the conventional PI and the validity and applicability of the proposed control method for high-speed applications with low sampling to fundamental frequency ratios.

Index Terms—High speed AC drive, 2-DOF decoupled discrete PI (DDPI) design, decoupled current control, discrete-time system modeling, low S2F ratio.

I. INTRODUCTION

In most high-performance industrial applications, such as in high-speed ac drive systems or medium voltage gridconnected converters, current control is a critical element, since its performance usually determines the overall system performance. A fast transient response and satisfactory steady state tracking performance is required [1].

During the past decades, remarkable efforts have been made in the development of high-performance current control,

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especially for ac machine drives. These include hysteresis regulator [2, 3], deadbeat controls [4-6], model predictive controls (MPC) [7-11], synchronous reference frame (SRF) proportional integral (PI) regulators and state feedback controls. Among them, the most widely used scheme is the PI current regulator, which has the advantage of robustness in cases of parameter mismatching and parameter variation due to saturation or temperature change [6, 12-14].

However, the bandwidth of a conventional PI regulator is limited by the time delay and discretization in digital implementation, which inhibits its use in high-performance drives [2, 13-16]. In high-speed applications, where a low ratio of sampling to fundamental (S2F) frequency is applied, high oscillation and even instability can occur if the design of the current regulator does not take these effects into account [14]. In transient states such as during sudden reference change and load disturbances, fast dynamic response is very important for high-speed drives. A slow transient characteristic not only degrades the performance of the current control but also causes further degradation of the outer speed control loop. Moreover, since conventional PI controllers are usually implemented in a synchronous reference frame (SRF), the cross-coupling of the states caused by the reference frame transformation deteriorates the dynamic performance of the current controller. Thus, although synchronous reference frame PI regulator provides the desirable performance in steady state, it is still of interest to improve its dynamic response.

Previous studies developed several control schemes for the digital inverter in order to improve the current regulating and dynamic performance and a summary is presented in TABLE I. For the digital implementation of continuous time domain design methods (i.e., SPI, FC-SPI and CPI), time delay compensation methods are suggested for further improving the dynamic response at low r_{S2F} [13, 15], and the active damping methods are introduced to improve the system disturbance rejection performance [17-21]. While the discrete time domain design methods provide a more straightforward way of decoupling design [14, 22-24], the proposal does not provide for full control of the closed-loop plant, which limits the controller capability in cases of disturbance rejection. Moreover, the constrained freedom of the control scheme restricts the ability of searching the optimal control voltage to force the current to the reference value in the minimum time possible, which causes a limited bandwidth and slower the dynamic response of the controller.

Previous design methods of PI current regulator			
Methods	Pro/Cons		
SPI[17]	 Scalar-notation-based PI controller (SPI) [17], the most widely separated current control strategy due to simplicity to apply. The current tracking performance is degraded by the cross-coupling effects due to digital implementation. 		
FC-SPI	 Feedforward compensated SPI Controller (FC-SPI), mathematically same as State feedback decoupling control methods. Improved dynamic performance of SPI, However, performance degrades at low r_{S2F}. 		
IMC [18]	 Internal model control (IMC) provides adequate suppression of output disturbances. However, the widely published IMC tuning rules, do a poor job suppressing load disturbances when the process dynamics are significantly slower than the desired closed-loop dynamics. 		
CPI [19-21]	 Complex-vector-based PI controller (CPI), provide a better decoupling performance and command tracking response than FC-SPI [12]. Present a low capability of rejecting dc disturbance (with respect to stationary frame) in comparation of FC-SPI [19] 		
DPI [14, 22-24]	 Discrete time domain design method-based PI (DPI) controller, considering the effects of time delay and ZOH characteristic. Provide improved tracking performance at low <i>r</i>_{S2F} [14]. Disturbance rejection performance is limited by the control freedom. 		

TABLE I

The cross-coupling effects in the current control system has been analyzed, and an accurate discrete-time-domain plant model has been developed in the previous work [25]. In this paper, a new control structure with both the satisfactory steadystate characteristics and fast transient response is proposed. The novelty of this paper lies in the design of the new structure of two degree of freedom (2-DOF) decoupled discrete PI (DDPI) control for low S2F ratios. Considering the discretization and time delay in digital control system, the controller is designed to eliminate the cross-coupling effects in synchronous reference frame (SRF) and ensure a decoupled tracking performance independent of the machine speed and S2F ratio. The proposed controller also features the deadbeat characteristic which provides a wide-bandwidth and low harmonic distortion solution for high-speed applications. This paper is organized as follows. In section II, an accurate system model considering the time delay and rotational transformation is built, and the crosscoupling in the current control system is analyzed. Section III analyzes the digital implementation and side effect of the existing design for low S2F applications, and subsequently an improved 2-DOF discrete PI controller is proposed. The latter is then verified by simulations and experiments, which are illustrated in section IV. To conclude, the matching results are shown in section V, where the benefits and applicability of the proposed decoupled discrete current control are highlighted.

II. DYNAMIC MODEL OF PM MACHINE, CURRENT REGULATION AND BACK-EMF DECOUPLING

The nonlinear state equations governing the electrical and



Fig 1. Simplified complex-frame control and plant model

electromagnetic behavior of a PM machine using complex vector notation, considering the armature voltage space vector $u_{\text{Conv}}^{\text{dq}}$ as the input, and armature current $i_{\text{Conv}}^{\text{dq}}$ as output variable of the motor, can be described by:

$$i_{\text{Conv}}^{\text{dq}} + \tau_{\sigma} \frac{\mathrm{d}}{\mathrm{d}\tau} i_{\text{Conv}}^{\text{dq}} = \frac{1}{r_{\sigma}} u_{\text{Conv}}^{\text{dq}} - \underbrace{j\omega_{k} L i_{Conv}^{dq} - j\omega_{k} \psi_{f}}_{cross-coupling}$$
(1)

 $\psi_{\rm f}$ is space vector that represent the rotor flux linkage, τ_{σ} is the transient stator time constant, r_{σ} is the stator resistance, and ω_k is the electrical angular frequency. Ignoring the back-EMF influence and taking the electrical and electromagnetic behavior of a PM machine as well the sampling, calculation, and D/A transfer characteristics into account [14, 22], a simplified current transfer loop can be illustrated as shown in Fig. 1 with the complex-valued transfer function of system dynamic described in [25]:

$$F_{PL}^{dq}(s) = \frac{i\frac{dq}{Conv}(s)}{u_{ref}^{dq}(s)}$$
$$= \underbrace{\underbrace{\frac{1-e^{-s\tau_s - j\omega_k\tau_s}}{SOH}}_{ZOH}}_{ZOH} \underbrace{\frac{e^{-s\tau_d}e^{-j\omega_k\tau_d}}{s\&c~delay}}_{S\&c~delay} \underbrace{\frac{1}{c_{\sigma}} \frac{1}{1+s\tau_{\sigma} + j\omega_k\tau_{\sigma}}}_{RL~dynamic}$$
(2)

 τ_s is the sampling period, τ_d is the time delay due to the sampling and calculating process. By introducing the transformation law to (2), the accurate discrete plant model considering the time delay and the transformation from stationary reference frame to rotational reference frame can be obtained:

$$F_{PL}^{dq}(z) = \frac{i_{Conv}^{dq}(z)}{u_{ref}^{dq}(z)} = \mathcal{Z}\left\{ \mathcal{L}^{-1}\left\{ F_{PL}^{dq}(s) \right\} \Big|_{t=k\tau_s} \right\} = \frac{K_s \cdot z^{-2}}{(1-\rho_2 z^{-1})} (3)$$

It can be seen from (3), the plant model includes two poles in complex valued transfer function, the first pole $\rho_1 = 0$ is



Fig. 2 Pole map of accurate plant model with varied r_{S2F}

located at origin and the second pole $\rho_2 = \delta_1 \delta_2$ with $\delta_1 = e^{-\tau_s/\tau_\sigma}$, $\delta_2 = e^{-j\omega_k\tau_s}$, $K_s = \iota_1\iota_2$ is the system gain with $\iota_1 = (1 - e^{-\tau_s/\tau_\sigma})/r_\sigma$, $\iota_2 = e^{-j2\omega_k\tau_s}$, Z and \mathcal{L}^{-1} represents the z-transformation and the inverse Laplace-transformation, respectively. The pole map of the accurate plant model in discrete time domain is illustrated in Fig. 2. As can be seen, the first pole ρ_1 is fixed to the coordinate origin of z-domain, while the second pole ρ_2 varies accordingly with the ratio of sampling to fundamental frequency ($r_{S2F} = f_s/f_e = \omega_s/\omega_k = 2\pi/\omega_k\tau_s$). As this ratio reduces, the system pole ρ_2 steps into the left half plane and the system gain K_s also changes.

III. CURRENT CONTROLLER DESIGN

A. Conventional PI current control





(b) FC-SPI with active damping



The schematic of the widely separated synchronous reference PI control with feedforward compensation decoupling method (hereinafter referred to as FC-SPI), is shown in Fig. 3(a). To improve the rejection of disturbance $u_{dis}^{dq}(s)$, in [26] it was suggested to implement an active resistance R_a in the digital controller, as shown in Fig. 3(b). This technique is named as 'active damping method', which has the same effect as increasing the resistance of the controlled machine, if the time delay and zero-order-hold characteristic are neglected [12].

However, the tracking performance and disturbance rejection performance of these controllers degrade at low S2F ratios where the practical issues caused by digital implementation process, such as inverter and sampling delay, cannot be ignored. As shown in Fig 4, the poles of the closed-loop system vary with S2F ratios, which indicate the incomplete decoupling and speed-dependent control performance.

B. Proposed 2-DOF DDPI

Due to the aforementioned limitations of conventional PI and active damping method in low S2F ratios, in this section, based on complex vector design method, a novel 2-DOF DDPI and its decoupling tuning method are proposed. The proposed method shows a frequency-independent dynamic response and improved stability. The scheme of proposed 2-DOF DDPI is shown in Fig. 5. A transfer function F_f^{dq} and a feedforward loop with an active damping gain K_{f3} is introduced to achieve the decoupling between the axes and the repositioning of the poles of plant (3).

$$F_f^{dq}(z) = \frac{K_{f1}}{1 - K_{f2} \cdot z^{-1}} \tag{4}$$

Then the inner loop transfer function can be described as:

$$\frac{i_{Conv}^{dq}(z)}{u_{ref_1}^{dq}(z)} = \frac{K_{f_1}K_s z^{-2}}{1 - (K_{f_2} + \rho_1)z^{-1} + (K_{f_3}K_{f_1}K_s + K_{f_2}\rho_1)z^{-2}}$$
(5)



Fig. 4. Pole map of FC-SPI with active damping



Fig. 5. 2-DOF discrete PI current controller

By properly choosing the values of gains K_{f1} , K_{f2} , and K_{f3} , the resulting system is completely decoupled, since all the coefficients involved in (5) are real numbers with:

$$K_{f1} = k_{c1}k_{c2}$$
 (6)

$$K_{f2} = \rho_2 - \rho_1 \tag{7}$$

$$K_{f3} = -K_{f2}\rho_1 \tag{8}$$

 $\rho_2 \in \mathbb{R}, |\rho_2| < 1$ is the constant parameter of the controller, which is selected by the designer. With this choice, the closed loop of the inner plant system results:

$$F_{C_inner}^{dq}(z) = \frac{i_{Conv}^{dq}(z)}{u_{ref\ 1}^{dq}(z)} = \frac{z^{-2}}{1 - \rho_2 z^{-1}}$$
(9)

The poles are relocated to coordinate origin and ρ_2 along the real axis in the z plane as shown in Fig. 6. Comparing to (3) and Fig. 2, the poles of the revised inner loop do not change with different values of the mechanical angular velocity ω_k . This verifies that the internal loop is capable of decoupling axes *d*



Fig. 6. Pole map of revised inner loop.

and *q* and relocating the poles. The outer-loop of DDPI controller (10) is designed to regulate the current tracking performance, with the zero z_0 used to compensate the pole ρ_2 of $F_{C_inner}^{dq}(z)$, with controller gain K_c equal to the real-valued factor γ , $\gamma > 0$ is introduced to shape the command response of the current controller, where $\gamma \in \mathbb{R}$ is a constant.

$$F_{DDPI}^{dq}(z) = \frac{u_{ref_{-1}}^{dq}(z)}{i_{conv,ref}^{dq}(z) - i_{conv}^{dq}(z)} = K_c \frac{1 - z_0 z^{-1}}{1 - z^{-1}}$$
(10)

$$z_0 = \rho_2 \tag{11}$$

$$K_c = \gamma \tag{12}$$

The closed-loop transfer function of the system without disturbance results:

$$F_{C_ref}^{dq}(z) = \frac{i_{Conv}^{dq}(z)}{i_{Conv_ref}^{dq}(z)} = \frac{\gamma z^{-2}}{1 - z^{-1} + \gamma z^{-2}}$$
(13)

While considering the disturbance in the control system, the closed-loop transfer function of system is obtained:

$$i_{Conv}^{dq}(z) = \gamma \frac{z^{-2}}{1 - z^{-1} + \gamma z^{-2}} i_{Conv_{ref}}^{dq}(z) + \frac{z^{-2}}{\underbrace{\frac{1 - z^{-1} + \gamma z^{-2}}{F_{dis_{A}}^{dq}(z)}} \cdot \underbrace{\frac{\iota_{1}\iota_{2}(1 - z^{-1})(1 - K_{f2}z^{-1})}{z^{-1} \cdot (1 - \rho_{2}z^{-1})}}_{F_{dis_{B}}^{dq}(z)} \cdot u_{dis}^{dq}(z) \quad (14)$$

It can be noticed that the disturbance transfer function $F_{dis_B}^{dq}(z)$ in (14) has the pole located at ρ_2 which is a constant tuning parameter chosen by designers and does not change with the rotating frequency ω_k . Designers can use the real-valued factor γ to shape the command response of the current controller and use ρ_2 to improve the disturbance rejection dynamics and system stability, as illustrated in Fig. 7.

C. PDPI (High-bandwidth tuning methods of 2-DOF DDPI)

In this section, the predictive decoupled PI (PDPI) controller is proposed, featuring the same structure but with different tuning method compared to that presented earlier in section B. It presents the deadbeat characteristic, provides high bandwidth, and further improves the dynamic response. The conventional PI controllers have limited bandwidth because there is only one degree of freedom (1-DOF) and, as a result, this scheme cannot find the optimal control voltage which will force the current to



Fig. 7. Pole map of 2-DOF DDPI at varied S2F ratios.

the reference value in the minimum possible time. By changing the tuning method of 2-DOF DDPI, a wide bandwidth solution with deadbeat characteristic can be achieved with:

$$K_{f1} = k_{c1}k_{c2} \tag{15}$$

$$K_{f2} = \rho_3 + \rho_2 - \rho_1 \tag{16}$$

$$K_{f3} = \rho_2 \rho_3 - K_{f2} \rho_1 \tag{17}$$

The inner loop transfer function is then obtained as:

$$F_{C_inner}^{dq}(z) = \frac{i_{Conv}^{dq}(z)}{u_{ref_1}^{dq}(z)} = \frac{z^{-2}}{(1-\rho_2 z^{-1})(1-\rho_3 z^{-1})}$$
(18)

By employing $z_0 = \rho_2$, $K_c = 1$ to (10) and $\rho_3 = -1$ to (18), the closed-loop transfer function of the system is obtained:

$$F_{C_ref}^{dq}(z) = \frac{i_{Conv}^{dq}(z)}{i_{Conv_ref}^{dq}(z)} = \frac{K_c z^{-2}}{1 - \underbrace{(\rho_3 + 1)}_{\zeta_1} z^{-1} + \underbrace{(K_c + \rho_3)}_{\zeta_2} z^{-2}} = z^{-2}$$
(19)

It can be seen from (19) that the transfer function shows a deadbeat characteristic, which allows for a fast-tracking performance. Considering the disturbance in the control system, the closed-loop transfer function of system is obtained:

$$i_{Conv}^{dq}(z) = z^{-2} \cdot i_{Conv_{ref}}^{dq}(z) + \underbrace{z^{-2}}_{F_{dis_A}^{dq}(z)} \cdot \underbrace{\underbrace{z^{-1} \cdot (1-z^{-1})(1-K_{f2}z^{-1})}_{z^{-1} \cdot (1-\rho_{2}z^{-1})}}_{F_{dis_B}^{dq}(z)} \cdot u_{dis}^{dq}(z) \quad (20)$$



Fig. 8. Pole map of PDPI at varied S2F ratios.

Comparing the closed loop pole maps Fig.7 and Fig.8 with Fig.4, it can be seen, the proposed 2-DOF DDPI controllers present a S2F ratio-independent characteristic. Additionally, it provides an adjusted disturbance rejection ability without affecting controller tracking performance.

IV. ANALYSIS AND VERIFICATION

A. Simulation and experimental setup.

To verify the proposed 2-DOF DDPI current regulator, simulations are performed within MATLAB/Simulink environment, where a continuous time-domain PMSM model (parameters obtained from experimental test results), an average model of two-level inverter (with one-step delay of output voltage) and a discrete controller are used. The inductance values of test machine are shown in Fig 9, and the other parameters are shown in Table II.

The validation of the proposed control strategy is also performed on a 120 krpm high speed dynamometer with the SiC inverter-fed high speed SPM prototype, as shown in Fig. 10. The SiC based two-level inverter is designed for high-speed application, where the maximum switching frequency is equal to 80kHz, and the dead time is equal to 500 ns. During the tests, the switching frequency is set as 10 kHz to emphasize control performances with reduced samples per fundamental period. The controllers for the experimental tests are implemented on the designed DSP + FPGA controller board. During the experimental test, the dyno works in speed mode and the test motor works in load mode to validate the proposed current



Fig 9. Machine inductance corresponding to different current.

TABLE II Test Machine Parameters			
Parameter	Value	Units	
Rated power	5	kW	
Rated speed	80k	rpm	
Phase resistance	0.67	Ω	
Pole pairs	2		



Fig 10. Experimental test platform.

control strategy. The same PWM frequency and control frequency are used in all simulations and experiments presented in this paper.

B. Performance Analysis of Proposed DDPI

1). Simulation results: Firstly, the dynamic response of conventional PI controller (FC-SPI) and proposed 2-DOF DDPI controllers are compared in simulation.



Fig 11. Transient response at 6000rpm, ratio = 50.



Fig. 12. Transient response comparison between FC-SPI (left) and proposed PDPI (right) with varied S2F ratios.



Fig. 13. Dynamic responses comparison at 9krpm, ratio = 33.

Fig. 11 presents the transient response of different regulators at high sampling to fundamental frequency ratio, i.e., $r_{S2F} = 50$. From top to bottom, Fig. 11(a) presents the performance of conventional decoupling method FC-SPI, which shows the longest settling time ($t_s = 50T_s$) and around 10% overshoot of d-axis current (which indicate the incomplete decoupling). The performance of the proposed 2-DOF DDPI shows a fasttracking performance ($t_s = 10T_s$) and zero overshoot of i_d (Fig. 11(b)). Additionally, with the proposed predictive tuning method, 2-DOF DDPI presents decoupled control performance with the deadbeat characteristic, which tracks the reference value in two steps ($t_s = 2T_s$), as shown in Fig.11(c). The decoupling performance of proposed 2-DOF DDPI with decreased S2F ratios is tested, as shown in Fig. 12. It can be seen from (a) to (c), as S2F ratio decreases, conventional decoupling method FC-SPI shows a degrading tracking performance with increased setting time (from $50T_s$ to $150T_s$) and overshoot of d-axis current (from 10% to 22%). On the other hand, the proposed 2-DOF DDPI shows S2F ratioindependent dynamic response and tracks the reference value in $2T_s$, as shown in Fig 12 (d) to (f). It is also worth mentioning that at extreme low S2F ratio, i.e., below 10, control system with FC-SPI becomes unstable, while the proposed 2-DOF DDPI can keep the same decoupled deadbeat control performance as shown in Fig 12.

2). Improved dynamic performance and decoupled control at different frequency/ratios: The decoupled control performance and the high bandwidth characteristic of proposed 2-DOF DDPI have been verified in the experimental test. Firstly, the tracking performances of FC-SPI and the proposed controller have been compared at relatively high S2F ratio ($r_{S2F}=33$), as shown in Fig. 13. The settling time (t_s) are marked, and the transient behavior of each method is highlighted by the red dash circle in the figure. It can be seen from Fig. 13(a), during the sudden load change, the conventional PI presents a slow tracking performance ($t_s >$ $50T_s$). While the proposed 2-DOF DDPI presents high bandwidth characteristic which tracks the reference value in two steps ($t_s = 2T_s$), shown in Fig. 13(b). The decoupled control performance of proposed 2-DOF DDPI controller with varied S2F ratios has been verified both in d-q domain and a-b-c domain, as shown in Fig. 14 and Fig. 15-17. In Fig. 14(a), the

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(b) 12000 rpm, ratio = 25 and $f_e = 400$ Hz

Fig. 14. Dynamic responses of proposed PDPI with sudden q-axis current reference change.



Fig. 15. Transient responses of PDPI at 6krpm, ratio = 50.



Fig. 16. Transient responses of PDPI at 9krpm, ratio = 33.



Fig. 17. Transient responses of PDPI at 12krpm, ratio = 25.

dynamic response of proposed control with a q-axis current reference step change (10A to 5A) at 9000 rpm is presented. It can be seen there is no cross-coupling between d and q axes currents, and the controller present a fast-tracking performance



(a) FC-SPI (b) 2-DOF DDPI (PDPI).

Fig. 18. Steady-state responses at 9krpm, ratio = 33.



Fig. 19. THD% of phase current at 9krpm, ratio = 33

without overshoot, this matches with the simulation results shown in Fig. 12(e). Same decoupled control performance at low S2F ratio ($r_{S2F} = 25$) is illustrated in Fig.14 (b), which experimentally verified the simulation results shown in Fig. 12(f).

The S2F ratio-independent decoupled tracking performance with minimum settling time of the proposed 2-DOF DDPI has been verified at stationary reference domain. The three-phase currents tracking performance with different S2F ratios, during sudden change of load, are shown in Fig. 15 to Fig. 17. The S2F ratio is reduced as the speed increase, but the current control settling time keep the same. The proposed controller presents a high bandwidth characteristic which tracks the reference value in two steps ($t_s = 2T_s$) regardless of the S2F ratio. The same conclusion can be also drawn from Fig. 12 (d) to (f).

3). Improved steady-state performance and THD%: The steady-state performance is compared and shown in Fig. 18. This experimental benchmark has confirmed the effectiveness of the proposed 2-DOF DDPI controller over the PI controller in terms of an overall low THD. The harmonic mitigation capability of the proposed DDPI controller and the PI controller is compared, as shown in Fig.19 and 20.

As shown in Fig. 19, the PI controller can maintain lower harmonics in a wide range of frequencies (e.g., 4kHz). The low frequency harmonics close to the fundamental frequency are reduced (Fig. 20) and the fundamental component increases by



Fig. 20. Low-order harmonics component amplitudes comparison between FC-SPI and 2-DOF DDPI (PDPI).

2.91%, while the total THD% is reduced by 30% with respect to the conventional PI controller (Fig. 19). These results are confirming the benefits of the method introduced and its applicability to high-speed drives where the sampling to fundamental frequency ratios is low.

4). Performance under parameter mismatching: The effect of the parameter variation on the controllers has been analysis using simulation, for two different tunings methods, as shown in Fig. 21 and 22. Here L is the machine real inductance value and L' is the machine parameter used in the control. It can be noticed that both tuning methods keep the same tracking performance with fixed settling time, the decoupled control performance is not affected by the parameter mismatch.

V. CONCLUSION

In this paper, a novel 2-DOF decoupled discrete PI method is proposed, which aims to eliminate the cross-coupling between d- and q- axes, and improves the dynamic response of conventional PI controllers. The proposed 2-DOF scheme provides an extra control freedom of the disturbance rejection, which effectively improves the system stability without effecting the tracking performance.

Furthermore, two decoupled tuning methods for the proposed 2-DOF DDPI have been designed. One is for decoupled control with improved stability, while the other is for decoupled control with deadbeat characteristics.

The proposed method is performed experimentally on a 5 kW high-speed SPMSM, with the matching simulation and practical test results validating the theoretical analysis. Without current and flux observers, the proposed discrete-time domain current controller allows to guarantee decoupled tracking performance of up to 15% of the switching frequency with respect to the state-of-art discrete current control of 8.3% [14]. The total THD% is reduced by a significant 30% compared to the conventional controller.

APPENDIX

The tuning method of conventional PI controller used in the paper has been explained in detail in the previous work [25]. To realize the maximum bandwidth, $k_{max} \approx \frac{9.3}{100} \cdot 2\pi f_s$ is selected as the controller gain in this paper. The discretization process of the conventional controller is illustrated as follow.

Synchronous PI (SPI) controller in s-domain:

$$F_{SPI}^{dq}(s) = k_c \frac{(\tau_i s + 1)}{\tau_i s} = \frac{k r_\sigma (1 + s \tau_\sigma)}{s}$$
(21)



Fig. 21. 2-DOF DDPI (method I) at 12000 rpm, $r_{S2F} = 25$



Fig. 22. 2-DOF DDPI (PDPI) at 12000 rpm, *r*_{S2F} = 25

By combining with the current feedback with gain $j\omega_k L_\sigma$ added at the output of $F_{SPI}(s)$, the imaginary part of the plant pole in $F_p^{dq}(s)$ can be canceled, i.e., it replaces the plant transfer function by $F_p^{dq}(s - j\omega_k) = 1/r_\sigma(1 + s\tau_\sigma)$, which is used in Feedforward Compensated SPI (FC-SPI). The resulting pole of $F_p^{dq}(s - j\omega_k)$ is compensated by the zero of controller (21), and the open loop transfer function of the current control system can be presented as:

$$F_O^{dq}(s) = F_{SPI}^{dq}(s) \cdot F_P^{dq}(s - j\omega_k) = \frac{k}{s}$$
(22)

Considering the time delay and the characteristic of D/A, and using the Tustin transformation, the PI current controller in the discrete time domain can be described as (23),

$$F_{SPI}^{dq}(z) = F_{SPI}^{dq}(s)\Big|_{s=\frac{2}{\tau_s}\frac{z-1}{z+1}} = A \cdot \frac{1}{1-z^{-1}} + B \cdot \frac{z^{-1}}{1-z^{-1}}$$
(23)

Where $A = k_c \left(1 + \frac{1}{2}\tau_s\tau_i^{-1}\right)$, $B = k_c \left(\frac{1}{2}\tau_s\tau_i^{-1} - 1\right)$, and the close-loop transfer function being (24):

$$F_{C}^{dq}(z) = \frac{u_{ref}^{dq}(z)}{i_{Conv_{ref}}^{dq}(z)} = \frac{AK_{S} \cdot z^{-2} + BK_{S} z^{-3}}{1 - (1 + \rho_{1})z^{-1} + (AK_{S} + \rho_{1})z^{-2} + BK_{S} z^{-3}}$$
(24)

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